

**Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

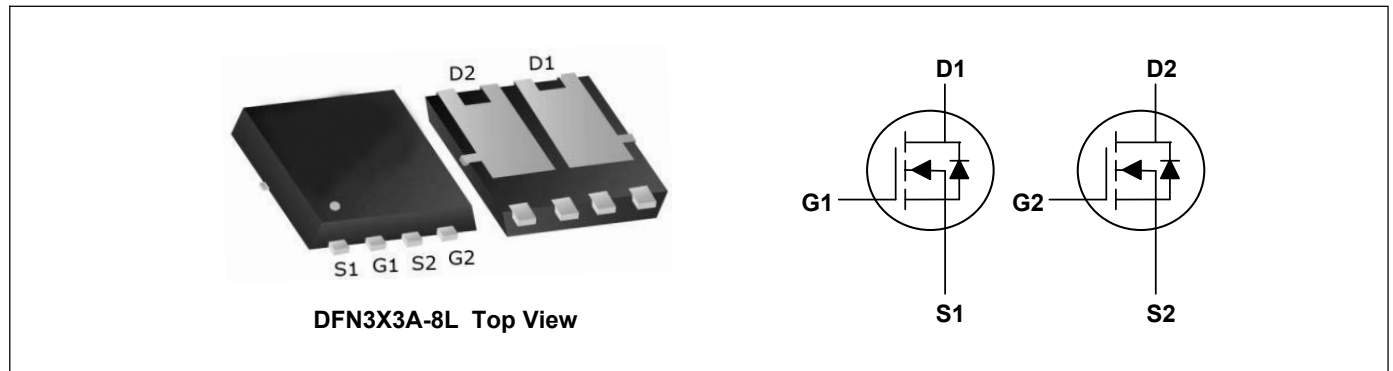
**Product Summary**



$V_{DS}$	30	V
$I_D$	30	A
$R_{DS(ON)}$ (at $V_{GS}=10V$ )	13	m $\Omega$
$R_{DS(ON)}$ (at $V_{GS}=10V$ )	18	m $\Omega$

**Applications**

- High Frequency Point-of-Load, Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch



**Absolute Maximum Ratings ( $T_C=25^\circ C$ , unless otherwise noted)**

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1</sup>	$I_D@T_C=25^\circ C$	30	A
Continuous Drain Current <sup>1</sup>	$I_D@T_C=100^\circ C$	19	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	120	A
Single Pulse Avalanche Energy <sup>3</sup>	$E_{AS}$	16	mJ
Total Power Dissipation <sup>4</sup>	$P_D@T_C=25^\circ C$	20	W
Storage Temperature Range	$T_{STG}$	-55 to 150	$^\circ C$
Operating Junction Temperature Range	$T_J$	-55 to 150	$^\circ C$

**Thermal Characteristics**

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	---	45	$^\circ C/W$
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	---	6.2	$^\circ C/W$

**Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30	---	---	V
Static Drain-Source On-Resistance <sup>2</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	10	13	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	---	13	18	mΩ
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.2	1.6	2.5	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =30V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
Gate Resistance	R <sub>g</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	---	4.7	---	Ω
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	20	---	nC
Gate-Source Charge	Q <sub>gs</sub>		---	4.2	---	
Gate-Drain Charge	Q <sub>gd</sub>		---	5.5	---	
Turn-On Delay Time	T <sub>d(on)</sub>	V <sub>DD</sub> =15V, V <sub>GS</sub> =10V, R <sub>G</sub> =3Ω, I <sub>D</sub> =20A	---	6	---	ns
Rise Time	T <sub>r</sub>		---	52	---	
Turn-Off Delay Time	T <sub>d(off)</sub>		---	23	---	
Fall Time	T <sub>f</sub>		---	12	---	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz	---	830	---	pF
Output Capacitance	C <sub>oss</sub>		---	110	---	
Reverse Transfer Capacitance	C <sub>rss</sub>		---	102	---	

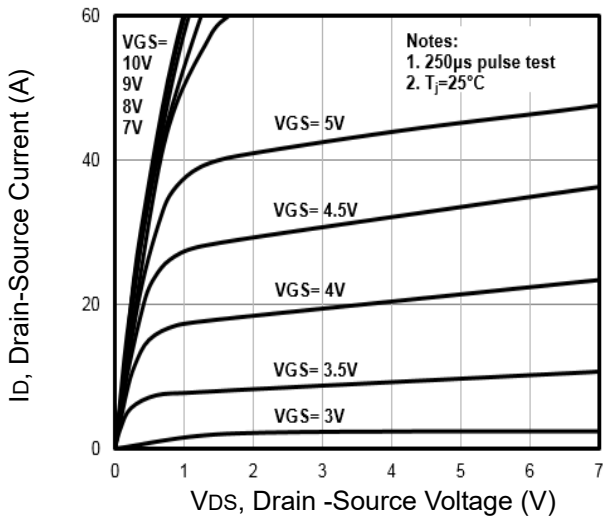
**Drain-Source Diode Characteristics**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Diode Forward Voltage <sup>2</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A, T <sub>J</sub> =25°C	---	0.8	1.2	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =20A, V <sub>GS</sub> =0V di/dt=100A/μs, T <sub>J</sub> =25°C	---	7	---	nS
Reverse Recovery Charge	Q <sub>rr</sub>		---	2	---	nC

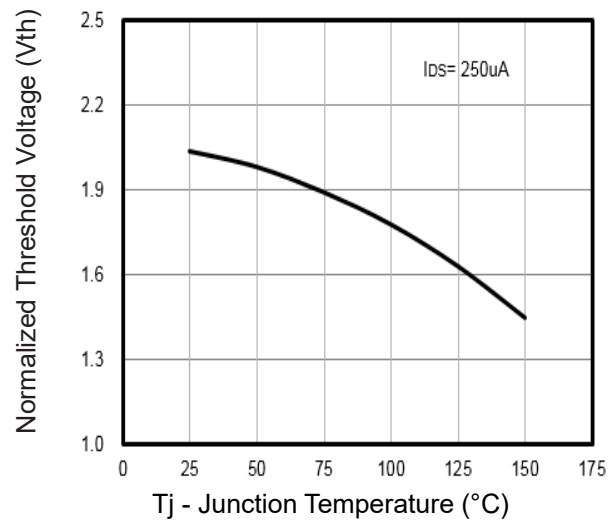
**Note:**

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=15V,V<sub>GS</sub>=10V,L=0.5mH
- 4.The power dissipation is limited by 150°C junction temperature

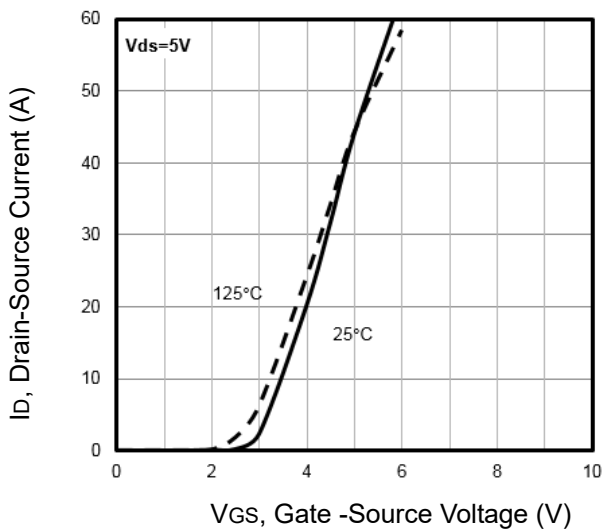
**Typical Characteristics**



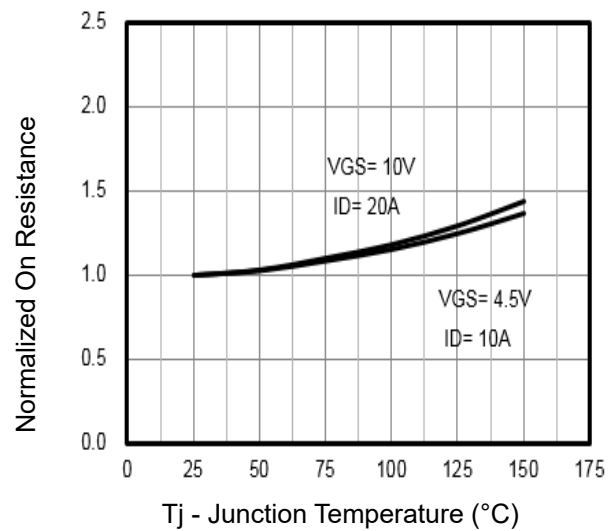
**Fig1.** Typical Output Characteristics



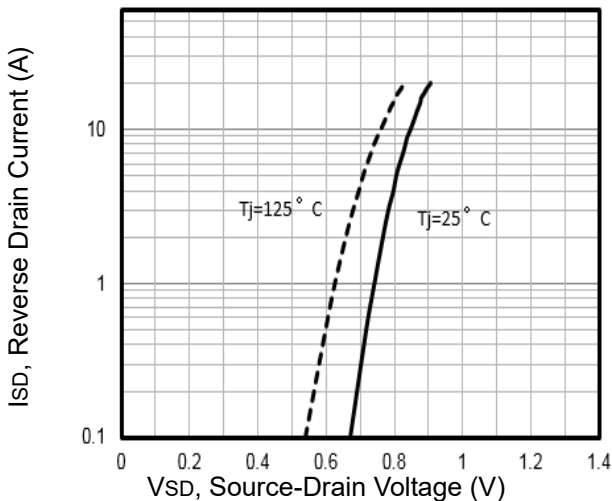
**Fig2.** Normalized Threshold Voltage Vs. Temperature



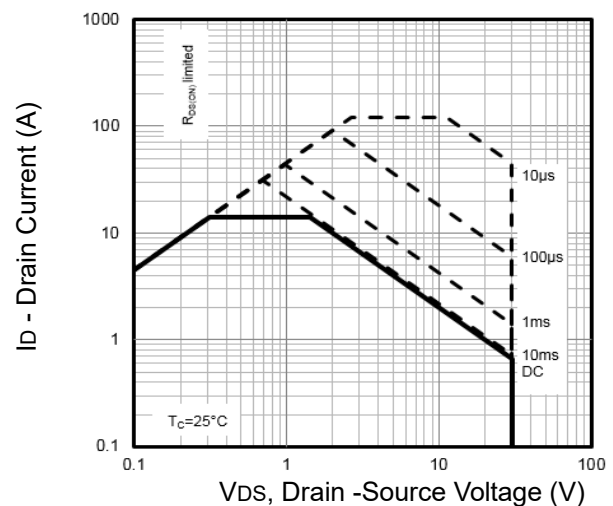
**Fig3.** Typical Transfer Characteristics



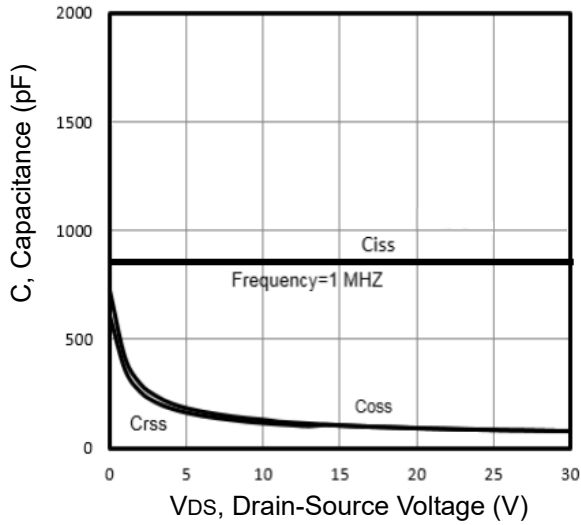
**Fig4.** Normalized On-Resistance Vs. Temperature



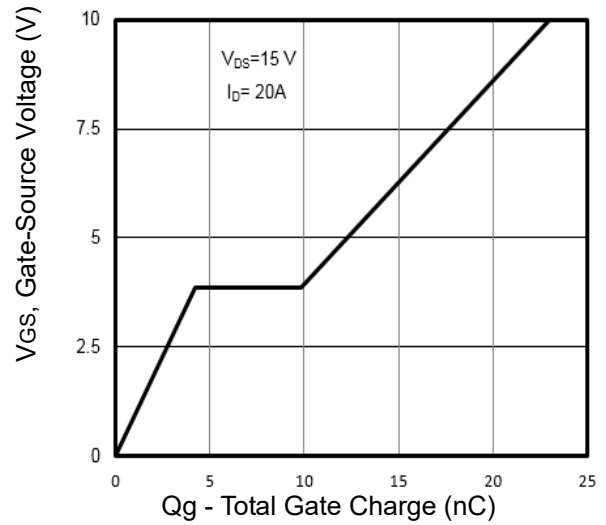
**Fig5.** Typical Source-Drain Diode Forward Voltage



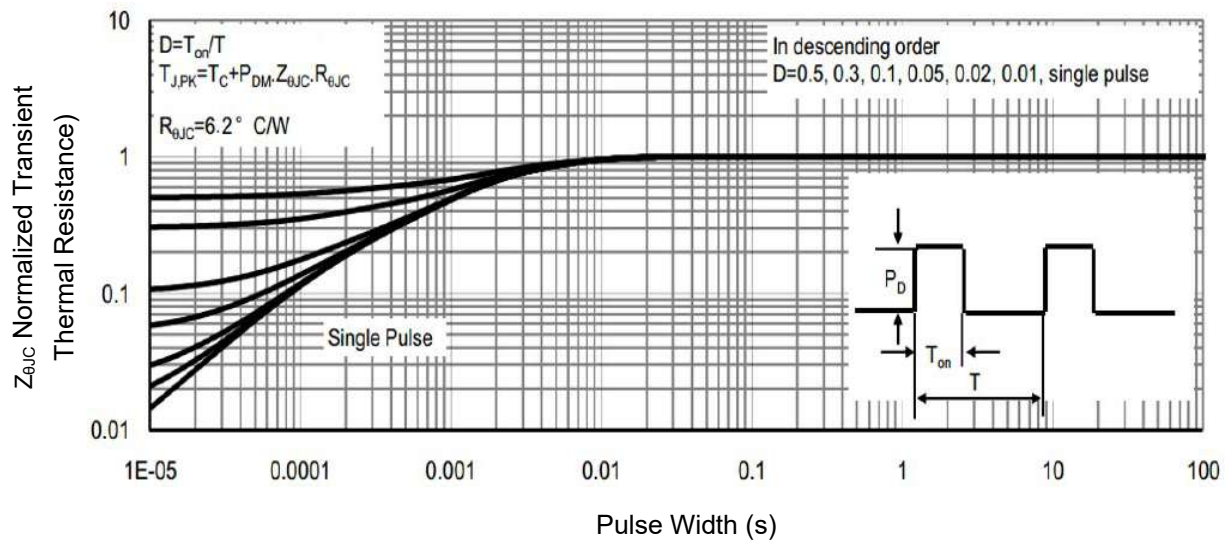
**Fig6.** Maximum Safe Operating Area



**Fig7.** Typical Capacitance Vs. Drain-Source Voltage

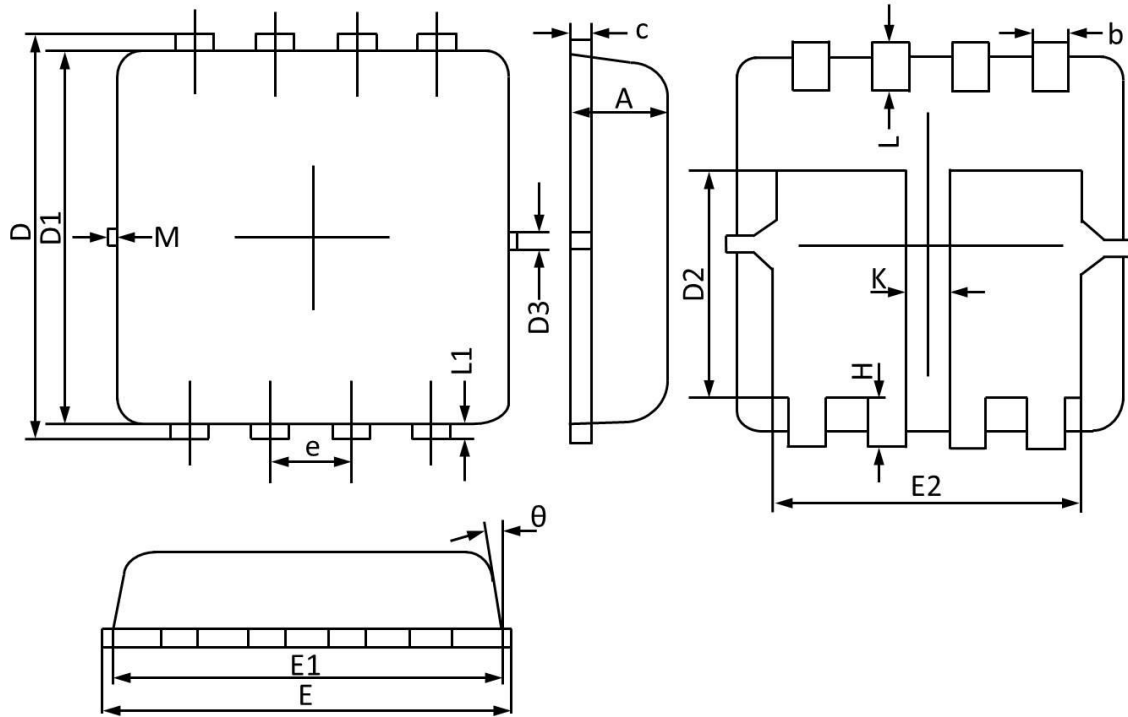


**Fig8.** Typical Gate Charge Vs. Gate-Source



**Fig9.** Normalized Maximum Transient Thermal Impedance

**DFN3X3A-8L Package Outline Dimensions**



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
<b>A</b>	0.70	0.75	0.85	<b>E2</b>	2.35	2.50	2.60
<b>b</b>	0.25	0.30	0.35	<b>e</b>	0.65 BSC		
<b>c</b>	0.10	0.17	0.25	<b>H</b>	0.30	0.40	0.50
<b>D</b>	3.10	3.30	3.45	<b>L</b>	0.30	0.40	0.50
<b>D1</b>	2.90	3.05	3.20	<b>L1</b>	0.13 REF		
<b>D2</b>	1.45	1.70	1.95	<b>K</b>	0.30 REF		
<b>D3</b>	0.13 REF			<b>theta</b>	0°		12°
<b>E</b>	3.05	3.25	3.40	<b>M</b>	0.15 REF		
<b>E1</b>	2.90	3.10	3.25				