

Features

- Low drain-source on-resistance: $R_{DS(ON)}=0.053\Omega(\text{typ})$
- Very Low FOM ($R_{DS(on)} \times Q_g$)
- Extremely low switching loss
- 100% avalanche tested
- RoHS compliant

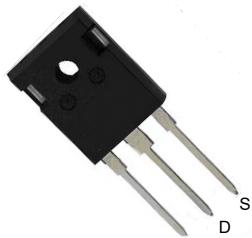
Key Performance Parameters



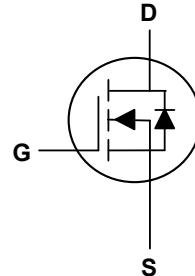
Parameter	Value	Unit
$V_{DS} @ T_{j,\max}$	600	V
$R_{DS(ON),\max}$	65	mΩ
I_D	50	A
$Q_{g,\text{typ}}$	75	nC
I_{DM}	129	A

Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



TO-247 Top View



Absolute Maximum Ratings($T_c=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	600	V
Gate-Source Voltage	V_{GS}	± 30	V
Continuous Drain Current ¹	I_D	50	A
Pulsed Drain Current ²	I_{DM}	129	A
Single Pulse Avalanche Energy ⁴	E_{AS}	281	mJ
Avalanche Current	I_{AS}	6.6	A
Repetitive Avalanche Energy	E_{AR}	3.3	mJ
MOSFET dv/dt ruggedness, $V_{DS} = 0\dots 400\text{V}$	dv/dt	50	V/ns
Reverse diode dv/dt ³ $V_{DS}=0\dots 400\text{V}$, $I_{SD} \leq I_D$		15	
Total Power Dissipation ($T_c=25^\circ\text{C}$)	P_D	329	W
Storage Temperature Range	T_{STG}	-55 to 150	°C
Operating Junction Temperature Range	T_J	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-Ambient (Max)	$R_{\theta JA}$	40	°C/W
Thermal Resistance Junction-Case (Max)	$R_{\theta JC}$	0.38	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	600	---	---	V
Static Drain-Source On-Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V}$, $I_D=25\text{A}$	---	53	65	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	2.5	---	4.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=600\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=480\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=150^\circ\text{C}$	---	2	---	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 30\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
Gate Resistance	R_g	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	1.3	---	Ω
Total Gate Charge	Q_g	$V_{\text{DS}}=400\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=25\text{A}$	---	75	---	nC
Gate-Source Charge	Q_{gs}		---	17	---	
Gate-Drain Charge	Q_{gd}		---	34	---	
Turn-On Delay Time	$T_{\text{d(on)}}$	$V_{\text{DS}}=400\text{V}$, $R_G=10\Omega$, $I_D=25\text{A}$, $V_{\text{GS}}=10\text{V}$	---	17	---	ns
Rise Time	T_r		---	8	---	
Turn-Off Delay Time	$T_{\text{d(off)}}$		---	70	---	
Fall Time	T_f		---	9	---	
Input Capacitance	C_{iss}	$V_{\text{DS}}=50\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	3200	---	pF
Output Capacitance	C_{oss}		---	80	---	
Reverse Transfer Capacitance	C_{rss}		---	2	---	

Drain-Source Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current	I_s	$T_c=25^\circ\text{C}$	---	---	50	A
Pulsed Source Current	I_{SM}		---	---	129	A
Diode Forward Voltage	V_{SD}	$V_G=0\text{V}$, $I_s=25\text{A}$, $T_J=25^\circ\text{C}$	---	0.7	1.2	V
Reverse Recovery Time	t_{rr}	$V_R=400\text{V}$, $I_F=25\text{A}$, $dI_F/dt=100\text{A}/\mu\text{s}$	---	410	---	ns
Reverse Recovery Charge	Q_{rr}		---	7	---	uC

Note:

1. Limited by $T_{j,\text{max}}$. Maximum Duty Cycle D = 0.50
2. Pulse width t_p limited by $T_{j,\text{max}}$
3. Identical low side and high side switch with identical R_G
4. $V_{DD}=50\text{V}$, $R_G=25\Omega$, $I_{AS}=6.6\text{A}$, Starting $T_J=25^\circ\text{C}$

Typical Characteristics

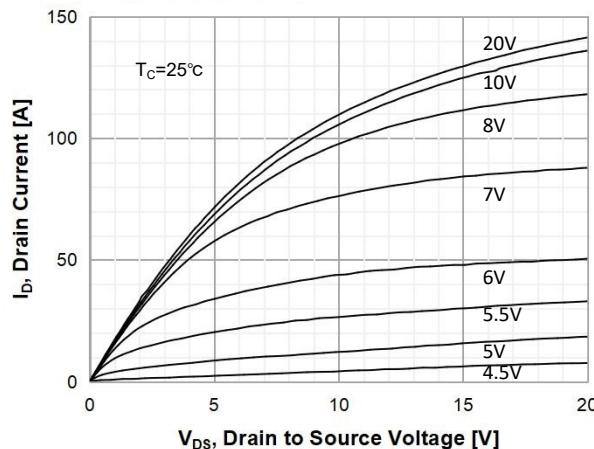


Figure 1. On-Region Characteristics

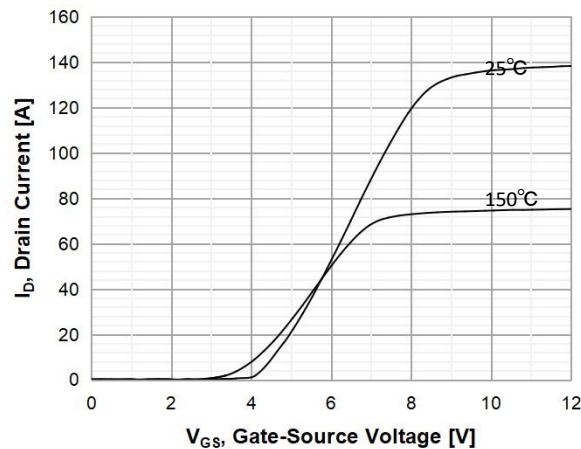


Figure 2. Transfer Characteristics

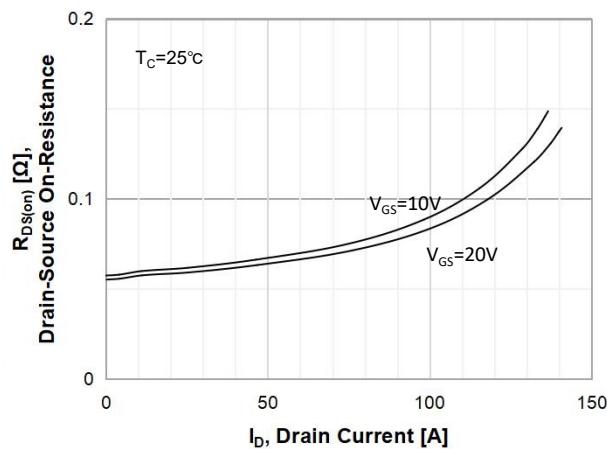


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

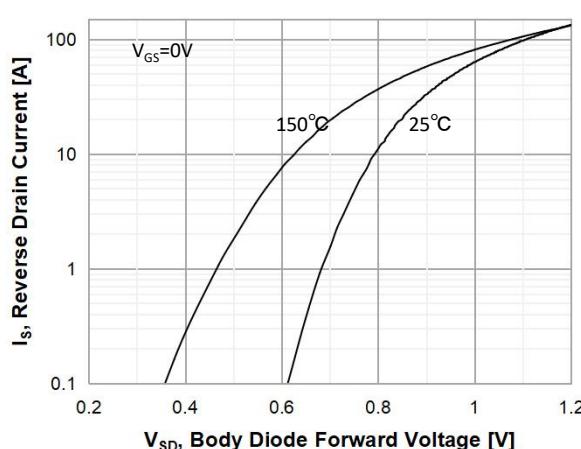


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

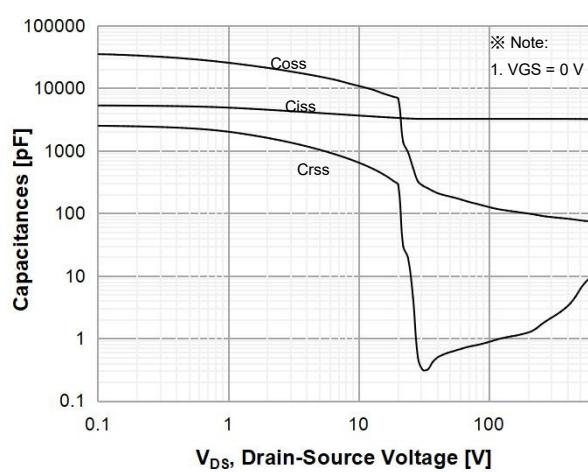


Figure 5. Capacitance Characteristics

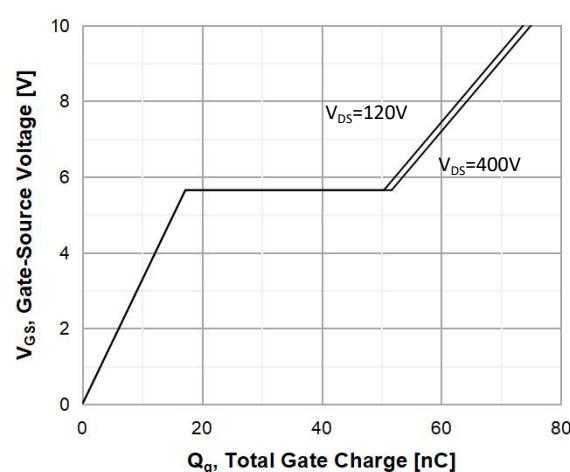
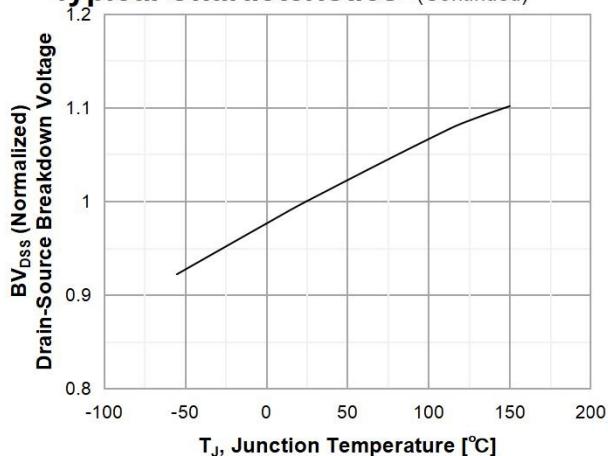
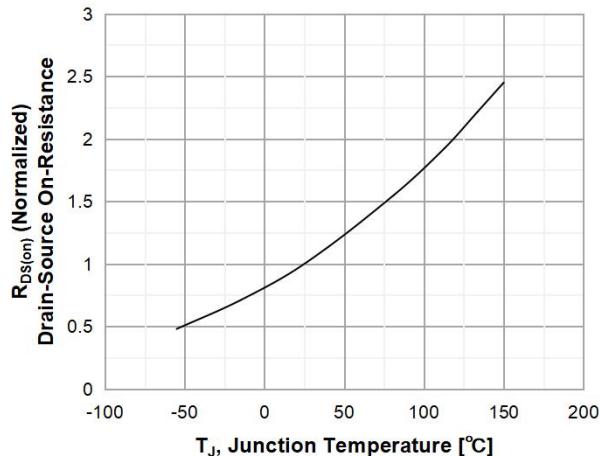
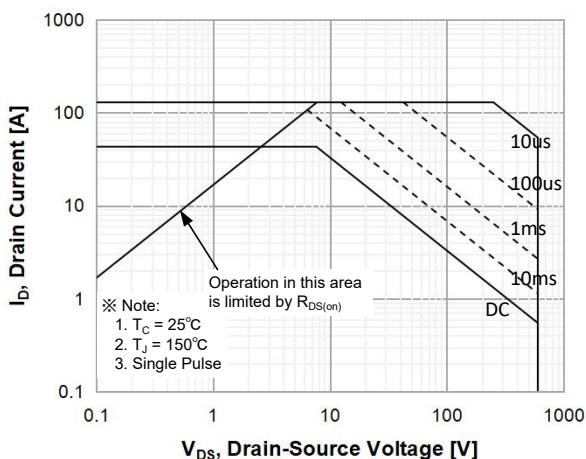
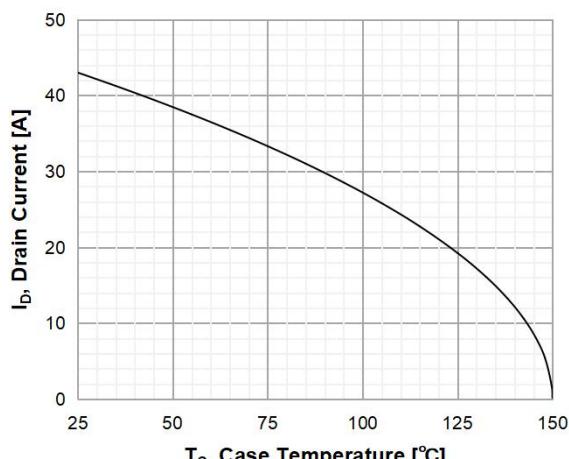
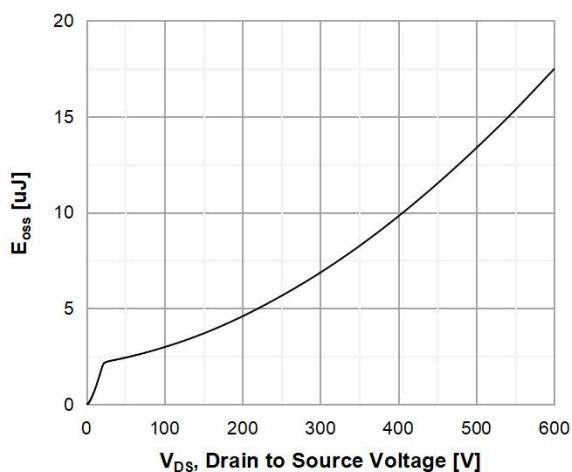
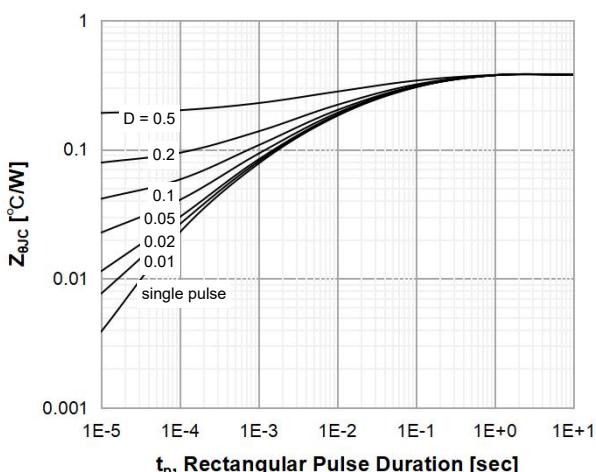
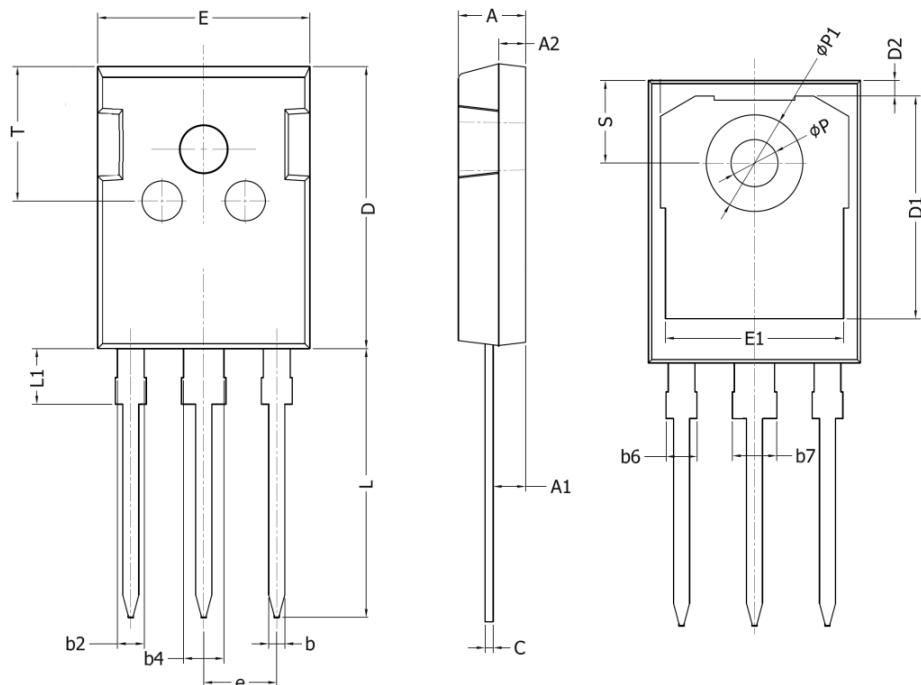


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs Temperature

Figure 8. On-Resistance Variation vs Temperature

Figure 9. Maximum Safe Operating Area

Figure 10. Maximum Drain Current vs. Case Temperature

Figure 11. E_{oss} vs. Drain to Source Voltage

Figure 12. Transient Thermal Response Curve

TO-247 Package Outline Dimensions



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	4.90	5.20
A1	2.31	2.51
A2	1.9	2.1
b	1.16	1.26
b2	1.96	2.06
b4	2.96	3.06
b6	-	2.25
b7	-	3.25
C	0.59	0.66
D	20.90	21.20
D1	16.25	16.85
D2	1.05	1.35
E	15.75	16.10
E1	13.00	13.60
e	5.436 BSC	
L	19.80	20.20
L1	-	4.30
P	3.40	3.60
P1	7.00	7.40
S	6.05	6.25
T	9.80	10.20