

**Features**

- Advanced Shield Gate Trench technology
- Super Low Gate Charge
- High-Speed Switching
- 100% EAS Guaranteed
- Green Device Available

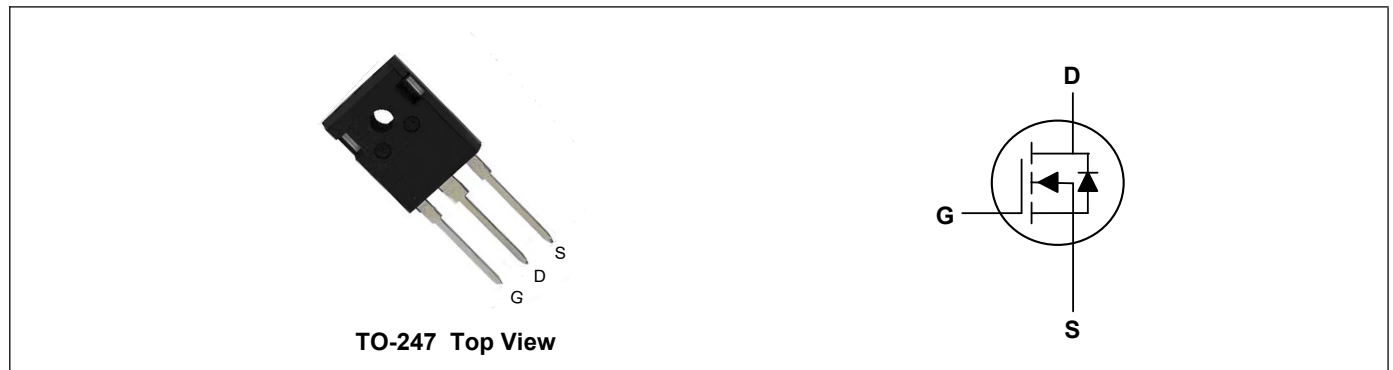
**Applications**

- High Frequency Point-of-Load, Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch

**Product Summary**



$V_{DS}$	200	V
$I_D$	130	A
$R_{DS(ON)}$ (at $V_{GS}=10V$ )	10.5	m $\Omega$



**Absolute Maximum Ratings( $T_C=25^\circ C$ , unless otherwise noted)**

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	200	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1</sup>	$I_D@T_C=25^\circ C$	130	A
Continuous Drain Current <sup>1</sup>	$I_D@T_C=100^\circ C$	93	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	370	A
Single Pulse Avalanche Energy <sup>3</sup>	$E_{AS}$	720	mJ
Total Power Dissipation	$P_D$	429	W
Storage Temperature Range	$T_{STG}$	-55 to 175	$^\circ C$
Operating Junction Temperature Range	$T_J$	-55 to 175	$^\circ C$

**Thermal Characteristics**

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	---	60	$^\circ C/W$
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	---	0.35	$^\circ C/W$

**Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	200	---	---	V
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	9	10.5	mΩ
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	2.0	---	4.0	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =160V, V <sub>GS</sub> =0V	---	---	1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =20A	---	70	---	S
Gate Resistance	R <sub>g</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	---	4	---	Ω
Total Gate Charge	Q <sub>g</sub>	V <sub>DD</sub> =100V, V <sub>GS</sub> =10V, I <sub>D</sub> =20A	---	55	---	nC
Gate-Source Charge	Q <sub>gs</sub>		---	17	---	
Gate-Drain Charge	Q <sub>gd</sub>		---	5	---	
Turn-On Delay Time	T <sub>d(on)</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =10V, R <sub>G</sub> =10Ω, I <sub>D</sub> =20A	---	16	---	ns
Rise Time	T <sub>r</sub>		---	20	---	
Turn-Off Delay Time	T <sub>d(off)</sub>		---	38	---	
Fall Time	T <sub>f</sub>		---	10	---	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =100V, V <sub>GS</sub> =0V, f=1MHz	---	4980	---	pF
Output Capacitance	C <sub>oss</sub>		---	420	---	
Reverse Transfer Capacitance	C <sub>rss</sub>		---	8	---	

**Drain-Source Diode Characteristics**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Diode Forward Voltage <sup>2</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A, T <sub>J</sub> =25°C	---	---	1.3	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =20A di/dt=100A/μs, T <sub>J</sub> =25°C	---	140	---	nS
Reverse Recovery Charge	Q <sub>rr</sub>		---	630	---	nC

**Note:**

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The EAS data shows Max. rating . The test condition is V<sub>DD</sub>=100V,L=0.4mH

**Typical Characteristics**

Fig 1. Typical Output Characteristics

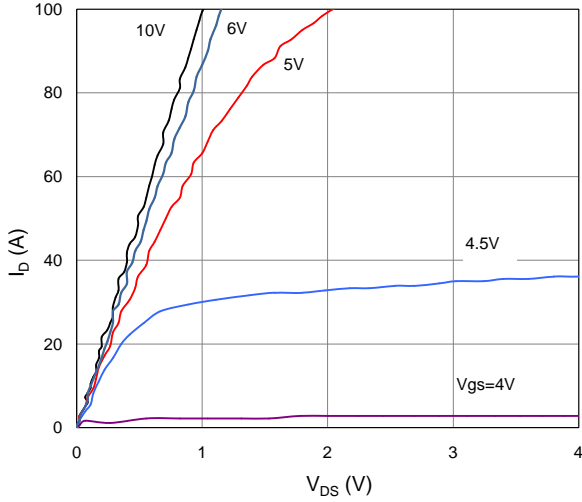


Figure 2. On-Resistance vs. Gate-Source Voltage

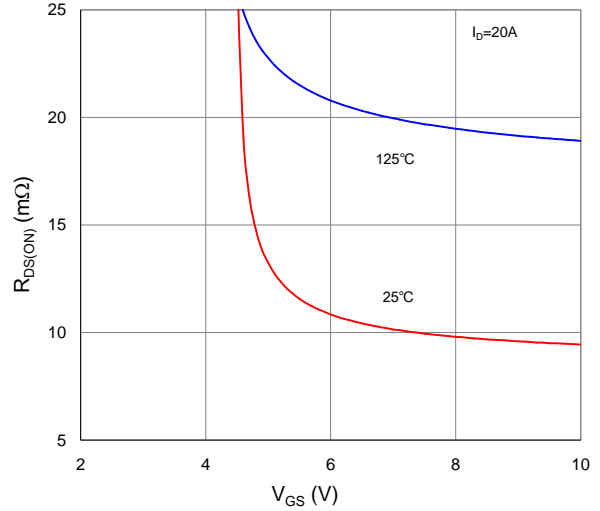


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

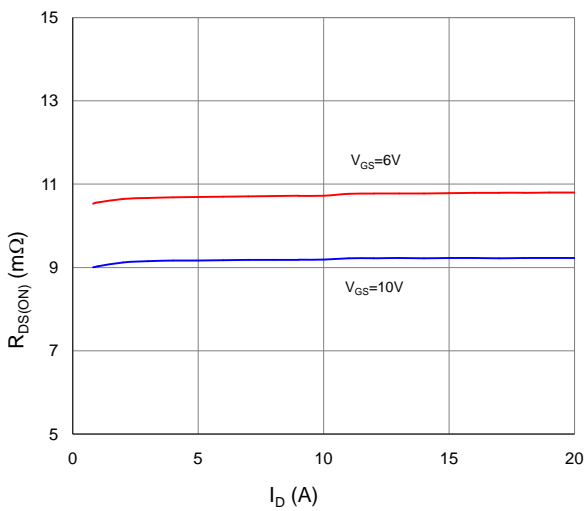


Figure 4. Normalized On-Resistance vs. Junction Temperature

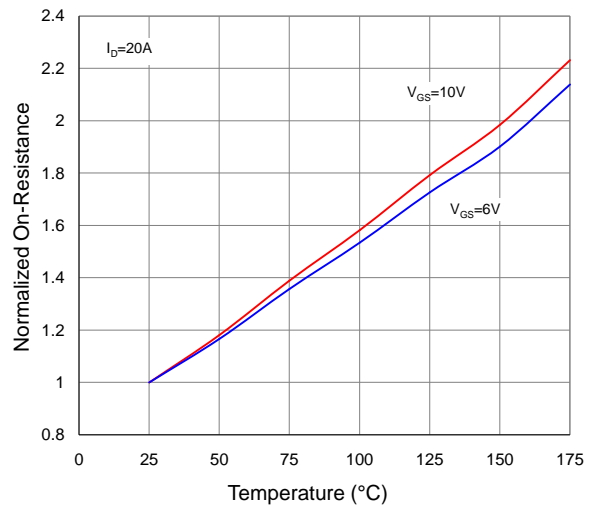


Figure 5. Typical Transfer Characteristics

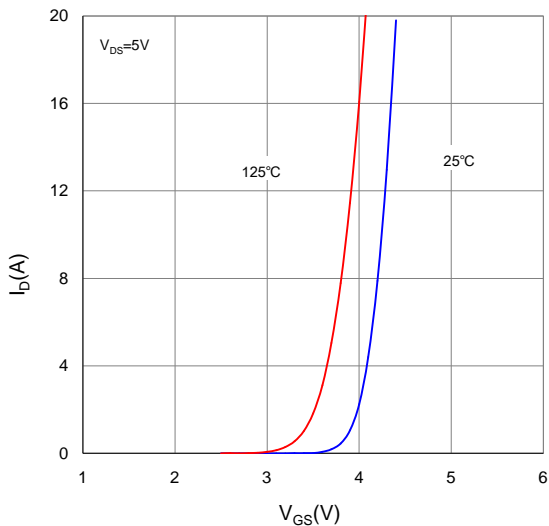


Figure 6. Typical Source-Drain Diode Forward Voltage

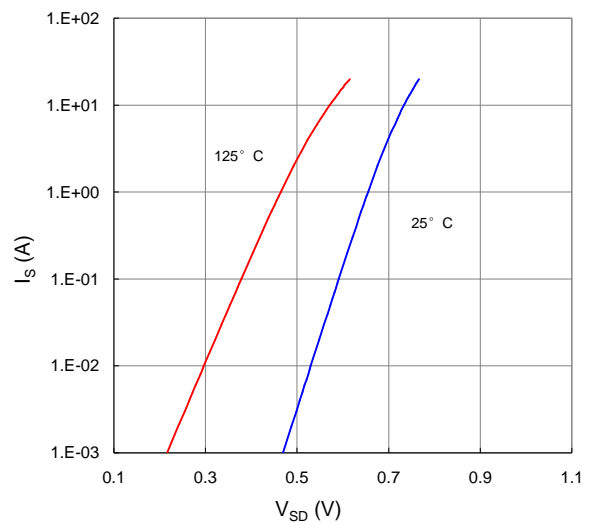


Figure 7. Typical Gate-Charge vs. Gate-to-Source Voltage

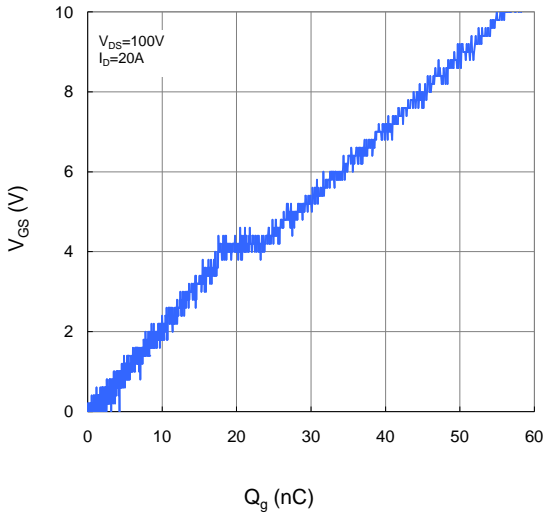


Figure 8. Typical Capacitance vs. Drain-to-Source Voltage

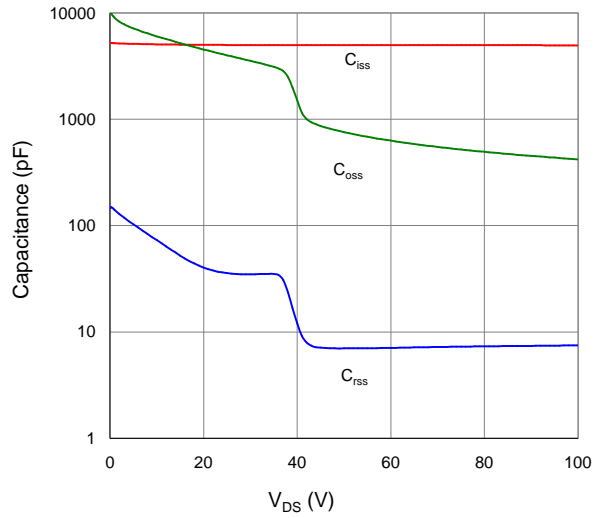


Figure 9. Maximum Safe Operating Area

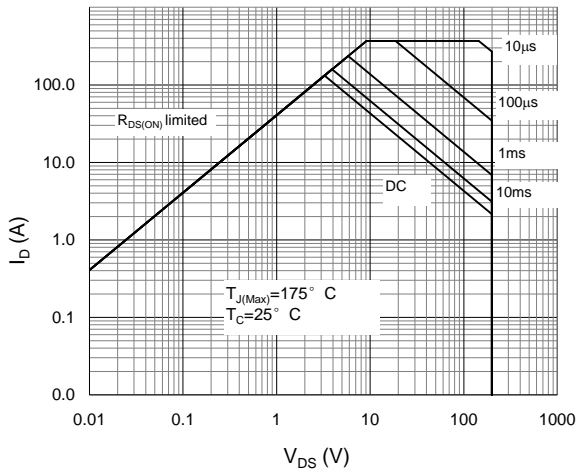


Figure 10. Maximum Drain Current vs. Case Temperature

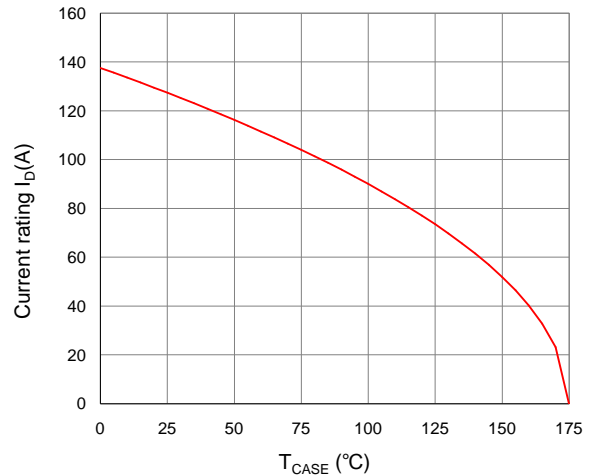
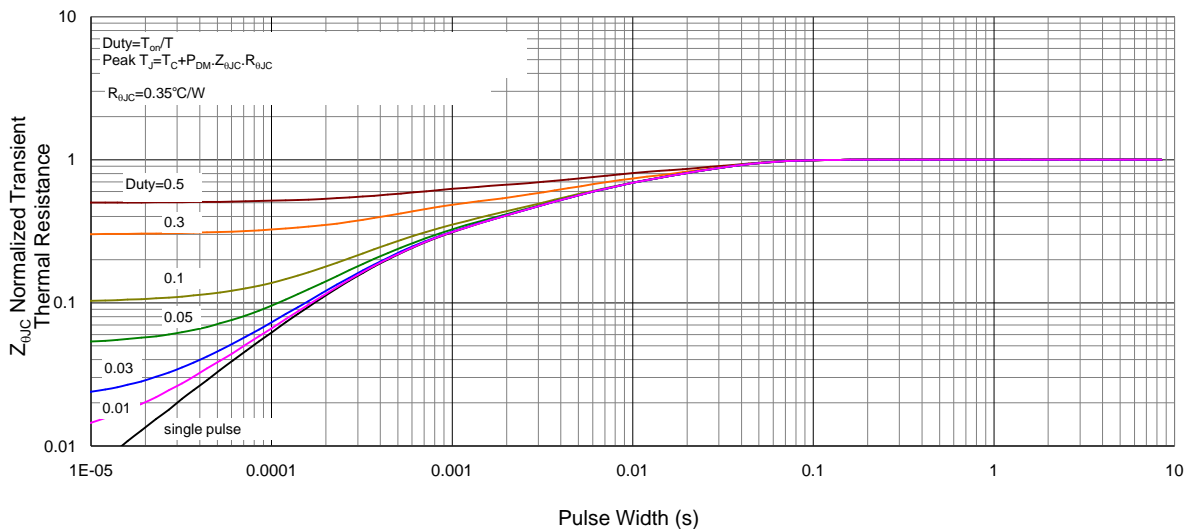
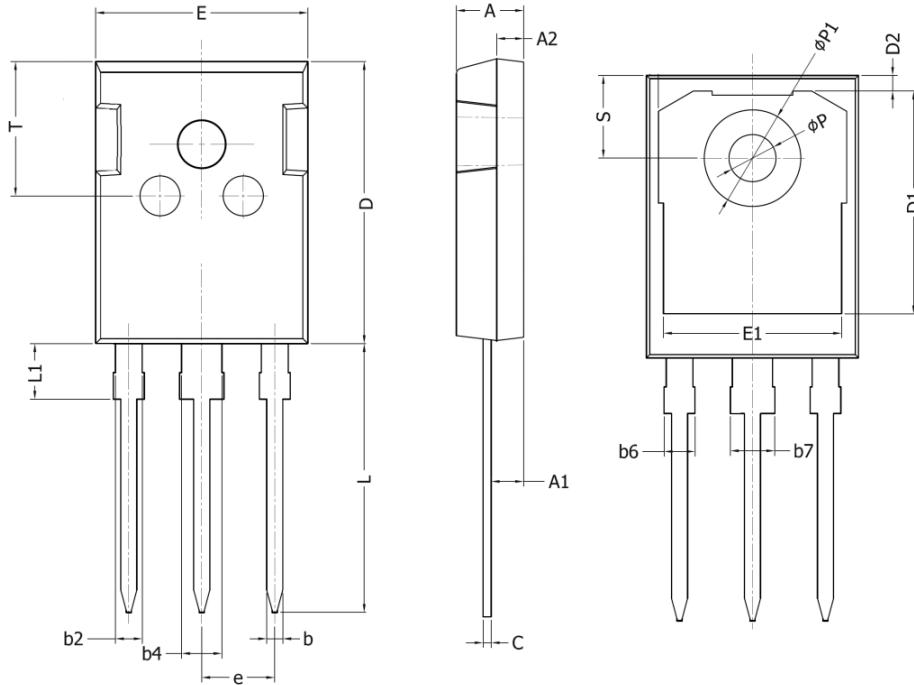


Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Case



**TO-247 Package Outline Dimensions**



Symbol	Dimensions In Millimeters	
	Min.	Max.
A	4.90	5.20
A1	2.31	2.51
A2	1.9	2.1
b	1.16	1.26
b2	1.96	2.06
b4	2.96	3.06
b6	-	2.25
b7	-	3.25
C	0.59	0.66
D	20.90	21.20
D1	16.25	16.85
D2	1.05	1.35
E	15.75	16.10
E1	13.00	13.60
e	5.436 BSC	
L	19.80	20.20
L1	-	4.30
P	3.40	3.60
P1	7.00	7.40
S	6.05	6.25
T	9.80	10.20