

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

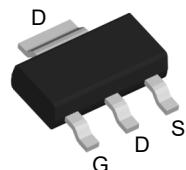
Product Summary



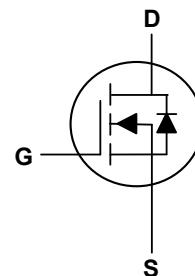
V_{DS}	100	V
I_D	5	A
$R_{DS(ON)}$ (at $V_{GS}=10V$)	140	mΩ

Applications

- High Frequency Point-of-Load,Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch



SOT223 Top View



Absolute Maximum Ratings($T_c=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹	I_D	5	A
Pulsed Drain Current ²	I_{DM}	24	A
Total Power Dissipation ³	P_D	3	W
Storage Temperature Range	T_{STG}	-55 to 150	°C
Operating Junction Temperature Range	T_J	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	---	71	°C/W
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	---	40	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	100	---	---	V
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}$, $I_D=5\text{A}$	---	115	140	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = 250\mu\text{A}$	1.0	1.8	2.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=100\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
Forward Transconductance	g_{fs}	$V_{\text{DS}}=5\text{V}$, $I_D=5\text{A}$	---	8	---	S
Total Gate Charge	Q_g	$V_{\text{DS}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=5\text{A}$	---	15	---	nC
Gate-Source Charge	Q_{gs}		---	3.2	---	
Gate-Drain Charge	Q_{gd}		---	4.7	---	
Turn-On Delay Time	$T_{\text{d}(\text{on})}$	$V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=2.5\Omega$, $R_L=15\Omega$	---	11	---	ns
Rise Time	T_r		---	7	---	
Turn-Off Delay Time	$T_{\text{d}(\text{off})}$		---	34	---	
Fall Time	T_f		---	9	---	
Input Capacitance	C_{iss}	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	685	---	pF
Output Capacitance	C_{oss}		---	120	---	
Reverse Transfer Capacitance	C_{rss}		---	85	---	

Drain-Source Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Diode Forward Voltage ²	V_{SD}	$V_{\text{GS}}=0\text{V}$, $I_s=5\text{A}$, $T_J=25^\circ\text{C}$	---	0.7	1.2	V

Note:

1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
3. The power dissipation is limited by 150°C junction temperature

Typical Characteristics

Figure1. Output characteristics

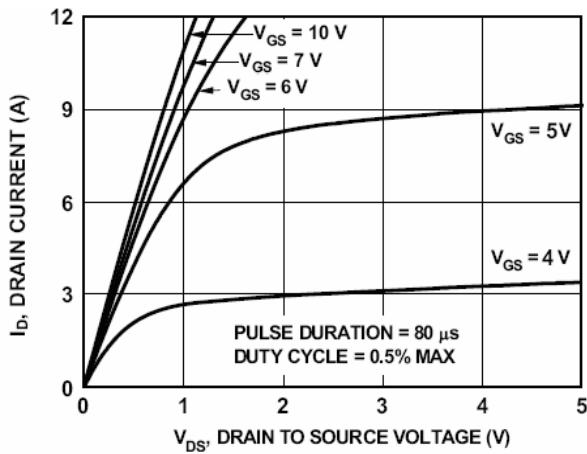


Figure2. Transfer characteristics

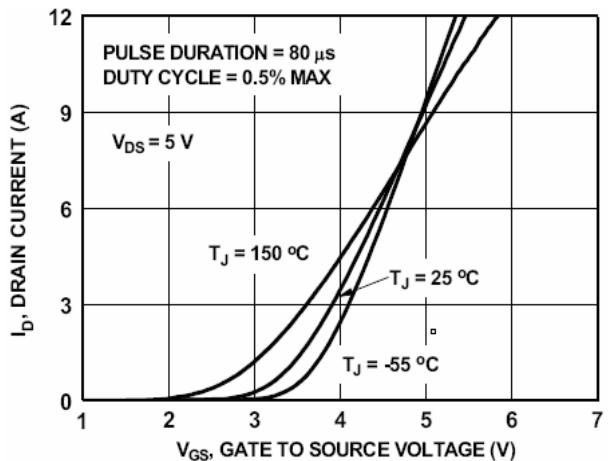


Figure3. Static drain-source on resistance

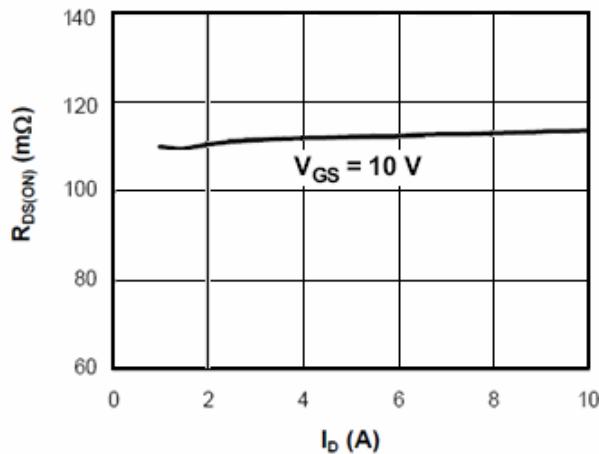


Figure4. R_{DS(ON)} vs Junction Temperature

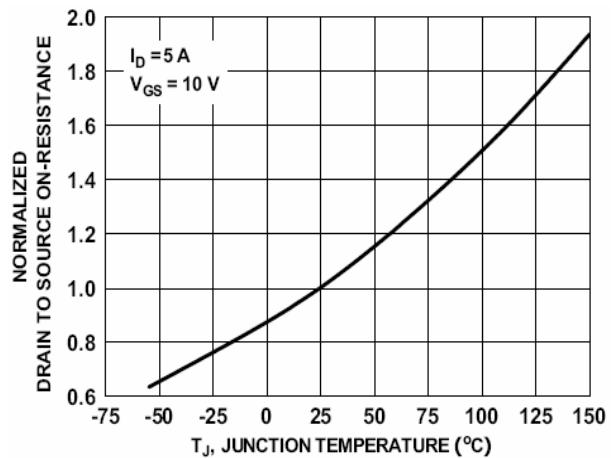


Figure5. BV_{DSS} vs Junction Temperature

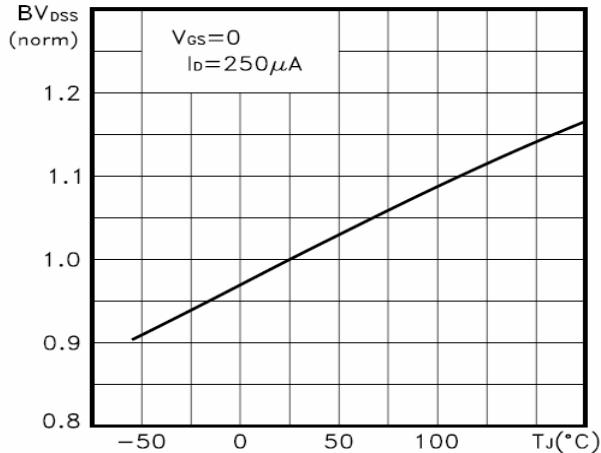


Figure6. V_{GS(th)} vs Junction Temperature

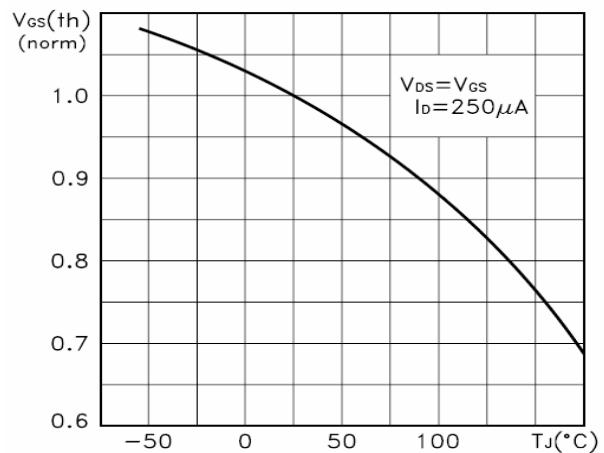
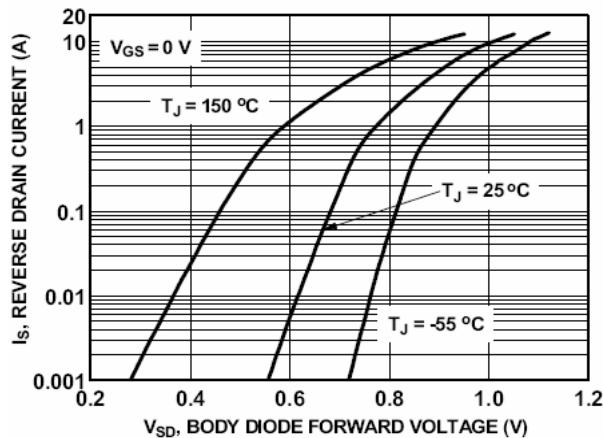
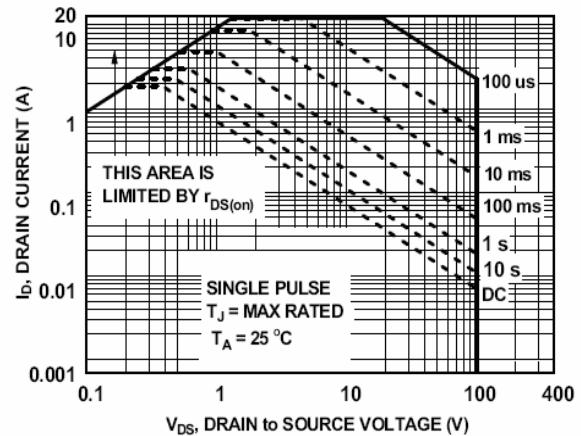
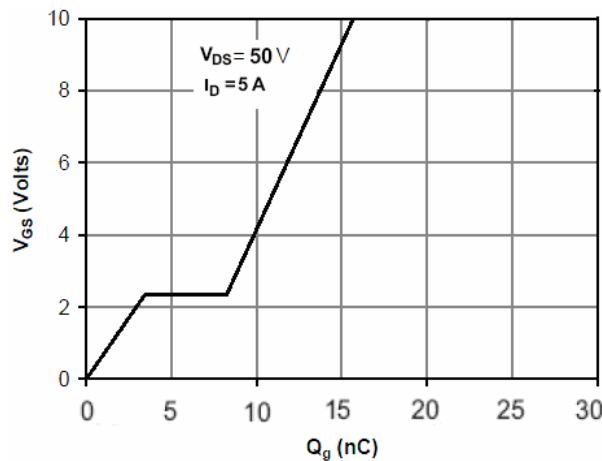
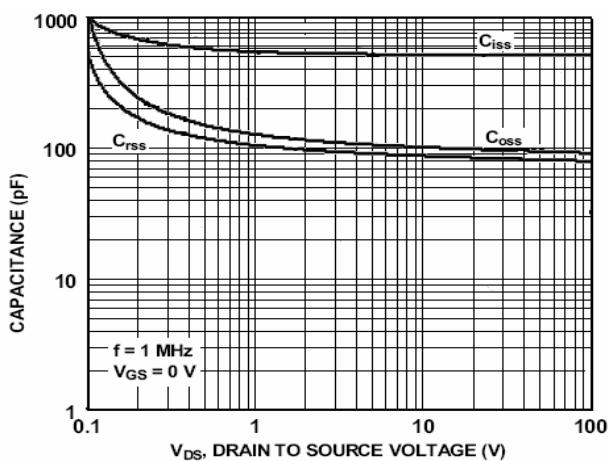
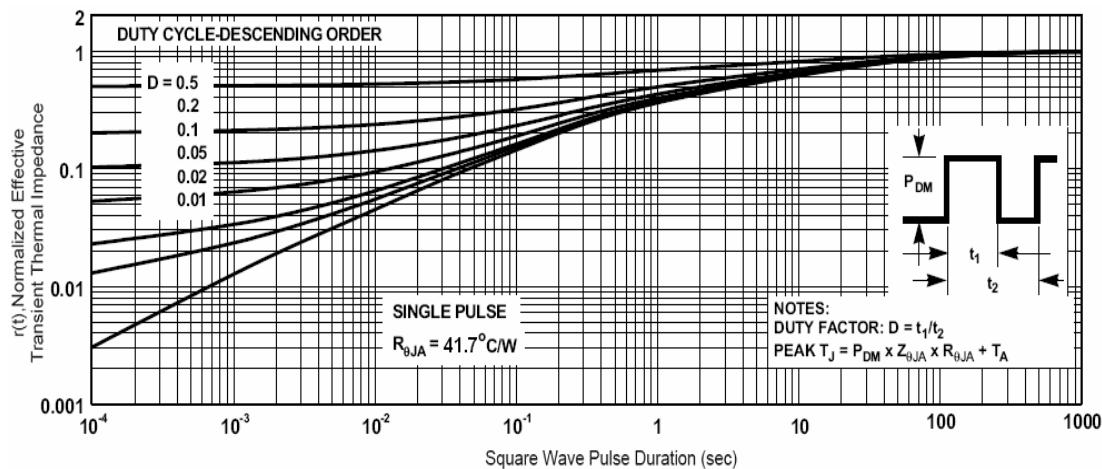
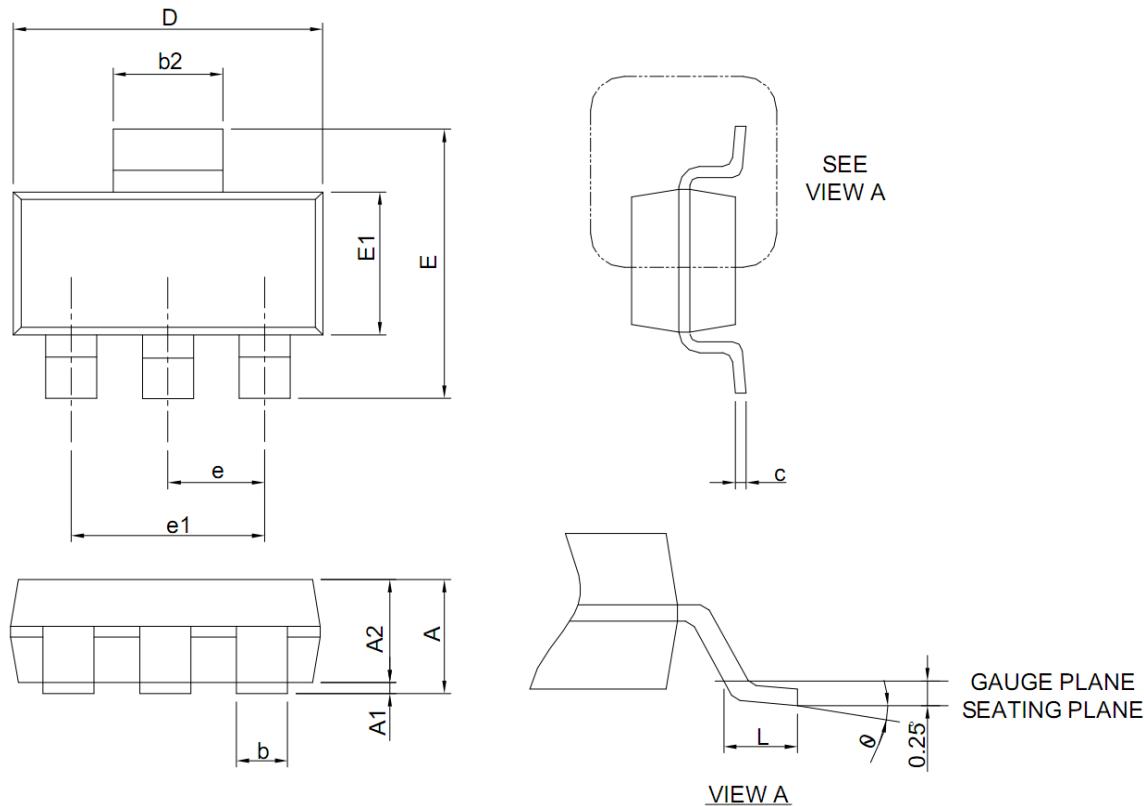


Figure7. Source-Drain Diode Forward Voltage

Figure8. Safe operating area

Figure9. Gate charge waveforms

Figure10. Capacitance

Figure11. Normalized Maximum Transient Thermal Impedance


SOT223 Package Outline Dimensions



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	1.50	1.65	1.80	A1	0.02	0.06	0.10
A2	1.50	1.60	1.70	b	0.66	0.72	0.80
b2	2.90	3.00	3.10	c	0.23	0.30	0.35
D	6.30	6.50	6.70	E	6.70	7.00	7.30
E1	3.30	3.50	3.70	e	2.30 REF		
e1	4.60 REF			L	0.75	--	1.15
θ	0°	--	10°				