

Features

- Advanced Shield Gate Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

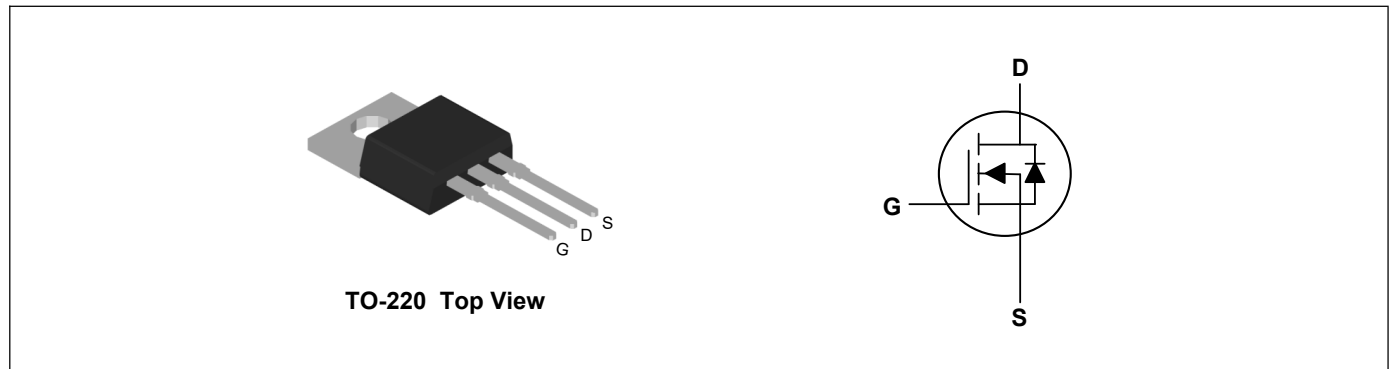
Applications

- High Frequency Point-of-Load, Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch

Product Summary



V_{DS}	80	V
I_D	68	A
$R_{DS(ON)}$ (at $V_{GS}=10V$)	11	m Ω
$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	15	m Ω



Absolute Maximum Ratings($T_c=25^\circ C$, unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	80	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹	I_D	68	A
Pulsed Drain Current ²	I_{DM}	148	A
Single Pulse Avalanche Energy ³	E_{AS}	180	mJ
Total Power Dissipation ⁴	P_D	75	W
Storage Temperature Range	T_{STG}	-55 to 150	$^\circ C$
Operating Junction Temperature Range	T_J	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	---	62	$^\circ C/W$
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	---	2	$^\circ C/W$

Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	80	---	---	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=15A$	---	9	11	$m\Omega$
		$V_{GS}=4.5V, I_D=12A$	---	12	15	$m\Omega$
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1.0	1.8	2.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=480V, V_{GS}=0V, T_J=25^{\circ}\text{C}$	---	---	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	---	---	± 100	nA
Total Gate Charge	Q_g	$V_{DD}=40V, V_{GS}=10V, I_D=14.5A$	---	24	---	nC
Gate-Source Charge	Q_{gs}		---	6.2	---	
Gate-Drain Charge	Q_{gd}		---	3.3	---	
Turn-On Delay Time	$T_{d(on)}$	$V_{DD}=40V, V_{GS}=10V, R_G=3\Omega, I_D=14.5A$	---	19	---	ns
Rise Time	T_r		---	10	---	
Turn-Off Delay Time	$T_{d(off)}$		---	24	---	
Fall Time	T_f		---	5	---	
Input Capacitance	C_{iss}	$V_{DS}=40V, V_{GS}=0V, f=1\text{MHz}$	---	1400	---	pF
Output Capacitance	C_{oss}		---	190	---	
Reverse Transfer Capacitance	C_{rss}		---	4	---	

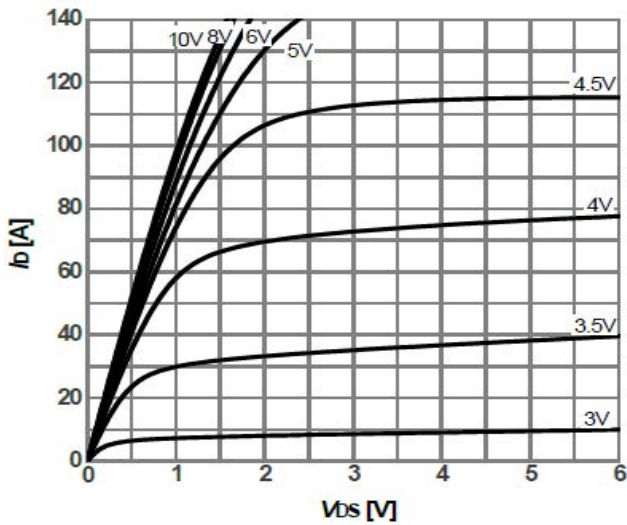
Drain-Source Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Diode Forward Voltage ²	V_{SD}	$V_{GS}=0V, I_S=15A, T_J=25^{\circ}\text{C}$	---	0.7	1.2	V
Reverse Recovery Time	t_{rr}	$I_F=10A, V_R=40V$ $di/dt=100A/\mu s, T_J=25^{\circ}\text{C}$	---	18	---	nS
Reverse Recovery Charge	Q_{rr}		---	60	---	nC

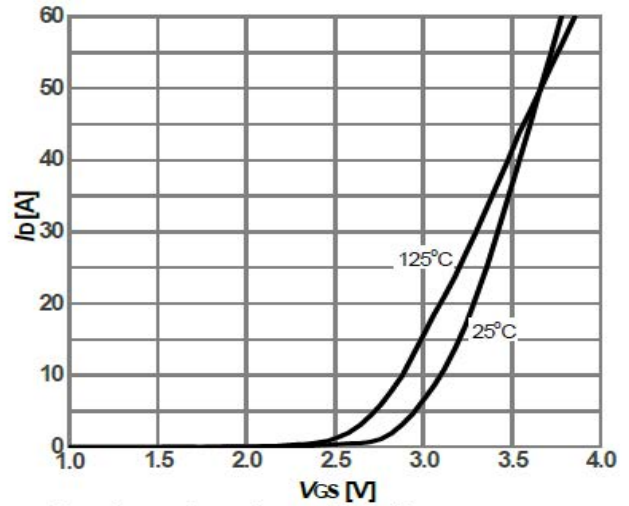
Note:

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{DD}=50V, I_D=30A$
- 4.The power dissipation is limited by 150 $^{\circ}\text{C}$ junction temperature

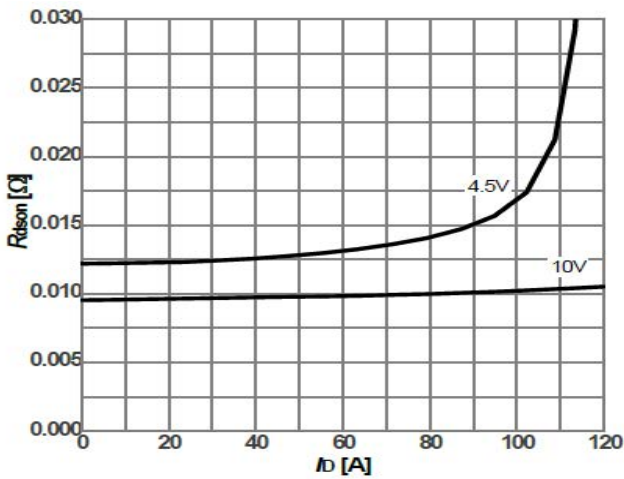
Typical Characteristics



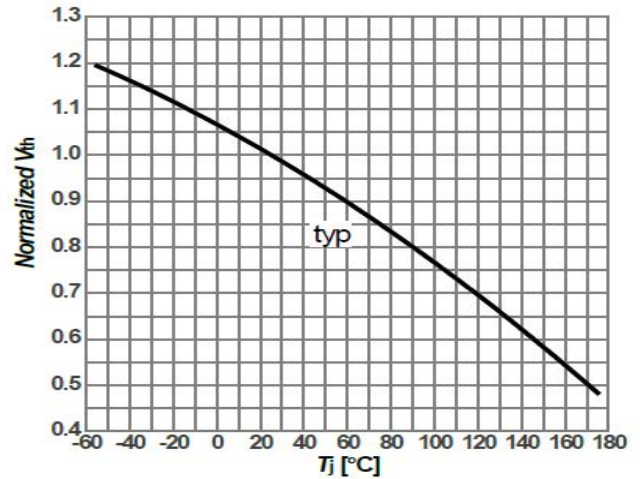
Typ. output characteristics



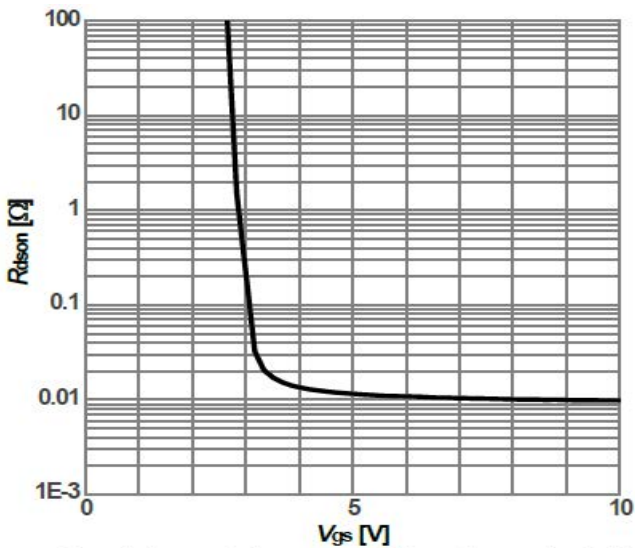
Typ. transfer characteristics



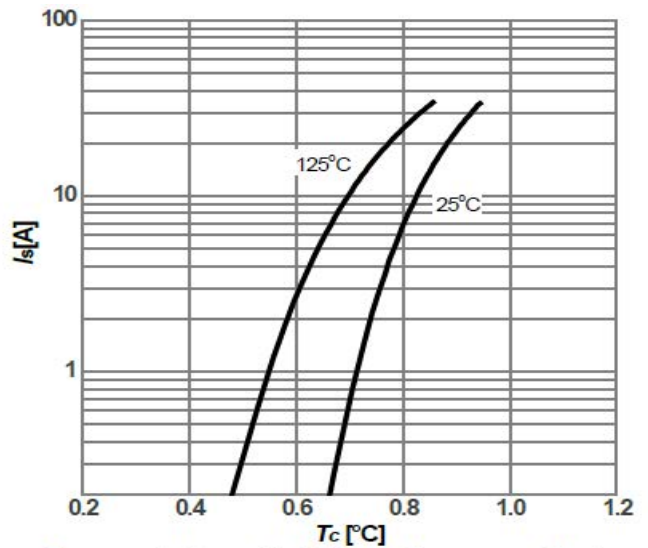
On-state resistance vs. Drain current



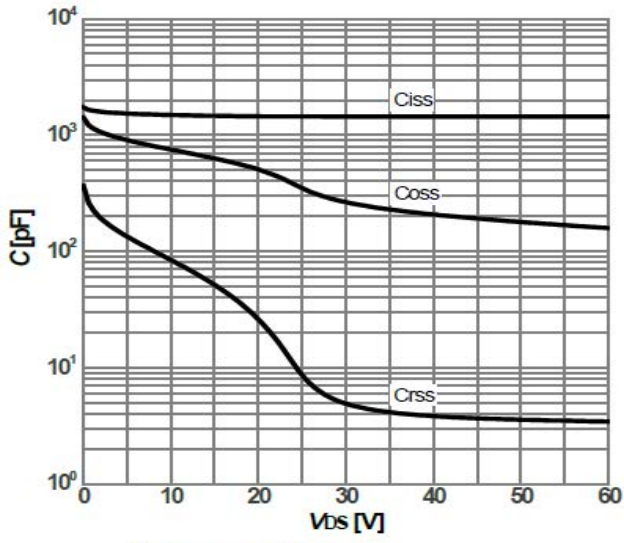
Gate threshold voltage vs. Junction temperature



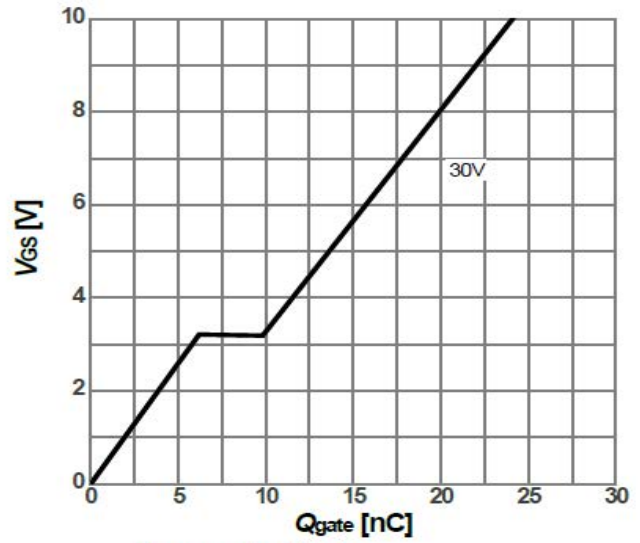
On-state resistance vs. V_{GS} characteristics



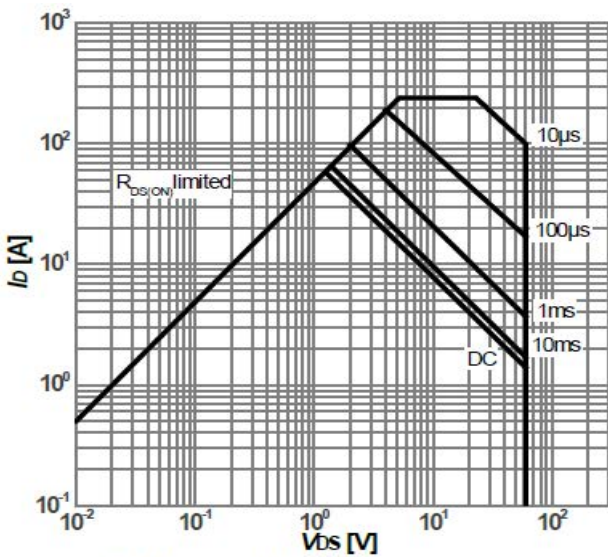
Forward characteristics of reverse diode



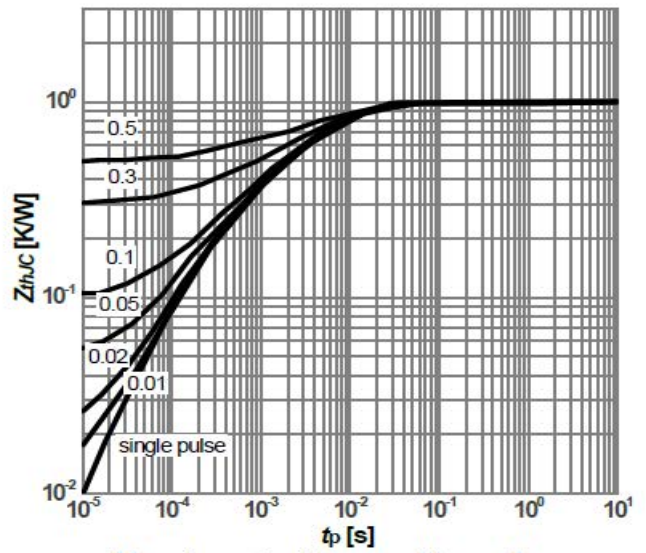
Typ. capacitances



Typ. gate charge

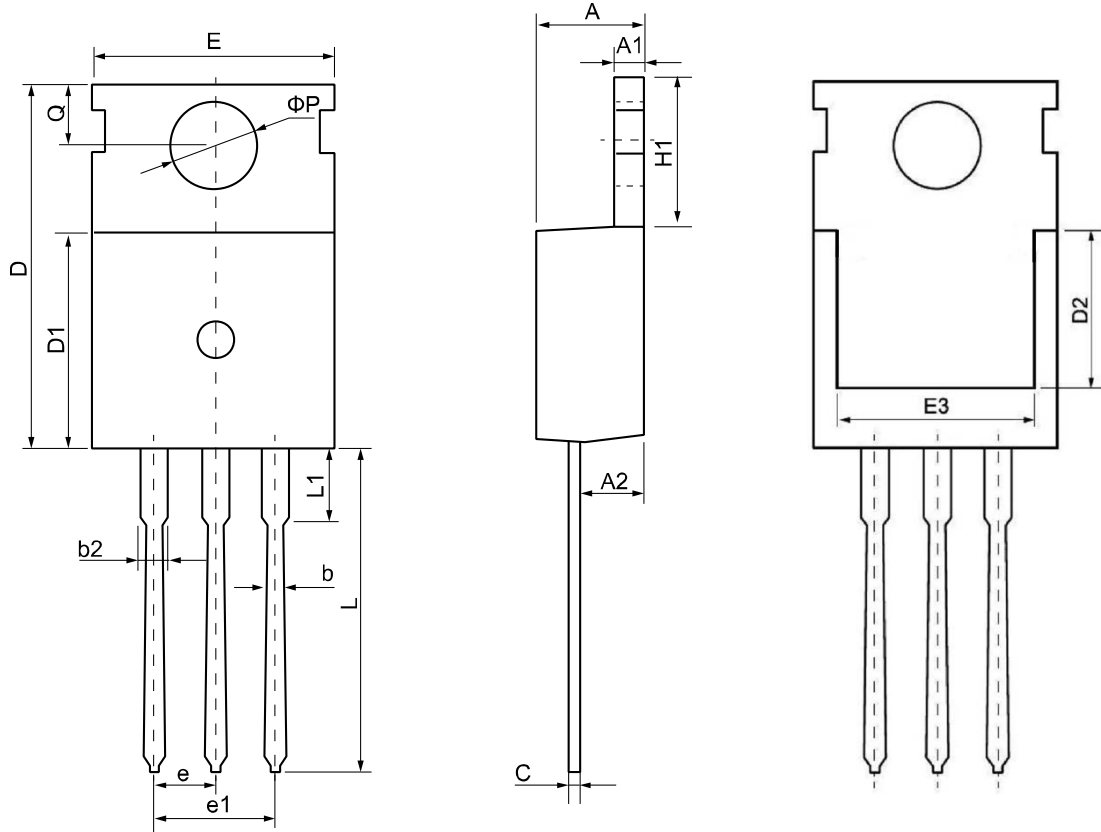


Safe operating area



Max. transient thermal impedance

TO-220 Package Outline Dimensions



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	4.30	4.55	4.75	E	9.65	10.00	10.25
A1	1.15	1.30	1.45	E3	7.00	--	--
A2	2.20	2.40	2.60	e	2.54 BSC		
b	0.70	0.80	0.95	e1	5.08 BSC		
b2	1.17	1.27	1.47	H1	6.30	6.50	6.80
c	0.40	0.50	0.65	L	12.70	13.50	14.10
D	15.30	15.60	15.90	L1	--	3.20	3.95
D1	8.90	9.10	9.35	φP	3.40	3.60	3.80
D2	5.50	--	--	Q	2.60	2.80	3.00