

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

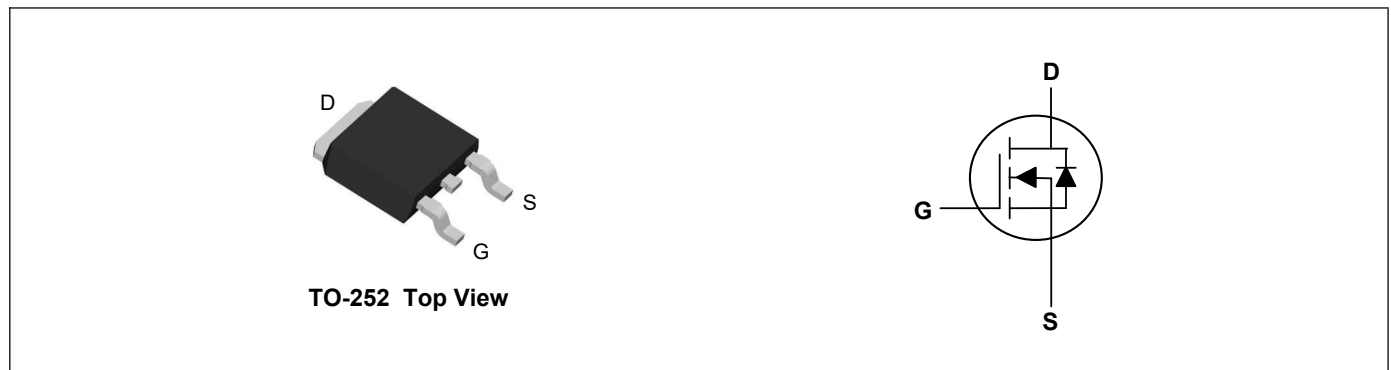
Applications

- High Frequency Point-of-Load, Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch

Product Summary



V_{DS}	80	V
I_D	100	A
$R_{DS(ON)}$ (at $V_{GS}=10V$)	5.6	m Ω



Absolute Maximum Ratings ($T_C=25^\circ C$, unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	80	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹	$I_D@T_C=25^\circ C$	100	A
Continuous Drain Current ¹	$I_D@T_C=100^\circ C$	64	A
Pulsed Drain Current ²	I_{DM}	400	A
Single Pulse Avalanche Energy ³	E_{AS}	211	mJ
Avalanche Current	I_{AS}	65	A
Total Power Dissipation ⁴	$P_D@T_C=25^\circ C$	154	W
Storage Temperature Range	T_{STG}	-55 to 150	$^\circ C$
Operating Junction Temperature Range	T_J	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	---	60	$^\circ C/W$
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	---	0.81	$^\circ C/W$

Electrical Characteristics (T_J=25°C, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250μA	80	---	---	V
Static Drain-Source On-Resistance ²	R _{DS(ON)}	V _{GS} =10V, I _D =30A	---	5	5.6	mΩ
Gate Threshold Voltage	V _{GS(th)}	V _{GS} =V _{DS} , I _D =250μA	2	3	4	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =80V, V _{GS} =0V, T _J =25°C	---	---	1	μA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
Forward Transconductance	g _{fs}	V _{DS} =10V, I _D =3A	---	13	---	S
Gate Resistance	R _g	V _{DS} =0V, V _{GS} =0V, f=1MHz	---	1.3	---	Ω
Total Gate Charge	Q _g	V _{DD} =40V, V _{GS} =10V, I _D =50A	---	32	---	nC
Gate-Source Charge	Q _{gs}		---	9.5	---	
Gate-Drain Charge	Q _{gd}		---	10	---	
Turn-On Delay Time	T _{d(on)}	V _{DD} =40V, V _{GS} =10V, R _G =6Ω, I _D =50A	---	10	---	ns
Rise Time	T _r		---	15	---	
Turn-Off Delay Time	T _{d(off)}		---	30	---	
Fall Time	T _f		---	35	---	
Input Capacitance	C _{iss}	V _{DS} =40V, V _{GS} =0V, f=1MHz	---	2200	---	pF
Output Capacitance	C _{oss}		---	540	---	
Reverse Transfer Capacitance	C _{rss}		---	15	---	

Drain-Source Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Diode Forward Voltage ²	V _{SD}	V _{GS} =0V, I _S =1A, T _J =25°C	---	0.8	1.1	V
Reverse Recovery Time	t _{rr}	I _S =10A, V _R =50V di/dt=100A/μs, T _J =25°C	---	50	---	nS
Reverse Recovery Charge	Q _{rr}		---	80	---	nC

Note:

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The EAS data shows Max. rating. The test condition is V_{DD}=50V, V_{GS}=10V, L=0.1mH
- 4.The power dissipation is limited by 150°C junction temperature

Typical Characteristics

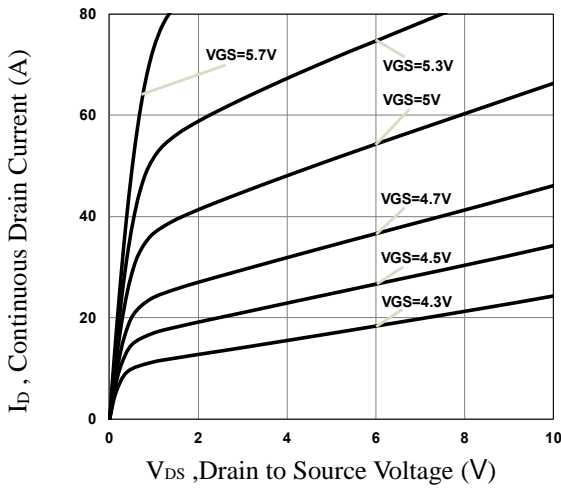


Fig.1 Typical Output Characteristics

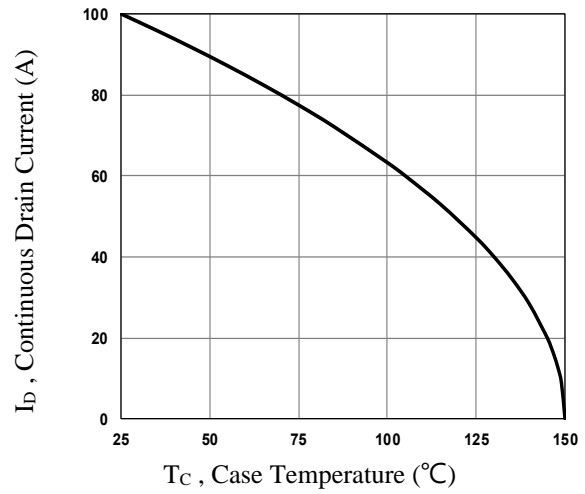


Fig.2 Continuous Drain Current vs. T_C

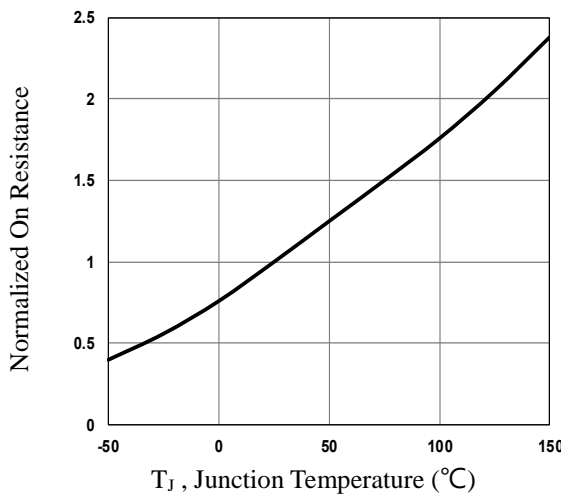


Fig.3 Normalized $R_{DS(on)}$ vs. T_J

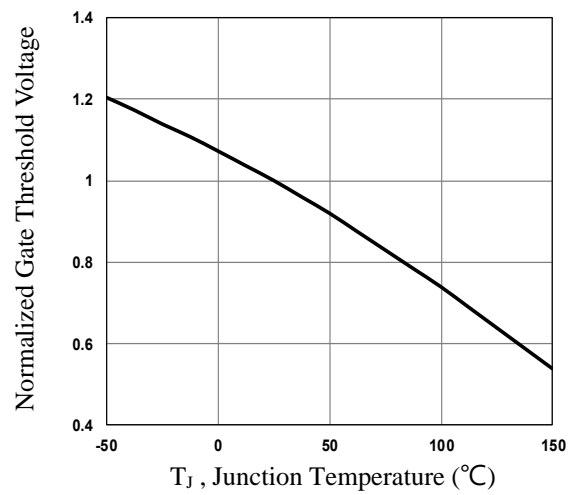


Fig.4 Normalized V_{th} vs. T_J

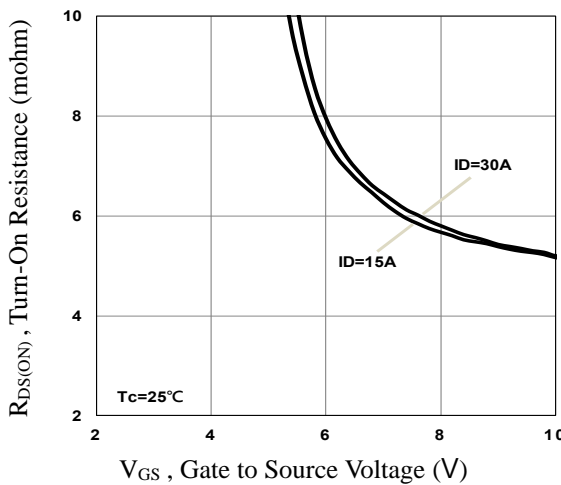


Fig.5 Turn-On Resistance vs. V_{GS}

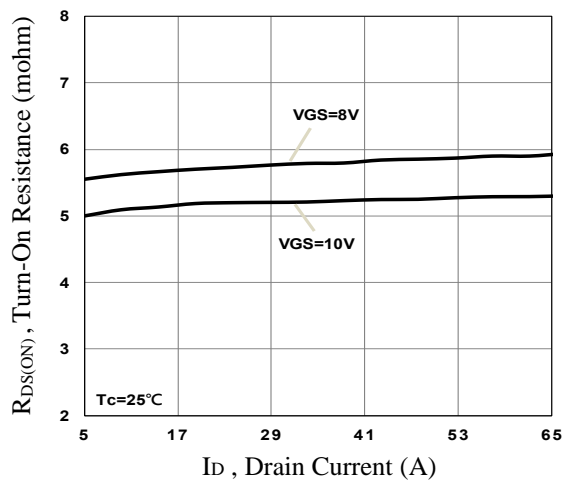


Fig.6 Turn-On Resistance vs. I_D

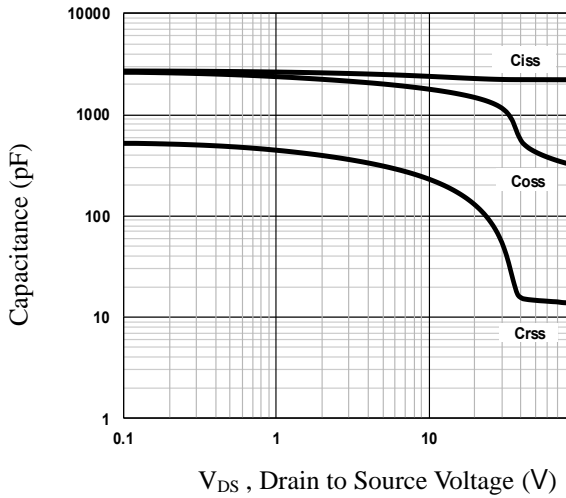


Fig.7 Capacitance Characteristics

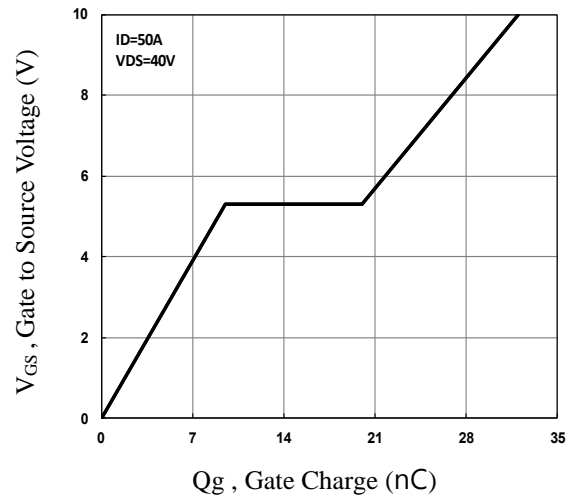


Fig.8 Gate Charge Characteristics

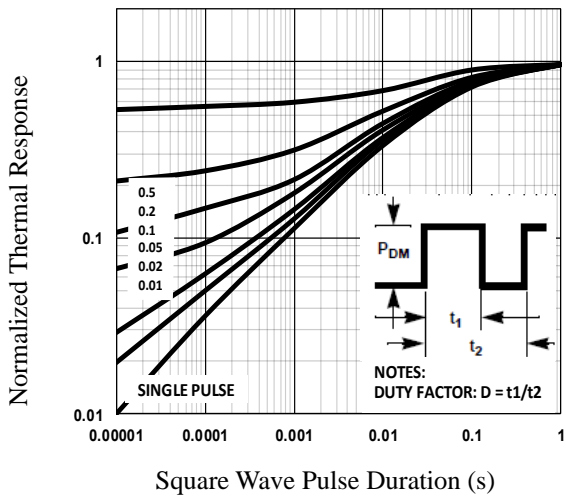


Fig.9 Normalized Transient Impedance

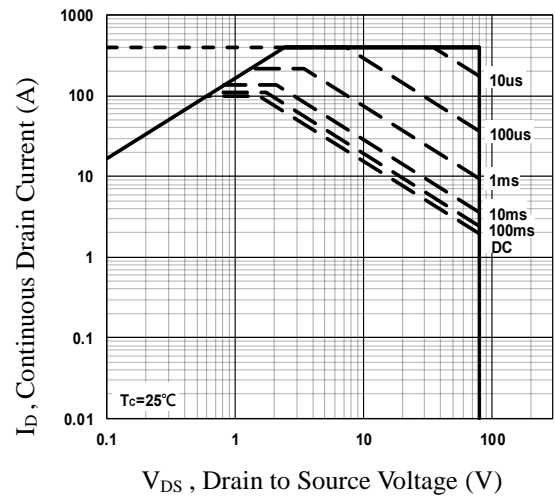
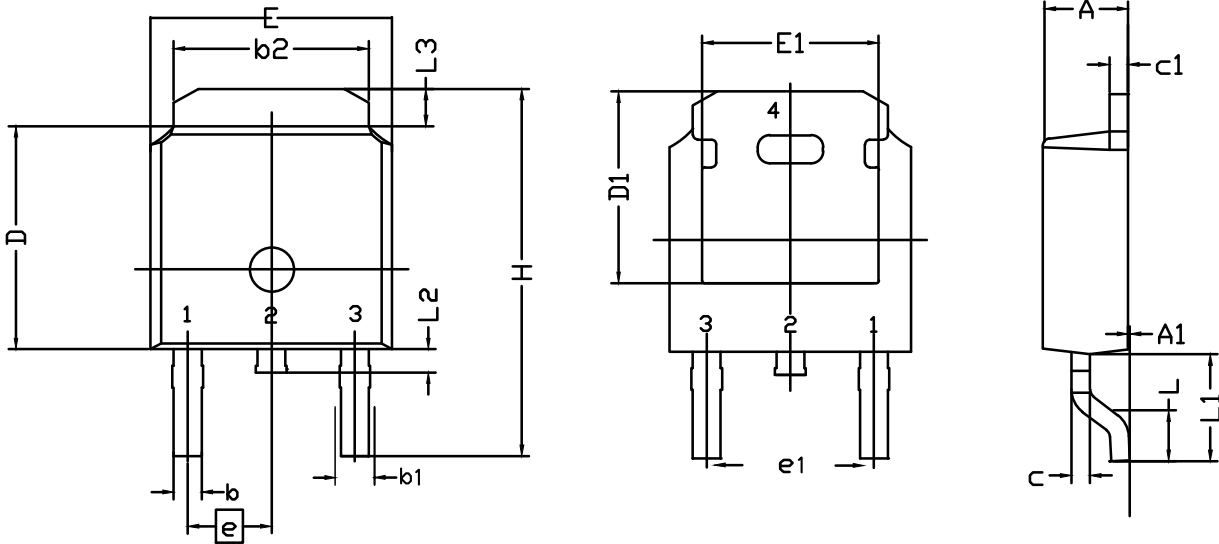


Fig.10 Maximum Safe Operation Area

TO-252 Package Outline Dimensions



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	2.20	2.30	2.38	E	6.40	6.60	6.731
A₁	0.00	0.10	0.20	E₁	4.40	--	--
b	0.64	0.76	0.89	e	2.286 BSC		
b₁	0.77	0.85	1.14	e₁	4.572 BSC		
b₂	5.00	5.33	5.46	H	9.40	10.00	10.40
c	0.458	0.508	0.610	L	1.40	1.52	1.77
C₁	0.458	0.508	0.620	L₁	--	2.743	--
D	5.98	6.10	6.223	L₂	0.60	0.80	1.01
D₁	5.20	5.25	5.38	L₃	0.90	1.06	1.25