

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

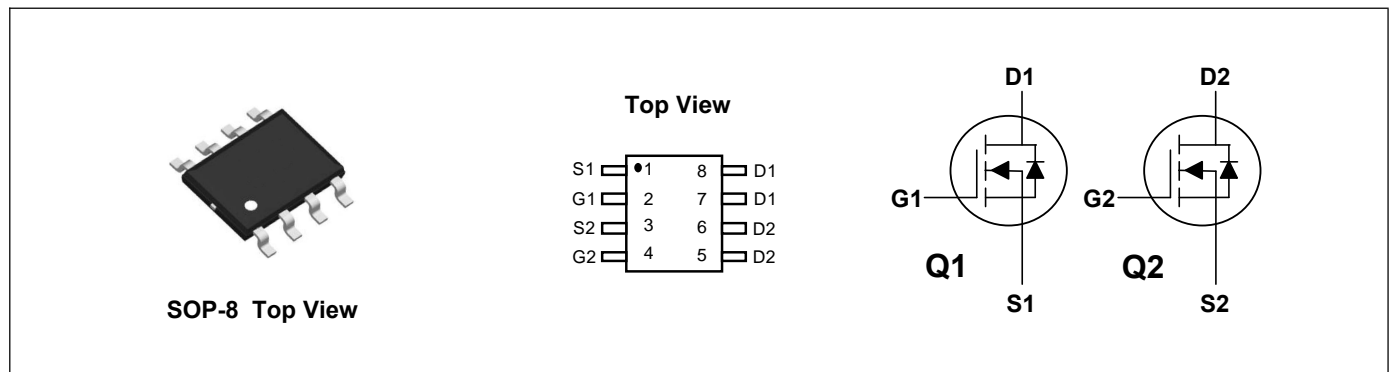
Product Summary



	Q1	Q2	
V_{DS}	40	40	V
I_D	8	12	A
$R_{DS(ON)}$ (at $V_{GS}=10V$)	18	14	m Ω
$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	28	20	m Ω

Applications

- High Frequency Point-of-Load, Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch



Absolute Maximum Ratings ($T_A=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Q1	Q2	Units
Drain-Source Voltage	V_{DS}	40	40	V
Gate-Source Voltage	V_{GS}	± 20	± 20	V
Continuous Drain Current ¹	$I_D@T_C=25^\circ\text{C}$	8	12	A
Continuous Drain Current ¹	$I_D@T_C=100^\circ\text{C}$	5.7	8.5	A
Pulsed Drain Current ²	I_{DM}	32	60	A
Total Power Dissipation ³	$P_D@T_C=25^\circ\text{C}$	2	2.5	W
Storage Temperature Range	T_{STG}	-55 to 150		$^\circ\text{C}$
Operating Junction Temperature Range	T_J	-55 to 150		$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient ¹ (Q1)	$R_{\theta JA}$	62.5	85	$^\circ\text{C/W}$
Thermal Resistance Junction-Ambient ¹ (Q2)	$R_{\theta JA}$	50	75	$^\circ\text{C/W}$

Q1 Electrical Characteristics (T_J=25°C, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	40	---	---	V
Static Drain-Source On-Resistance ²	R _{DS(ON)}	V _{GS} =10V, I _D =8A	---	15.8	18	mΩ
		V _{GS} =4.5V, I _D =4A	---	22	28	mΩ
Gate Threshold Voltage	V _{GS(th)}	V _{GS} =V _{DS} , I _D =250uA	1.0	1.5	2.0	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =40V, V _{GS} =0V, T _J =25°C	---	---	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
Forward Transconductance	g _{fs}	V _{DS} =5V, I _D =8A	33	---	---	S
Total Gate Charge	Q _g	V _{DS} =20V, V _{GS} =10V, I _D =8A	---	23	---	nC
Gate-Source Charge	Q _{gs}		---	3.5	---	
Gate-Drain Charge	Q _{gd}		---	5.3	---	
Turn-On Delay Time	T _{d(on)}	V _{DD} =20V, V _{GS} =10V, R _G =3Ω, R _L =2.5Ω	---	5.5	---	ns
Rise Time	T _r		---	14	---	
Turn-Off Delay Time	T _{d(off)}		---	24	---	
Fall Time	T _f		---	12	---	
Input Capacitance	C _{iss}	V _{DS} =20V, V _{GS} =0V, f=1MHz	---	964	---	pF
Output Capacitance	C _{oss}		---	109	---	
Reverse Transfer Capacitance	C _{rss}		---	96	---	

Drain-Source Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Diode Forward Voltage ²	V _{SD}	V _{GS} =0V, I _S =8A, T _J =25°C	---	0.8	1.2	V

Note:

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The power dissipation is limited by 150°C junction temperature

Q1 Typical Characteristics

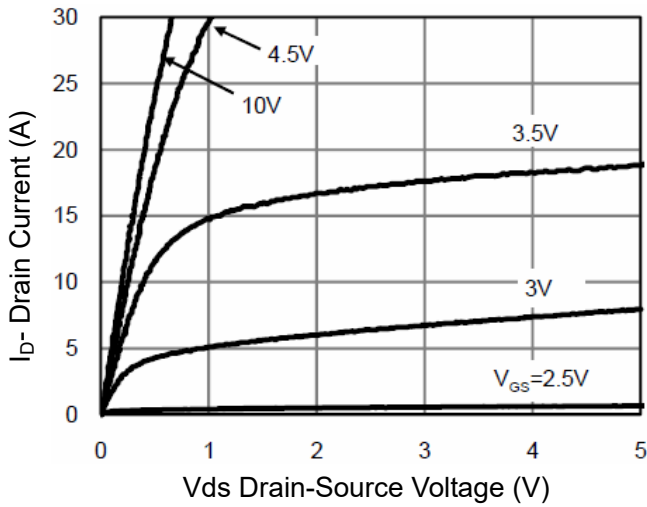


Figure 1 Output Characteristics

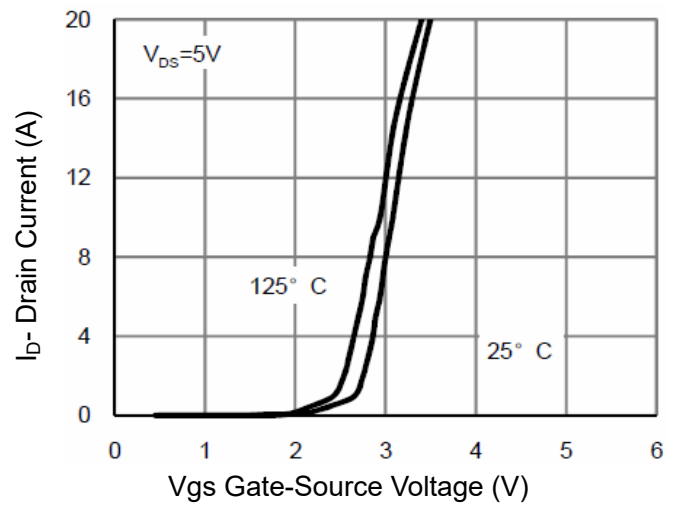


Figure 2 Transfer Characteristics

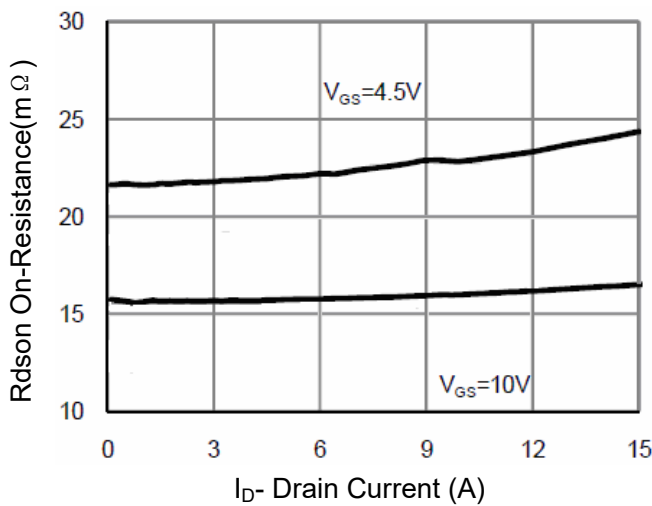


Figure 3 Drain-Source On-Resistance

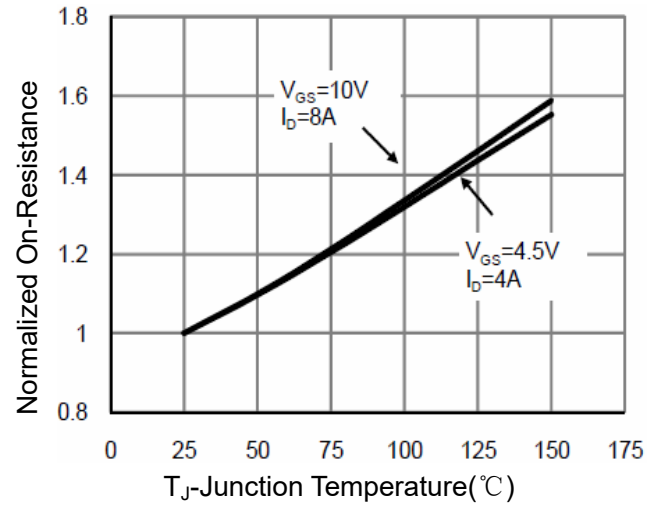


Figure 4 Drain-Source On-Resistance

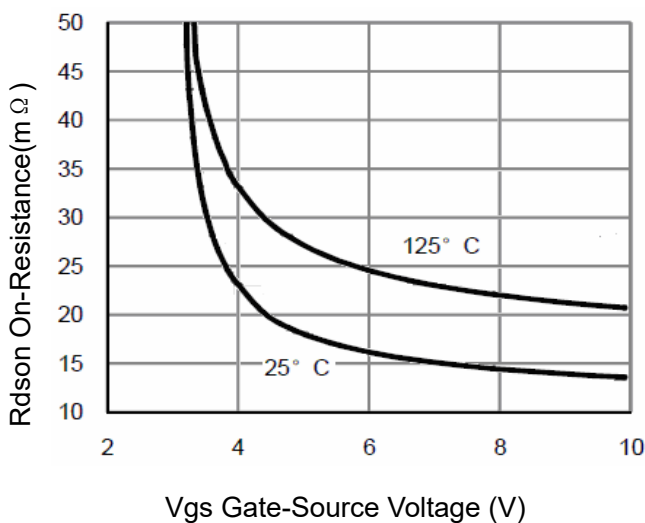


Figure 5 Rds(on) vs Vgs

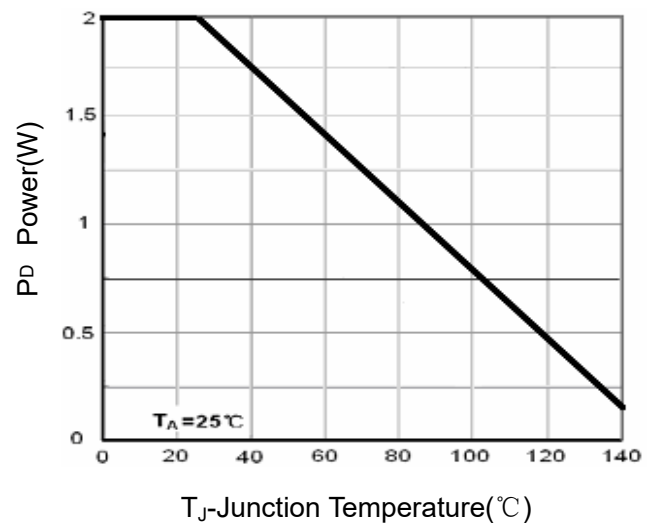


Figure 6 Power Dissipation

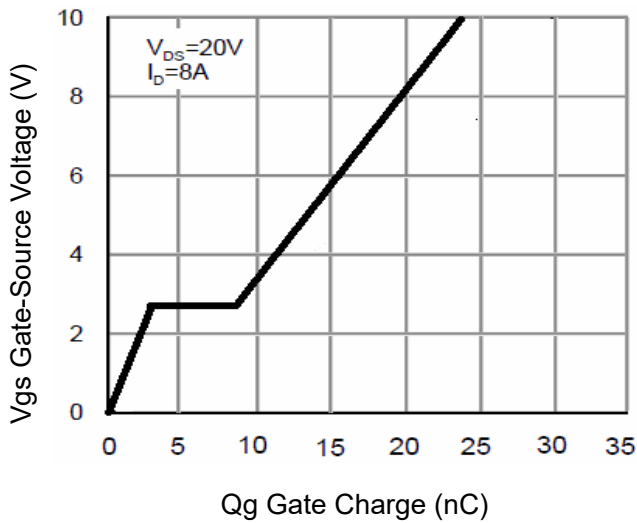


Figure 7 Gate Charge

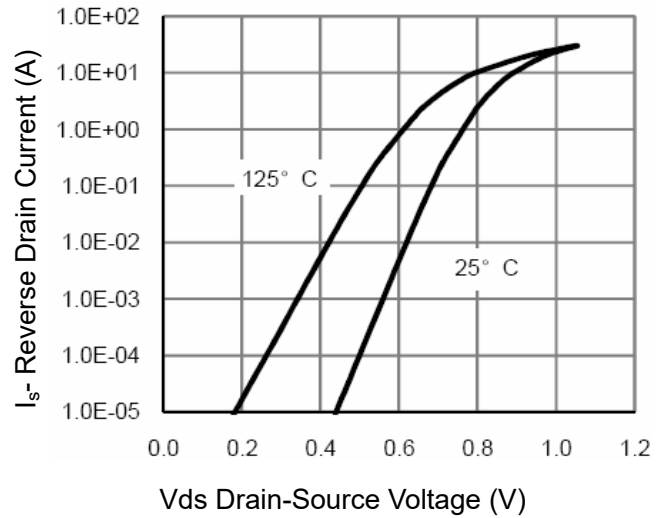


Figure 8 Source- Drain Diode Forward

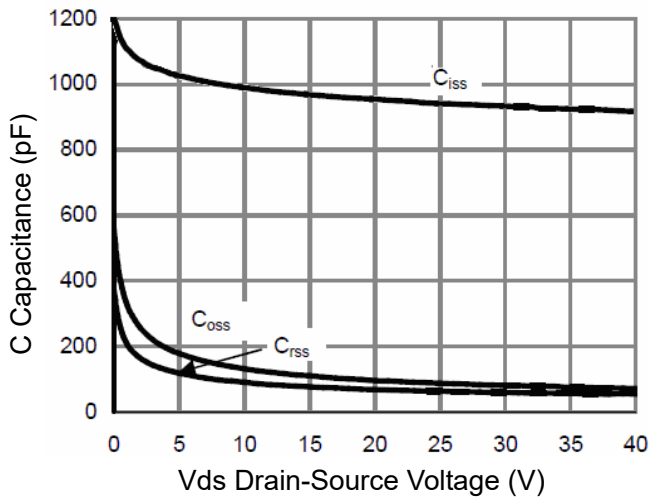


Figure 9 Capacitance vs Vds

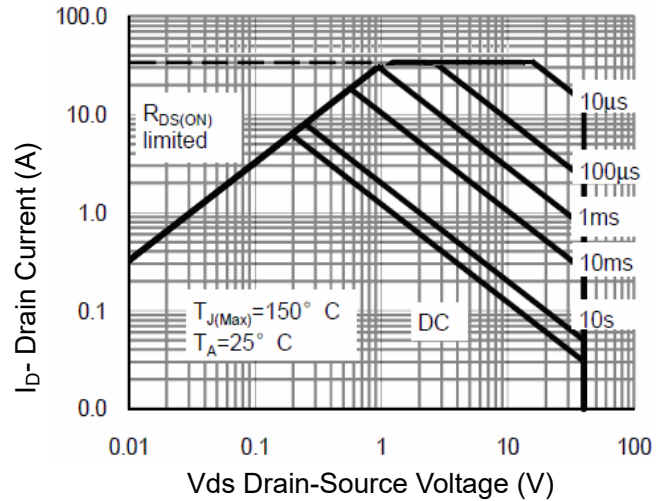


Figure 10 Safe Operation Area

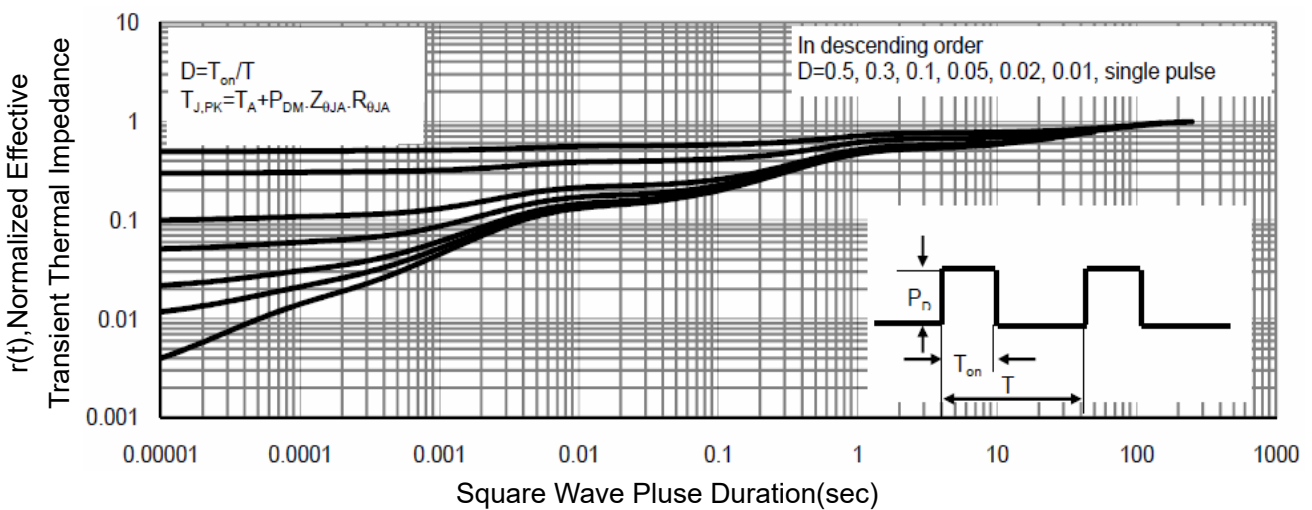


Figure 11 Normalized Maximum Transient Thermal Impedance

Q2 Electrical Characteristics (T_J=25°C, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	40	---	---	V
Static Drain-Source On-Resistance ²	R _{DS(ON)}	V _{GS} =10V, I _D =10A	---	11.7	14	mΩ
		V _{GS} =4.5V, I _D =8A	---	15.6	20	mΩ
Gate Threshold Voltage	V _{GS(th)}	V _{GS} =V _{DS} , I _D =250uA	1.2	1.6	2.5	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =40V, V _{GS} =0V, T _J =25°C	---	---	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
Forward Transconductance	g _{fs}	V _{DS} =5V, I _D =10A	---	75	---	S
Total Gate Charge	Q _g	V _{DS} =20V, V _{GS} =10V, I _D =10A	---	38	---	nC
Gate-Source Charge	Q _{gs}		---	5.6	---	
Gate-Drain Charge	Q _{gd}		---	7.4	---	
Turn-On Delay Time	T _{d(on)}	V _{DD} =20V, V _{GS} =10V, R _G =3Ω, R _L =2Ω	---	6.4	---	ns
Rise Time	T _r		---	17.2	---	
Turn-Off Delay Time	T _{d(off)}		---	29.6	---	
Fall Time	T _f		---	16.8	---	
Input Capacitance	C _{iss}	V _{DS} =20V, V _{GS} =0V, f=1MHz	---	1780	---	pF
Output Capacitance	C _{oss}		---	209	---	
Reverse Transfer Capacitance	C _{rss}		---	160	---	

Drain-Source Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current ¹	I _S		---	---	12	A
Diode Forward Voltage ²	V _{SD}	V _{GS} =0V, I _S =10A, T _J =25°C	---	---	1.2	V
Reverse Recovery Time	t _{rr}	I _F =10A, di/dt=100A/μs, T _J =25°C	---	29	---	nS
Reverse Recovery Charge	Q _{rr}		---	26	---	nC

Note:

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The power dissipation is limited by 150°C junction temperature

Q2 Typical Characteristics

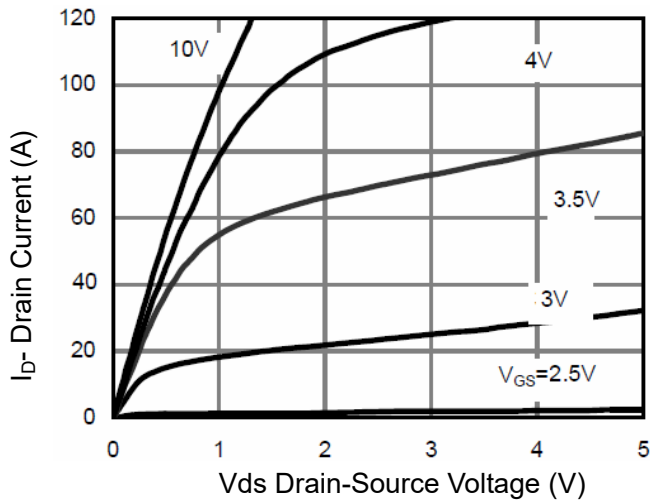


Figure 1 Output Characteristics

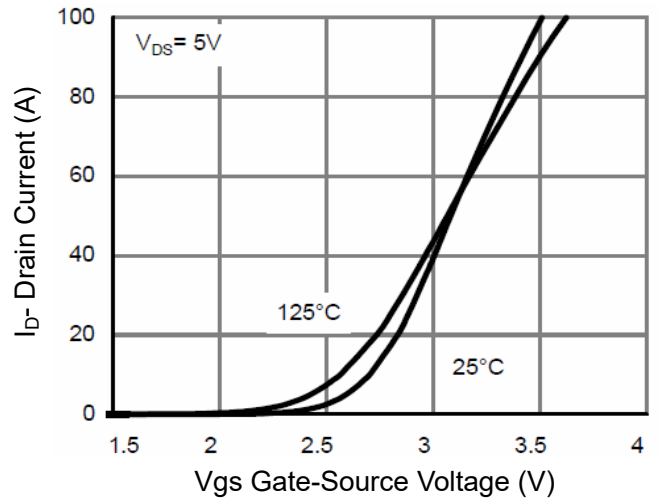


Figure 2 Transfer Characteristics

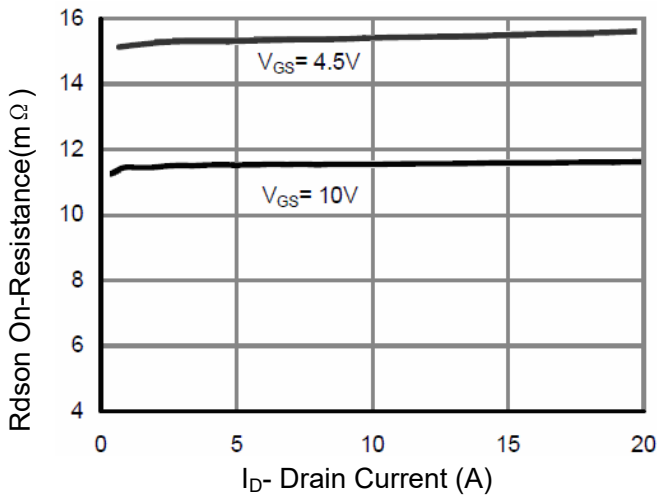


Figure 3 Rdson- Drain Current

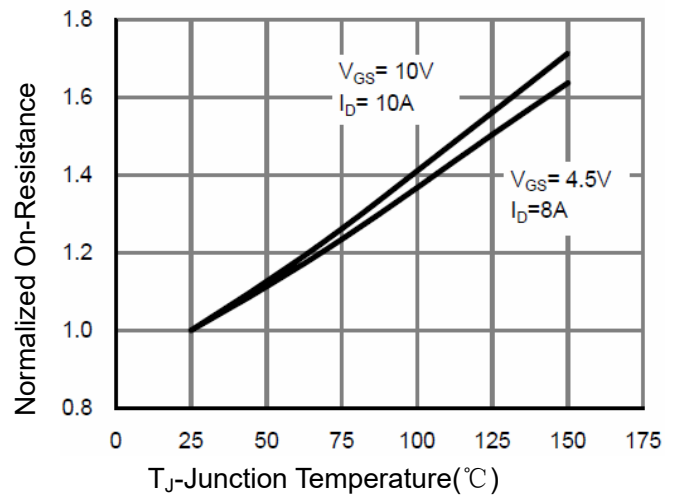


Figure 4 Rdson-Junction Temperature

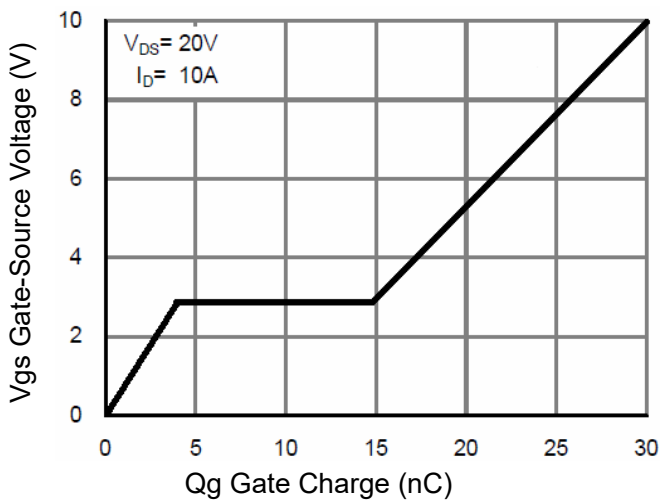


Figure 5 Gate Charge

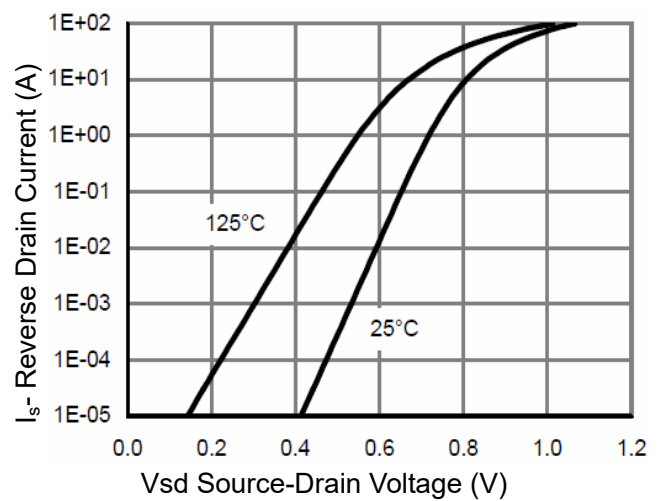


Figure 6 Source- Drain Diode Forward

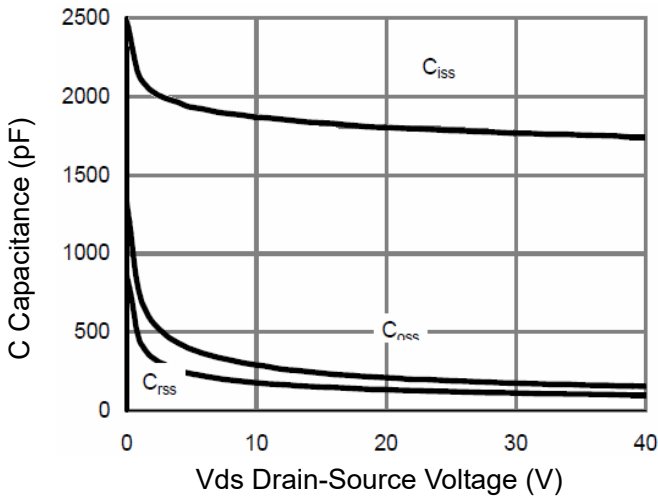


Figure 7 Capacitance vs Vds

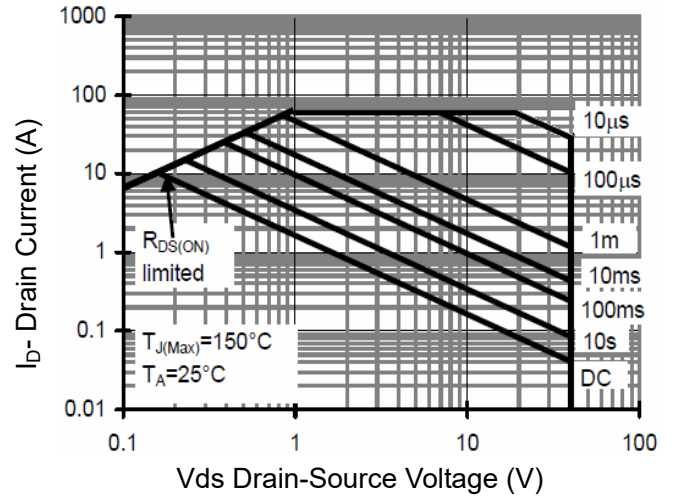


Figure 8 Safe Operation Area

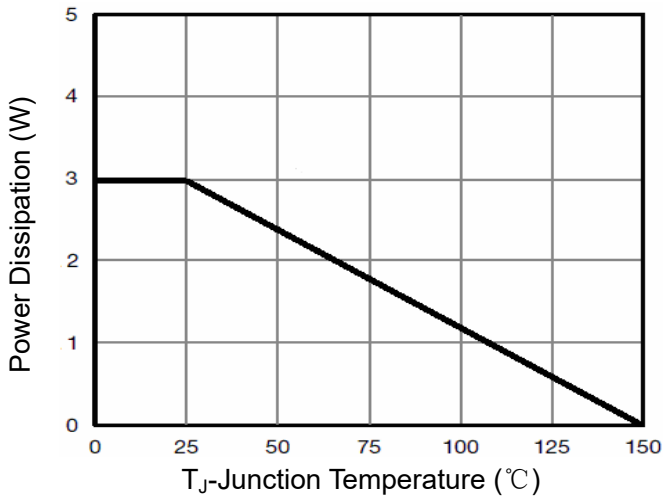


Figure 9 Power De-rating

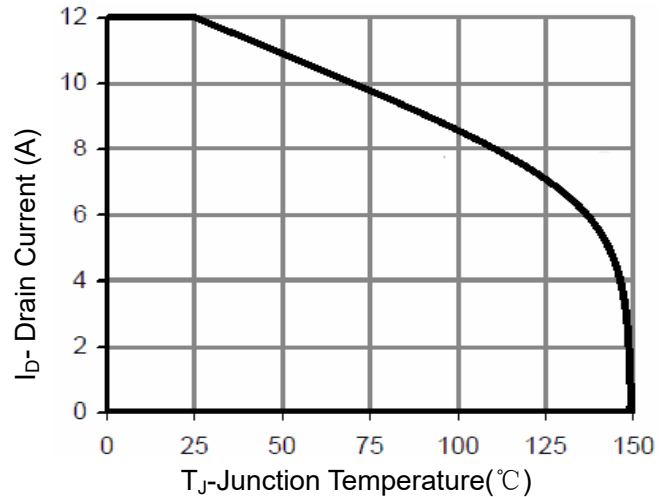


Figure 10 Current De-rating

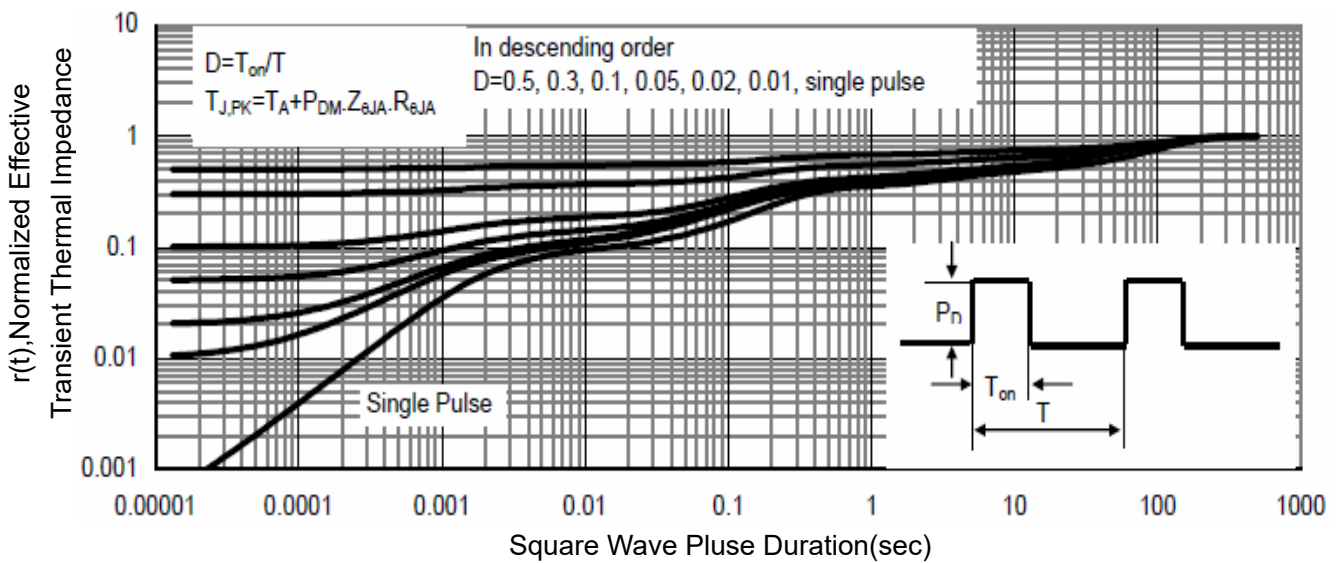
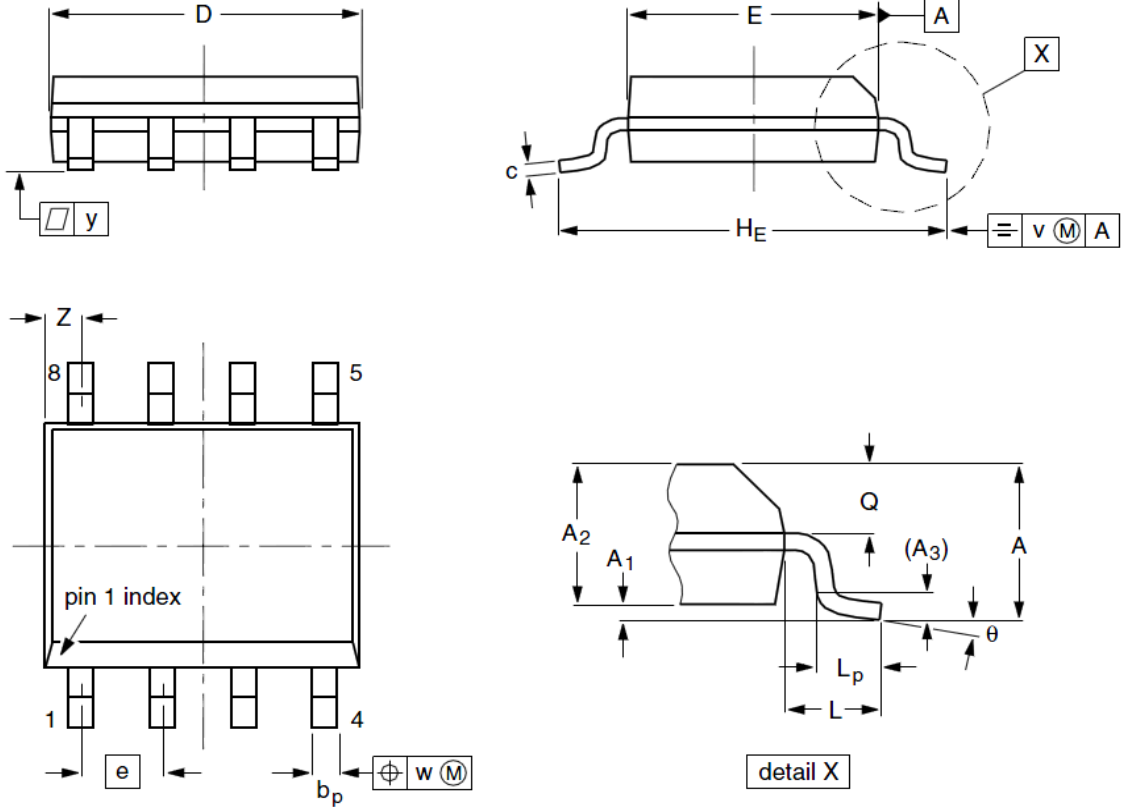


Figure 11 Normalized Maximum Transient Thermal Impedance

SOP-8 Package Outline Dimensions



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	1.35	1.55	1.75	A₁	0.10	0.18	0.25
A₂	1.25	1.45	1.65	A₃	--	0.25	--
b_p	0.36	0.42	0.51	c	0.19	0.22	0.25
D	4.70	4.92	5.10	E	3.80	3.90	4.00
e	--	1.27	--	H_E	5.80	6.00	6.20
L	--	1.05	--	L_p	0.40	0.68	1.00
Q	0.60	0.65	0.73	v	--	0.25	--
w	--	0.25	--	y	--	0.10	--
Z	0.30	0.50	0.70	θ	0°		8°