

20V Common-Drain Dual N-Channel MOSFET

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Green Device Available
- ESD Protected 2KV Embedded

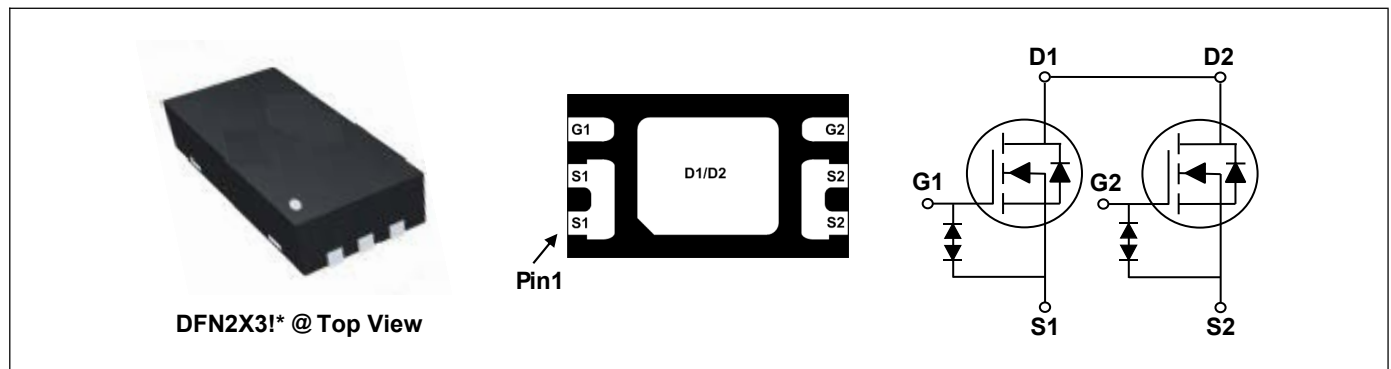
Product Summary



| | | |
|----------------------------------|------|------------|
| V_{DS} | 20 | V |
| I_D | 8 | A |
| $R_{DS(ON)}$ (at $V_{GS}=4.5V$) | 16 | m Ω |
| $R_{DS(ON)}$ (at $V_{GS}=2.5V$) | 27.5 | m Ω |

Applications

- Handheld Instruments
- Battery Switch



Absolute Maximum Ratings($T_A=25^{\circ}C$, unless otherwise noted)

| Parameter | Symbol | Rating | Units |
|--|-----------------------|------------|-------------|
| Drain-Source Voltage | V_{DS} | 20 | V |
| Gate-Source Voltage | V_{GS} | ± 12 | V |
| Continuous Drain Current, V_{GS} @ 4.5V ¹ | $I_D@T_A=25^{\circ}C$ | 8 | A |
| Continuous Drain Current, V_{GS} @ 4.5V ¹ | $I_D@T_A=70^{\circ}C$ | 6.4 | A |
| Pulsed Drain Current ² | I_{DM} | 50 | A |
| Total Power Dissipation ¹ | $P_D@T_A=25^{\circ}C$ | 1.56 | W |
| Storage Temperature Range | T_{STG} | -55 to 150 | $^{\circ}C$ |
| Operating Junction Temperature Range | T_J | -55 to 150 | $^{\circ}C$ |

Thermal Characteristics

| Parameter | Symbol | Typ | Max | Unit |
|---|-----------------|-----|-----|---------------|
| Thermal Resistance Junction-Ambient ¹ (t \leq 10s) | $R_{\theta JA}$ | --- | 80 | $^{\circ}C/W$ |

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Electrical Characteristics ($T_J=25^{\circ}\text{C}$, unless otherwise noted)

| Parameter | Symbol | Conditions | Min | Typ | Max | Unit |
|--|--------------|---|-----|------|----------|------------|
| Drain-Source Breakdown Voltage | BV_{DSS} | $V_{GS}=0V, I_D=250\mu A$ | 20 | --- | --- | V |
| Static Drain-Source On-Resistance ² | $R_{DS(ON)}$ | $V_{GS}=4.5V, I_D=2A$ | --- | 13 | 16 | m Ω |
| | | $V_{GS}=4.0V, I_D=2A$ | --- | 13.5 | 17 | m Ω |
| | | $V_{GS}=3.7V, I_D=2A$ | --- | 14 | 18 | m Ω |
| | | $V_{GS}=3.1V, I_D=2A$ | --- | 16 | 21 | m Ω |
| | | $V_{GS}=2.5V, I_D=2A$ | --- | 20.5 | 27.5 | m Ω |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{GS}=V_{DS}, I_D=250\mu A$ | 0.5 | --- | 1.2 | V |
| Drain-Source Leakage Current | I_{DSS} | $V_{DS}=16V, V_{GS}=0V, T_J=25^{\circ}\text{C}$ | --- | --- | 1 | μA |
| | | $V_{DS}=16V, V_{GS}=0V, T_J=55^{\circ}\text{C}$ | --- | --- | 5 | μA |
| Gate-Source Leakage Current | I_{GSS} | $V_{GS}=\pm 8V, V_{DS}=0V$ | --- | --- | ± 10 | μA |
| Forward Transconductance | g_{fs} | $V_{DS}=10V, I_D=4A$ | --- | 42 | --- | S |
| Total Gate Charge | Q_g | $V_{DS}=15V, V_{GS}=4.5V, I_D=3A$ | --- | 10.6 | --- | nC |
| Gate-Source Charge | Q_{gs} | | --- | 2.2 | --- | |
| Gate-Drain Charge | Q_{gd} | | --- | 4.1 | --- | |
| Turn-On Delay Time | $T_{d(on)}$ | $V_{DD}=15V, V_{GS}=4.5V, R_G=6\Omega, I_D=3A$ | --- | 7 | --- | ns |
| Rise Time | T_r | | --- | 36 | --- | |
| Turn-Off Delay Time | $T_{d(off)}$ | | --- | 46.5 | --- | |
| Fall Time | T_f | | --- | 15 | --- | |
| Input Capacitance | C_{iss} | $V_{DS}=10V, V_{GS}=0V, f=1\text{MHz}$ | --- | 735 | --- | pF |
| Output Capacitance | C_{oss} | | --- | 83 | --- | |
| Reverse Transfer Capacitance | C_{rss} | | --- | 81 | --- | |

Drain-Source Diode Characteristics

| Parameter | Symbol | Conditions | Min | Typ | Max | Unit |
|--|----------|---|-----|-----|-----|------|
| Continuous Source Current ¹ | I_S | $V_G=V_D=0V, \text{Force Current}$ | --- | --- | 8 | A |
| Pulsed Source Current ² | I_{SM} | | --- | --- | 50 | A |
| Diode Forward Voltage ² | V_{SD} | $V_{GS}=0V, I_S=8A, T_J=25^{\circ}\text{C}$ | --- | --- | 1.2 | V |

Note:

- The data tested by surface mounted on a 1 inch² FR-4 board with 20Z copper, $t \leq 10s$.
- The data tested by pulsed, pulse width $\leq 10\mu s$, duty cycle $\leq 1\%$

Typical Characteristics

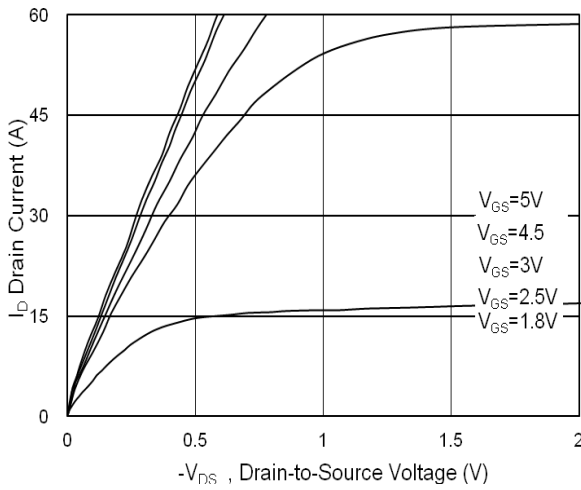


Fig.1 Typical Output Characteristics

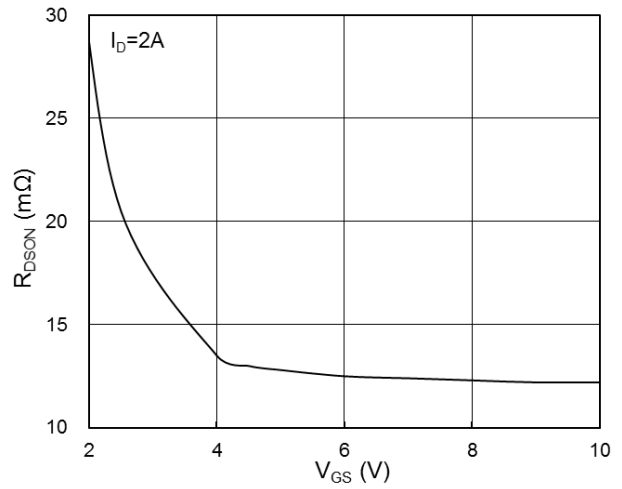


Fig.2 On-Resistance vs. G-S Voltage

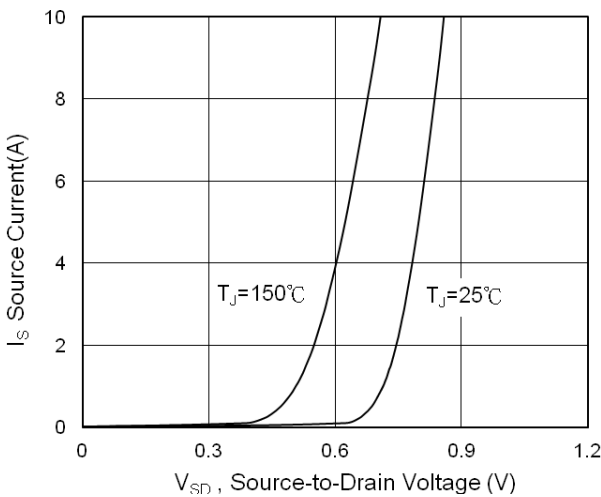


Fig.3 Forward Characteristics of Reverse

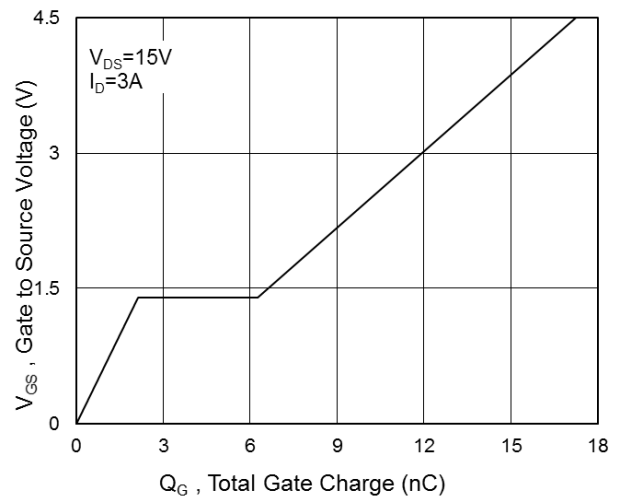


Fig.4 Gate-Charge Characteristics

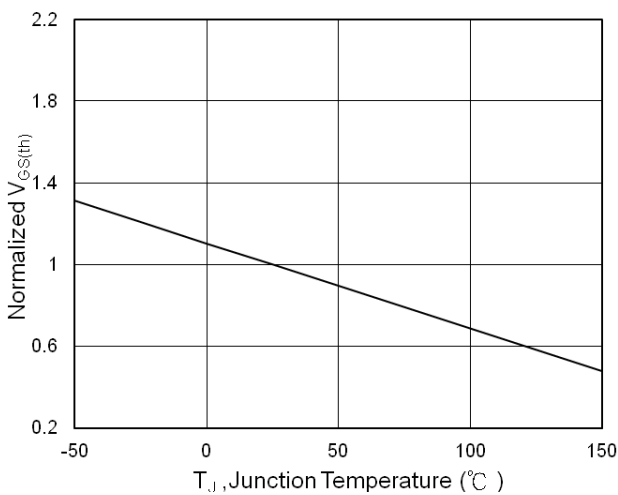


Fig.5 $V_{GS(th)}$ vs. T_J

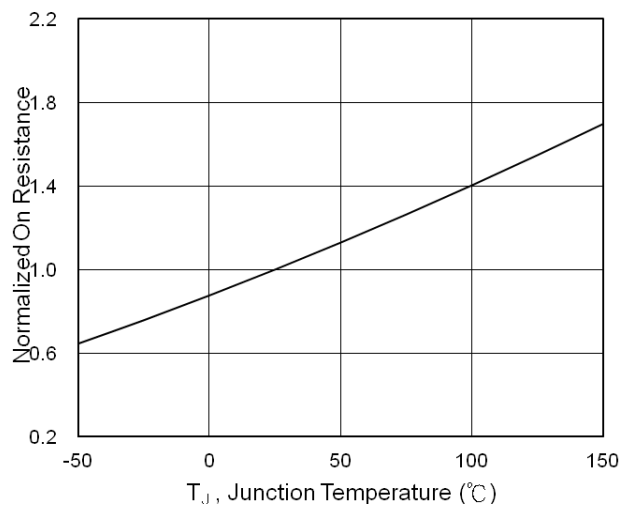


Fig.6 Normalized $R_{DS(on)}$ vs. T_J

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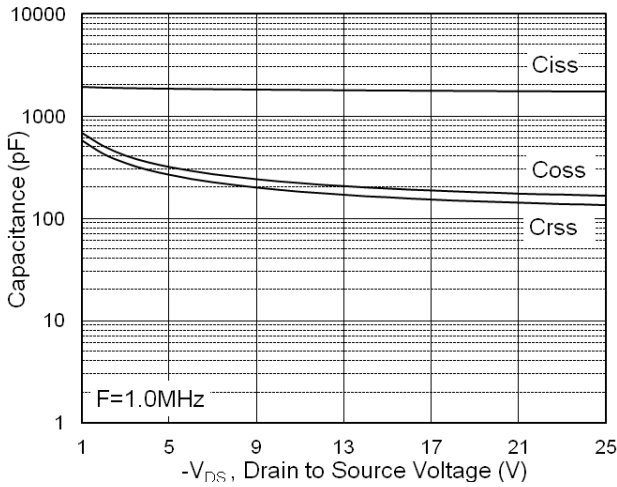


Fig.7 Capacitance

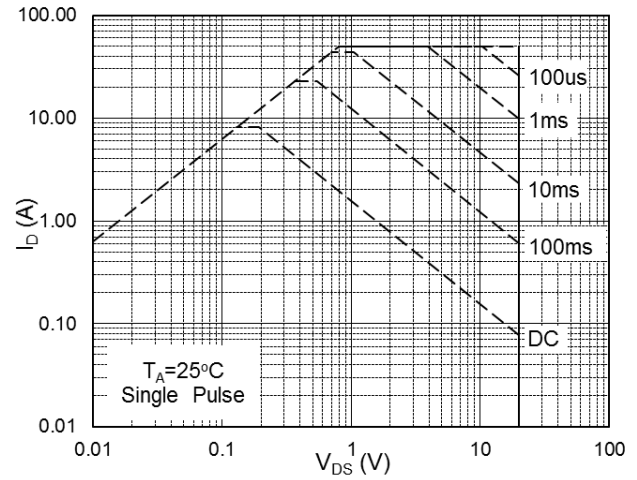


Fig.8 Safe Operating Area

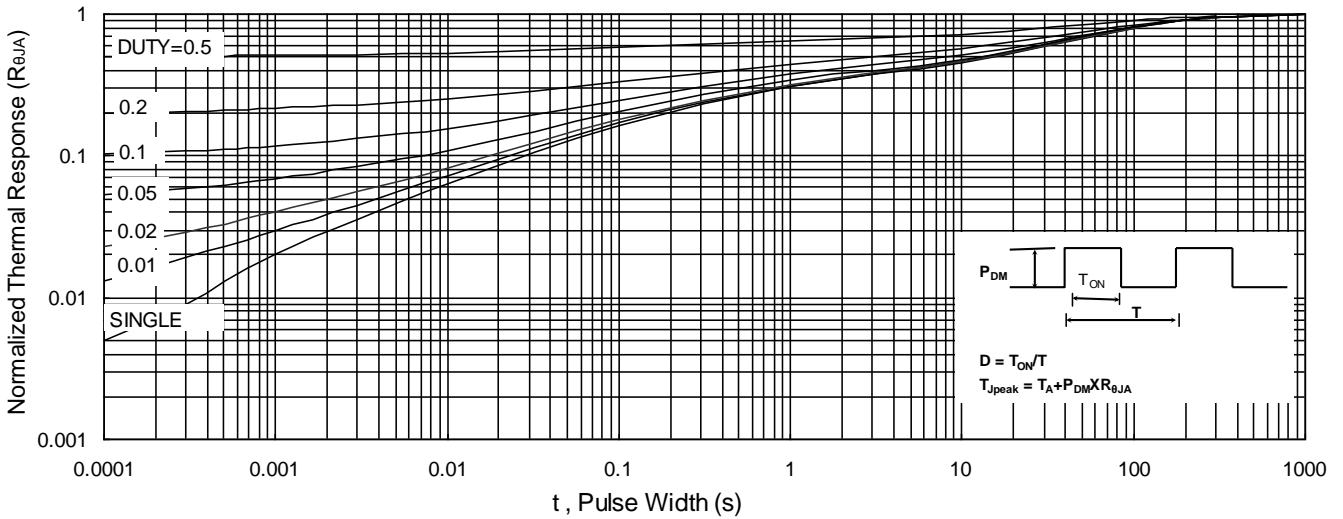


Fig.9 Normalized Maximum Transient Thermal Impedance

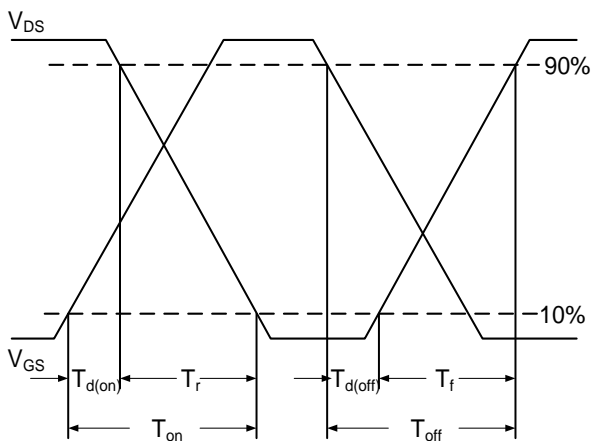


Fig.10 Switching Time Waveform

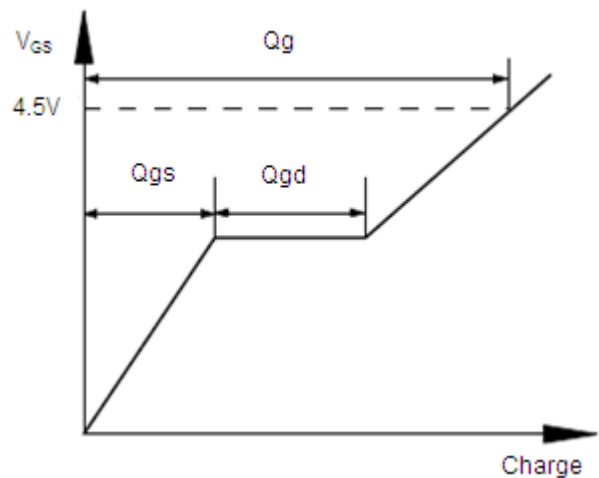
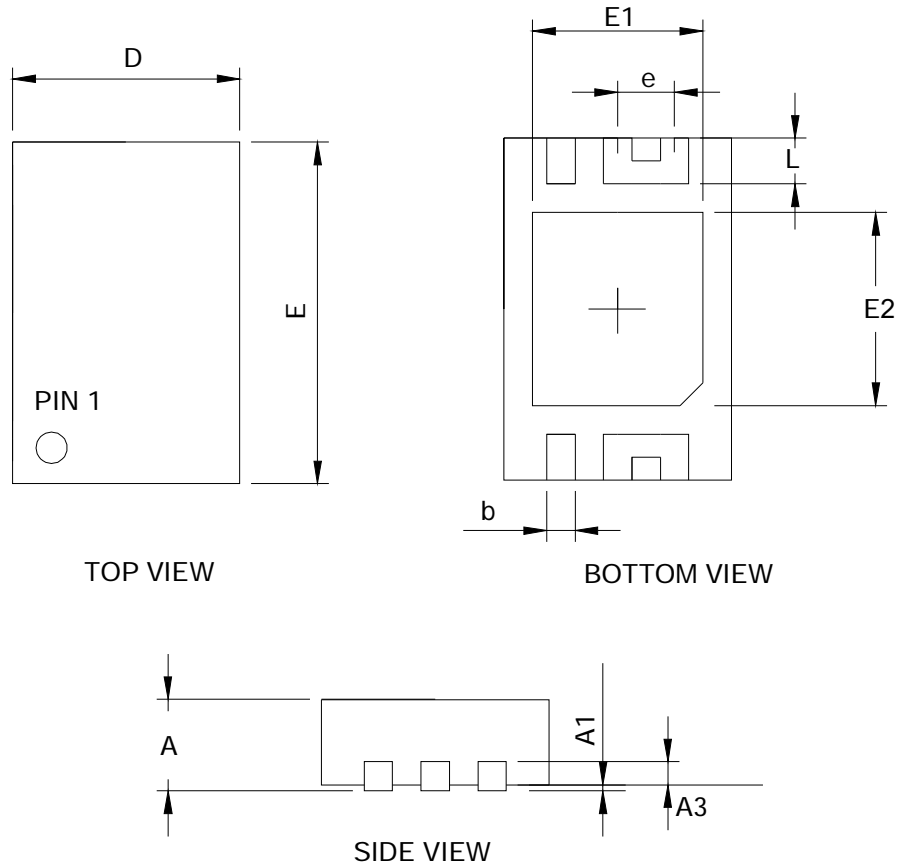


Fig.11 Gate Charge Waveform

DFN2X3-6L Package Outline Dimensions



DIMENSIONS (unit : mm)

| Symbol | Min | Typ | Max | Symbol | Min | Typ | Max |
|-----------|-------|-------|-------|-----------|---------|------|------|
| A | 0.70 | 0.80 | 0.85 | A1 | 0.00 | -- | 0.05 |
| A3 | 0.195 | 0.200 | 0.211 | D | 1.95 | 2.00 | 2.05 |
| E | 2.95 | 3.00 | 3.05 | E1 | 1.45 | 1.50 | 1.55 |
| E2 | 1.65 | 1.70 | 1.75 | b | 0.20 | 0.25 | 0.30 |
| L | 0.35 | 0.40 | 0.45 | e | 0.50BSC | | |