

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

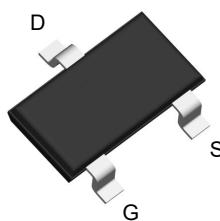
Product Summary



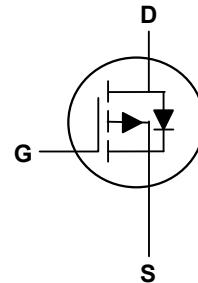
V_{DS}	-40	V
I_D	-2.9	A
$R_{DS(ON)}$ (at $V_{GS}=-10V$)	68	mΩ
$R_{DS(ON)}$ (at $V_{GS}=-4.5V$)	100	mΩ

Applications

- High Frequency Point-of-Load, Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch



SOT23 Top View



Absolute Maximum Ratings($T_A=25^\circ C$, unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	-40	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	$I_D @ T_A=25^\circ C$	-2.9	A
Continuous Drain Current	$I_D @ T_A=100^\circ C$	-2.32	A
Pulsed Drain Current ²	I_{DM}	-11.6	A
Total Power Dissipation ($T_A=25^\circ C$)	P_D	1	W
Power Dissipation – Derate above 25°C		8	mW/°C
Storage Temperature Range	T_{STG}	-55 to 150	°C
Operating Junction Temperature Range	T_J	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	---	125	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}$, $I_D=-250\mu\text{A}$	-40	---	---	V
Static Drain-Source On-Resistance ²	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}$, $I_D=-2\text{A}$	---	55	68	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_D=-1\text{A}$	---	75	100	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = -250\mu\text{A}$	-1.0	---	-2.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=-40\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	-1	uA
		$V_{\text{DS}}=-32\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	-10	uA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
Forward Transconductance	g_{fs}	$V_{\text{DS}}=-10\text{V}$, $I_D=-1\text{A}$	---	3	---	S
Total Gate Charge	Q_g	$V_{\text{DS}}=-32\text{V}$, $V_{\text{GS}}=-10\text{V}$, $I_D=-2\text{A}$	---	6.4	13	nC
Gate-Source Charge	Q_{gs}		---	0.5	2	
Gate-Drain Charge	Q_{gd}		---	2.7	6	
Turn-On Delay Time	$T_{\text{d}(\text{on})}$	$V_{\text{DD}}=-20\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_G=6\Omega$, $I_D=-1\text{A}$	---	12	24	ns
Rise Time	T_r		---	9	20	
Turn-Off Delay Time	$T_{\text{d}(\text{off})}$		---	45	90	
Fall Time	T_f		---	5	10	
Input Capacitance	C_{iss}	$V_{\text{DS}}=-25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	600	1200	pF
Output Capacitance	C_{oss}		---	60	120	
Reverse Transfer Capacitance	C_{rss}		---	43	80	

Drain-Source Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current ¹	I_s	$V_G=V_D=0\text{V}$, Force Current	---	---	-2.9	A
Pulsed Source Current ²	I_{SM}		---	---	-5.8	A
Diode Forward Voltage ²	V_{SD}	$V_{\text{GS}}=0\text{V}$, $I_s=-1\text{A}$, $T_J=25^\circ\text{C}$	---	---	-1	V

Note:

1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.

2.The data tested by pulsed , pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$

3.The power dissipation is limited by 150°C junction temperature

Typical Characteristics

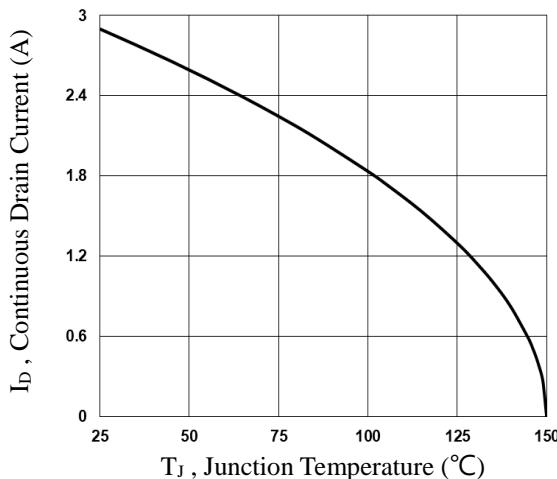


Fig.1 Continuous Drain Current vs. T_J

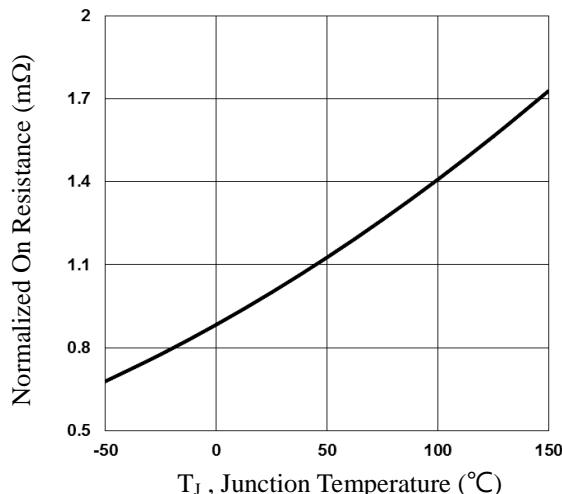


Fig.2 Normalized $R_{DS(on)}$ vs. T_J

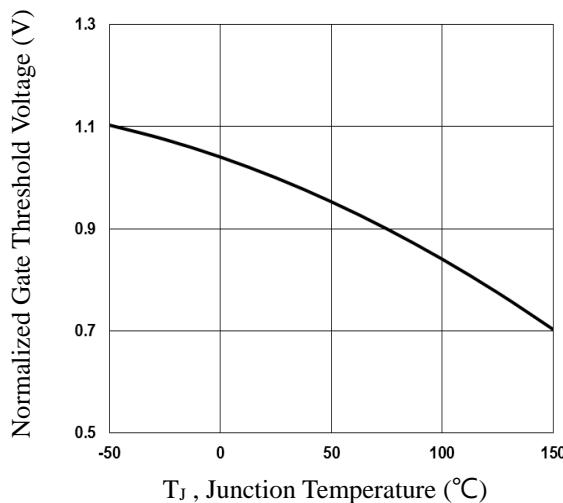


Fig.3 Normalized V_{th} vs. T_J

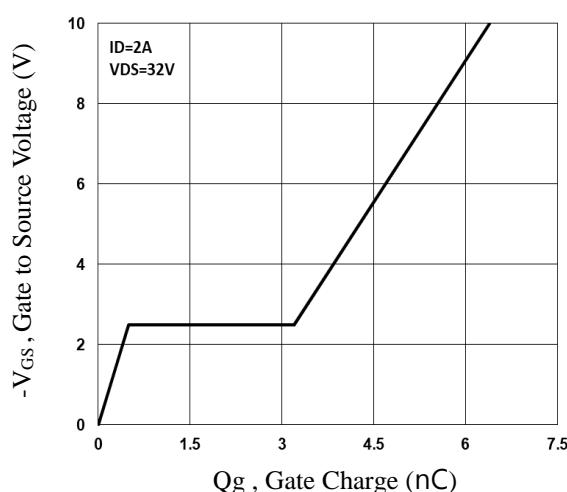


Fig.4 Gate Charge Waveform

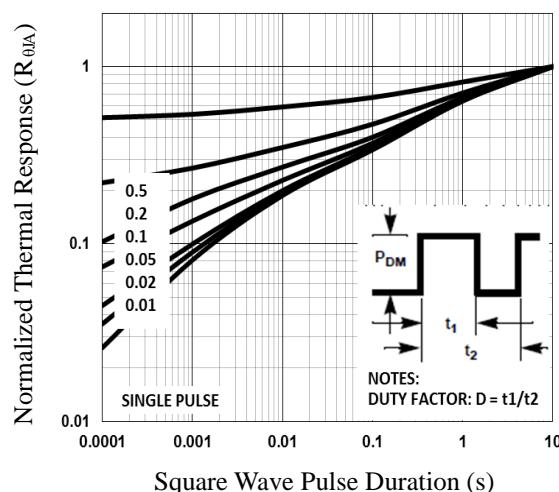


Fig.5 Normalized Transient Impedance

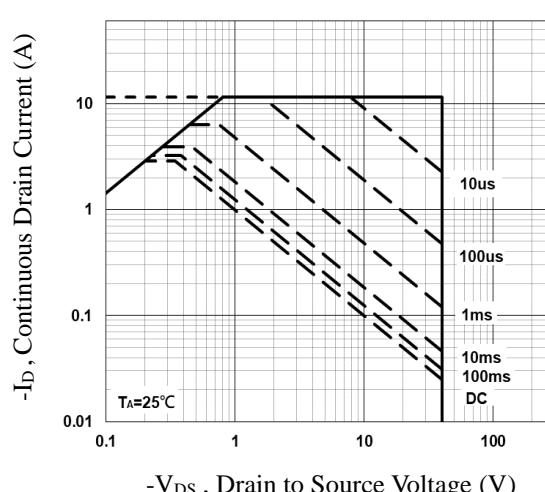


Fig.6 Maximum Safe Operation Area

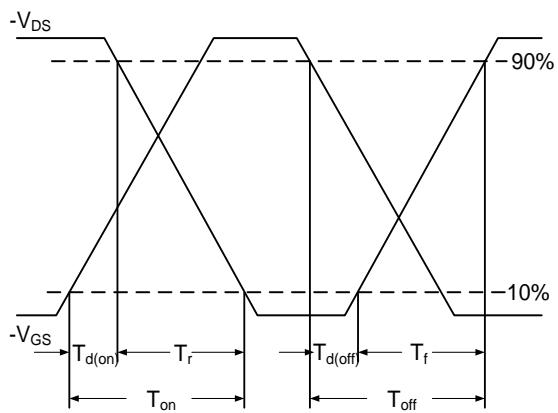


Fig.7 Switching Time Waveform

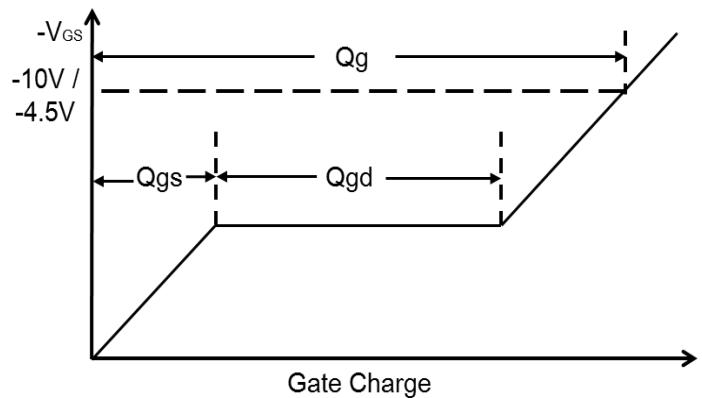
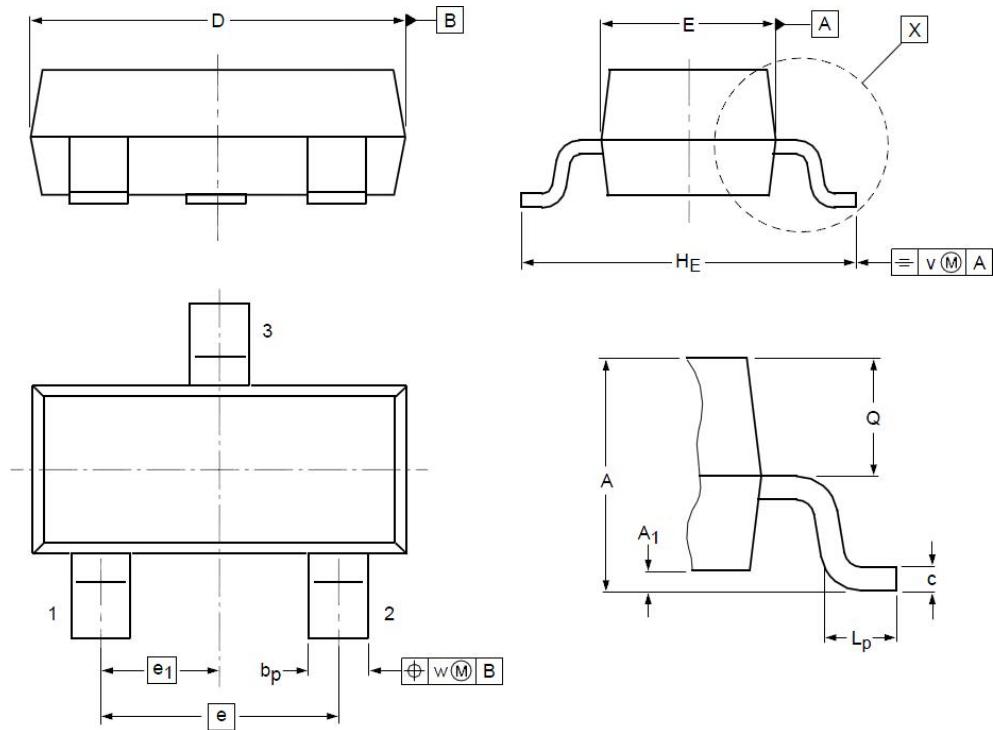


Fig.8 Gate Charge Waveform

SOT23 Package Outline Dimensions



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	0.90	1.05	1.20	e₁	--	0.95	--
A₁	0.01	0.05	0.10	H_E	2.10	2.40	2.50
b_p	0.38	0.42	0.48	L_P	0.40	0.50	0.60
c	0.09	0.13	0.15	Q	0.45	0.49	0.55
D	2.80	2.92	3.00	V	--	0.20	--
E	1.20	1.33	1.40	W	--	0.10	--
e	--	1.90	--				