AT80R850K

800V Super Junction Power MOSFET

Features

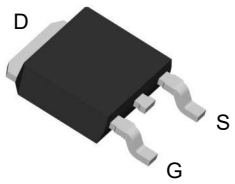
- Low drain-source on-resistance:$R_{DS(ON)}$=0.74Ω(typ)
- Easy to control gate switching
- Enhancement mode: V_{th} = 2.0 to 4.0V
- 100% avalanche tested
- Built-in ESD Diode
- RoHS compliant

Key Performance Parameters

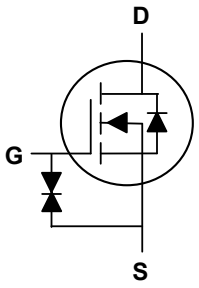
Parameter	Value	Unit
V_{DS} @ $T_{j,max}$	800	V
$R_{DS(ON),max}$	850	mΩ
I_D	6.6	A
$Q_{g,typ}$	13.7	nC
I_{DM}	19.8	A

Applications

- Switch Mode Power Supply (SMPS)
- TV power & LED Lighting Power
- AC to DC Converters
- Telecom



TO-252 Top View



Absolute Maximum Ratings(T_C=25℃, unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	800	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current[1]	I_D@T_C=25℃	6.6	A
Continuous Drain Current[1]	I_D@T_C=100℃	4.2	A
Pulsed Drain Current[2]	I_{DM}	19.8	A
Single Pulse Avalanche Energy[4]	EAS	84	mJ
Avalanche Current	I_{AS}	1.4	A
MOSFET dv/dt ruggedness, V_{DS} = 0...400V	dv/dt	50	V/ns
Reverse diode dv/dt[3] V_{DS}=0...400V, I_{DS}≤I_D		15	
Total Power Dissipation （T_C=25℃）	P_D	66	W
Storage Temperature Range	T_{STG}	-55 to 150	℃
Operating Junction Temperature Range	T_J	-55 to 150	℃

Thermal Characteristics

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-Ambient (Max)	$R_{\theta JA}$	62.5	℃/W
Thermal Resistance Junction-Case (Max)	$R_{\theta JC}$	1.9	℃/W

Electrical Characteristics (T_J=25℃, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V , I_D=1mA	800	---	---	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS}=10V , I_D=1.6A	---	740	850	mΩ
Gate Threshold Voltage	$V_{GS(th)}$	V_{GS}=V_{DS} , I_D =220uA	2.0	---	4.0	V
Drain-Source Leakage Current	I_{DSS}	V_{DS}=800V , V_{GS}=0V , T_J=25℃	---	---	1	uA
		V_{DS}=800V, V_{GS}=0V, T_J=150℃	---	---	100	uA
Gate-Source Leakage Current	I_{GSS}	V_{GS}=±20V , V_{DS}=0V	---	---	±1	uA
Total Gate Charge	Q_g	V_{DD}=640V , V_{GS}=10V , I_D=2.8A	---	13.7	---	nC
Gate-Source Charge	Q_{gs}		---	2.9	---	
Gate-Drain Charge	Q_{gd}		---	4.2	---	
Turn-On Delay Time	$T_{d(on)}$	V_{DD}=400V , R_G=25Ω, I_D=2.8A	---	23	---	ns
Rise Time	T_r		---	18	---	
Turn-Off Delay Time	$T_{d(off)}$		---	74	---	
Fall Time	T_f		---	17	---	
Input Capacitance	C_{iss}	V_{DS}=500V , V_{GS}=0V , f=1MHz	---	635	---	pF
Output Capacitance	C_{oss}		---	14.6	---	
Reverse Transfer Capacitance	C_{rss}		---	2.5	---	

Drain-Source Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current	I_S	T_C=25℃	---	---	6.6	A
Pulsed Source Current	I_{SM}		---	---	19.8	A
Diode Forward Voltage	V_{SD}	V_G=0V , I_S=2.8A ,T_J=25℃	---	---	1.3	V
Reverse Recovery Time	t_{rr}	V_R=400V, I_F=2.8A, di_F/dt=100A/μs	---	170	---	ns
Reverse Recovery Charge	Q_{rr}		---	1.1	---	uC

Note:

1. Limited by $T_{j,max.}$ Maximum Duty Cycle D = 0.50
2. Pulse width t_p limited by $T_{j,max}$
3. Identical low side and high side switch with identical R_G
4. V_{DD}=50V,R_G=25Ω,I_{AS}=1.4A

Typical Characteristics

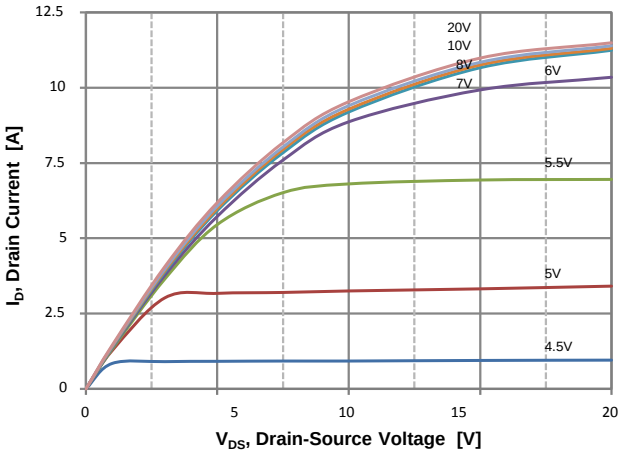


Figure 1. On Region Characteristics

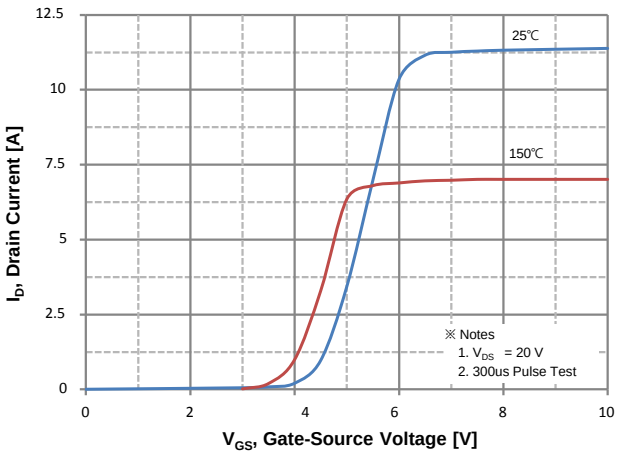


Figure 2. Transfer Characteristics

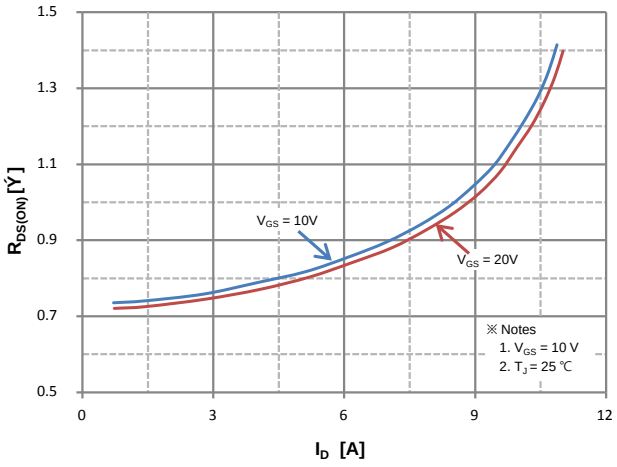


Figure 3. On Resistance Variation vs Drain Current and Gate Voltage

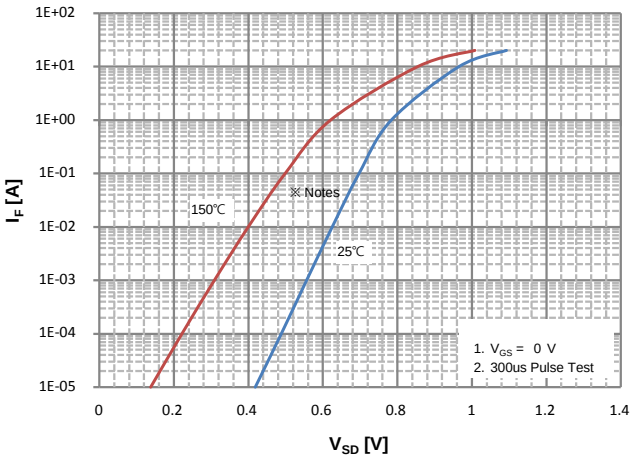


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

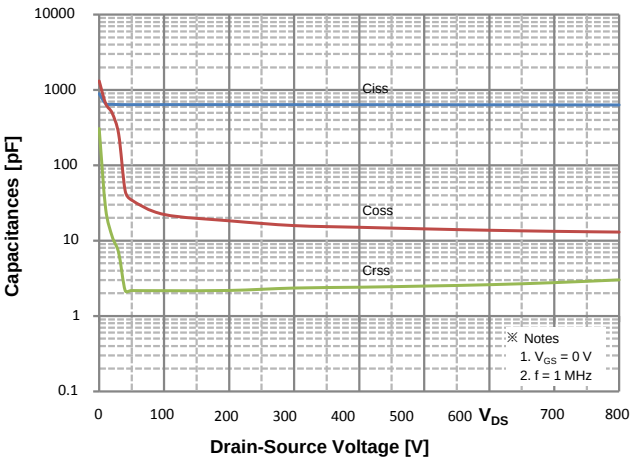


Figure 5. Capacitance Characteristics

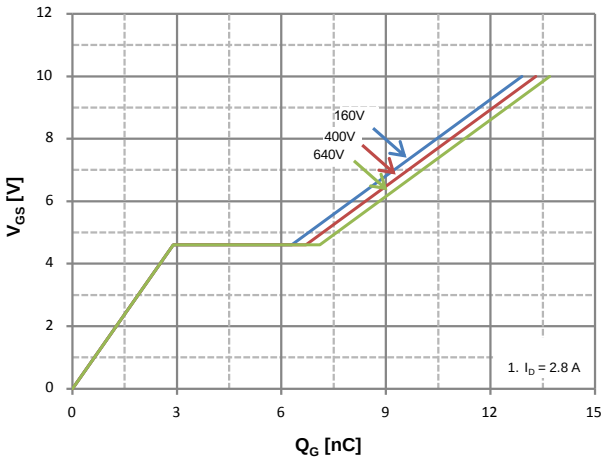


Figure 6. Gate Charge Characteristics

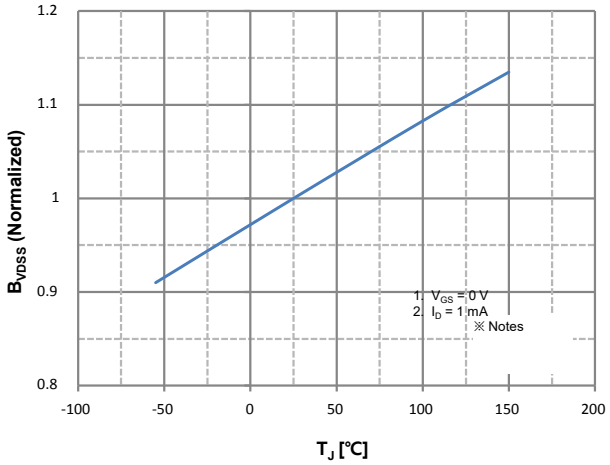


Figure 7. Breakdown Voltage Variation vs. Temperature

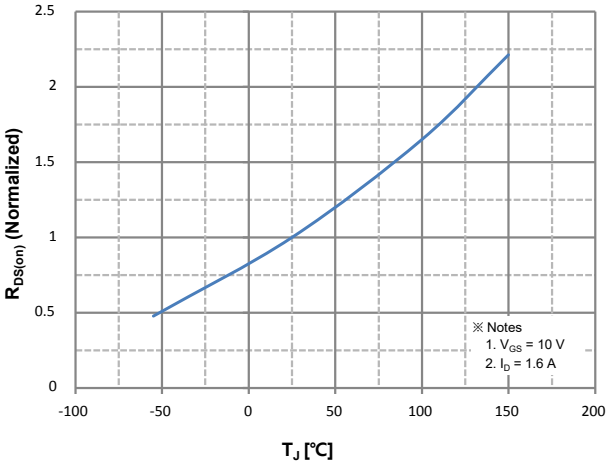


Figure 8. On-Resistance Variation vs. Temperature

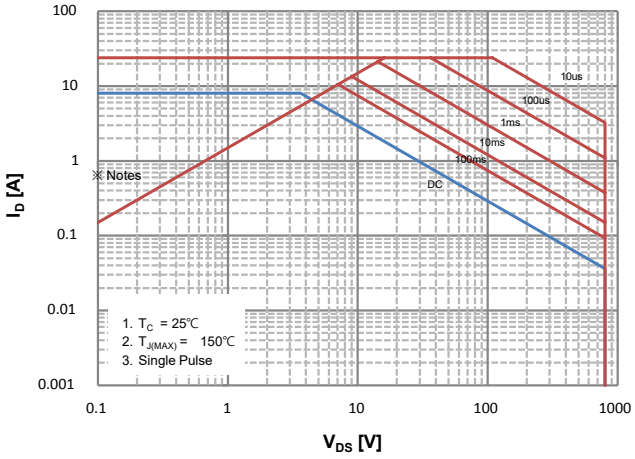


Figure 9. Maximum Safe Operating Area

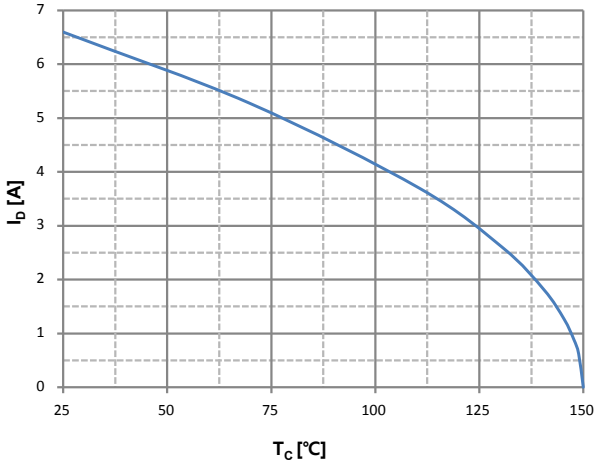


Figure 10. Maximum Drain Current vs. Case Temperature

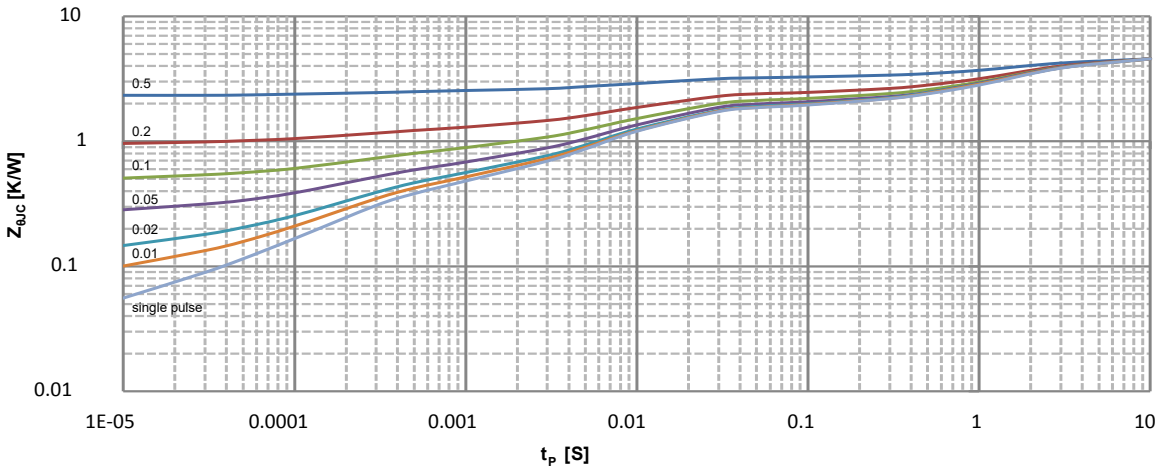


Figure 11. Transient Thermal Response Curve

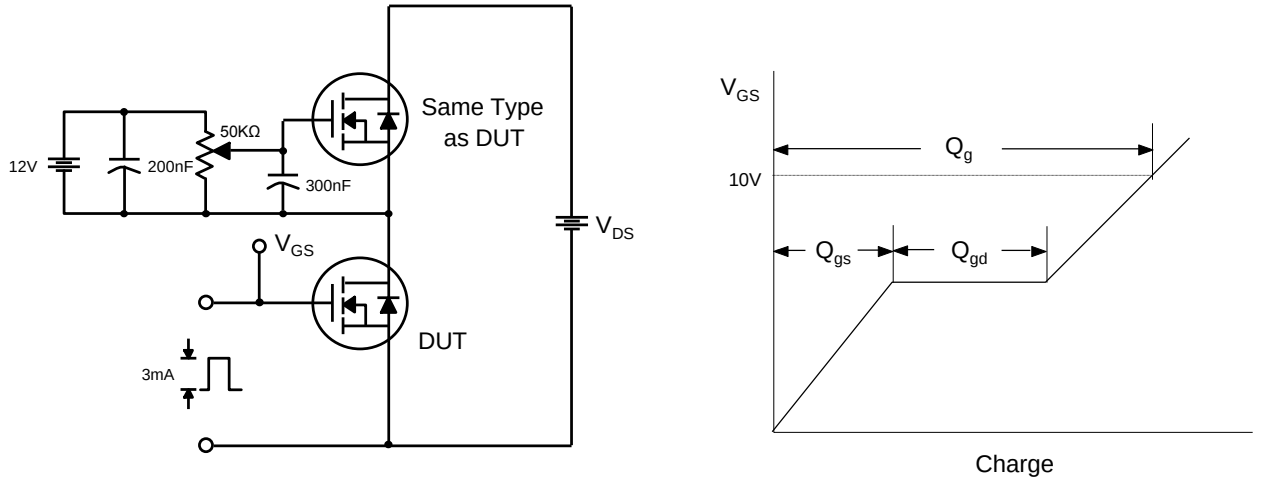


Fig 12. Gate Charge Test Circuit & Waveform

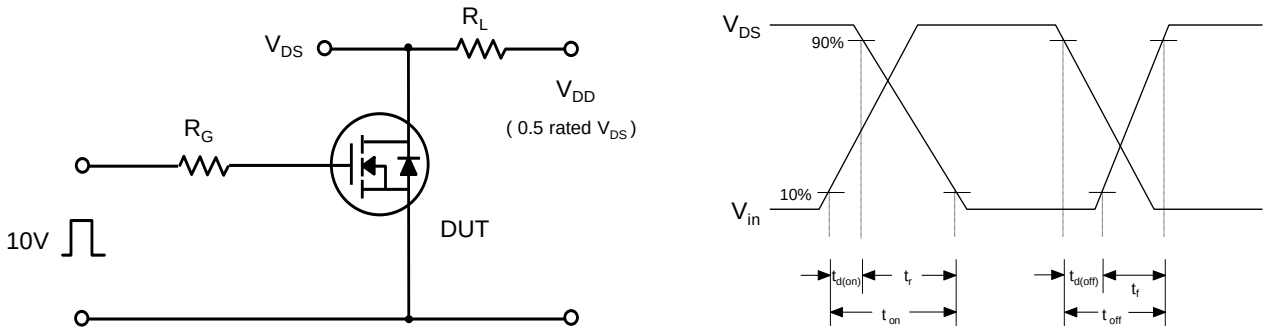


Fig 13. Resistive Switching Test Circuit & Waveforms

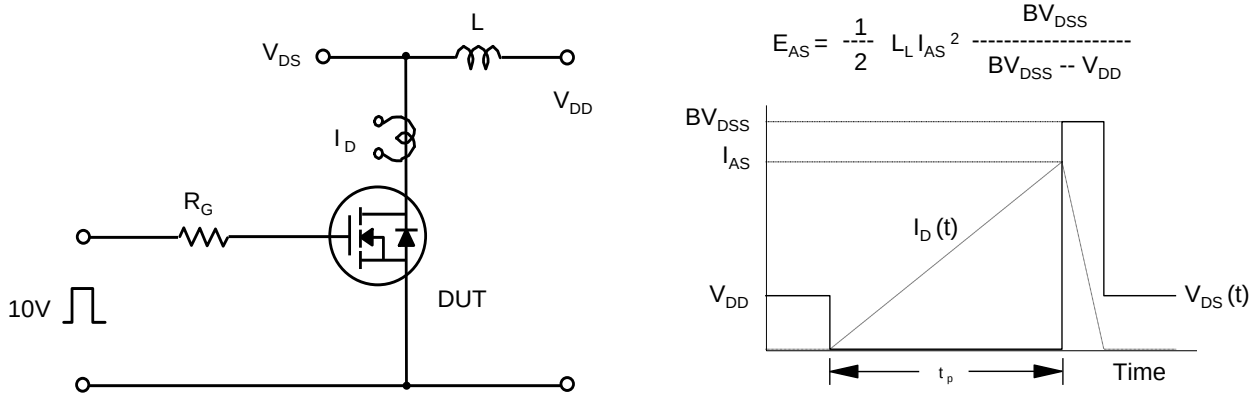
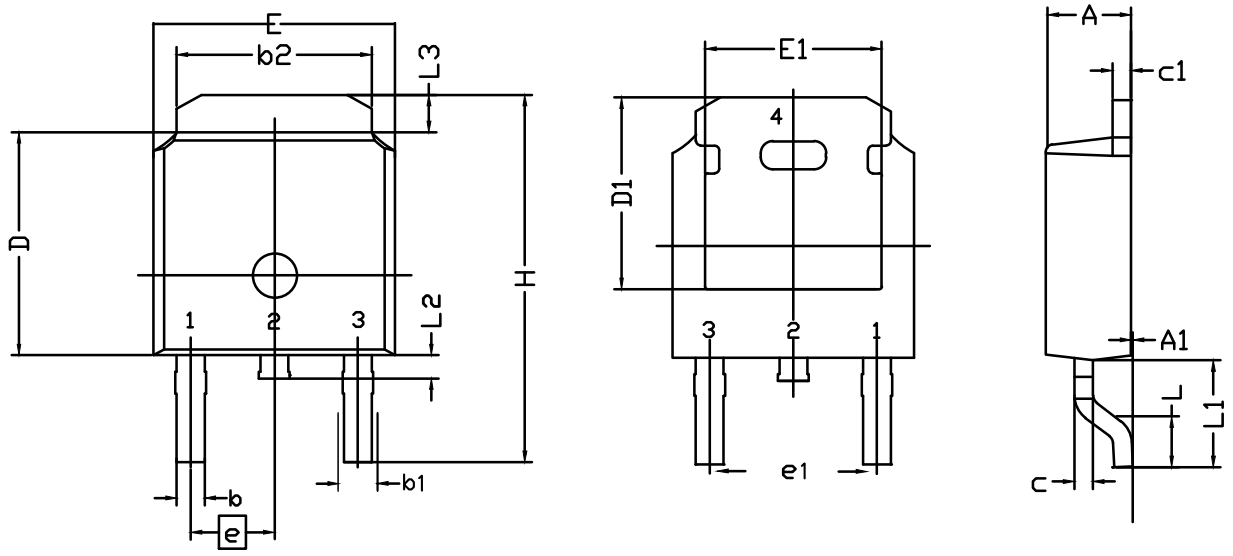


Fig 14. Unclamped Inductive Switching Test Circuit & Waveforms

TO-252 Package Outline Dimensions



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	2.20	2.30	2.38	E	6.40	6.60	6.731
A_1	0.00	0.10	0.20	E_1	4.40	--	--
b	0.64	0.76	0.89	e	2.286 BSC		
b_1	0.77	0.85	1.14	e_1	4.572 BSC		
b_2	5.00	5.33	5.46	H	9.40	10.00	10.40
c	0.458	0.508	0.610	L	1.40	1.52	1.77
C_1	0.458	0.508	0.620	L_1	--	2.743	--
D	5.98	6.10	6.223	L_2	0.60	0.80	1.01
D_1	5.20	5.25	5.38	L_3	0.90	1.06	1.25