

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

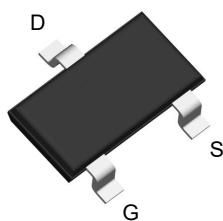
Product Summary



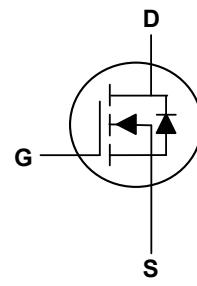
V_{DS}	30	V
I_D	5.8	A
$R_{DS(ON)}$ (at $V_{GS}=10V$)	30	mΩ
$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	40	mΩ

Applications

- High Frequency Point-of-Load,Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch



SOT23-3L Top View



Absolute Maximum Ratings($T_A=25^\circ C$, unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹	I_D	5.8	A
Pulsed Drain Current ²	I_{DM}	20	A
Total Power Dissipation ³	P_D	1.4	W
Storage Temperature Range	T_{STG}	-55 to 150	°C
Operating Junction Temperature Range	T_J	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	---	89	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	30	---	---	V
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}$, $I_D=5\text{A}$	---	23	30	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=4\text{A}$	---	28	40	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	1.2	1.6	2.4	V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
Forward Transconductance	g_{fs}	$V_{\text{DS}}=5\text{V}$, $I_D=5\text{A}$	---	15	---	S
Total Gate Charge	Q_g	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=5\text{A}$	---	12.6	---	nC
Gate-Source Charge	Q_{gs}		---	1.9	---	
Gate-Drain Charge	Q_{gd}		---	2.6	---	
Turn-On Delay Time	$T_{\text{d(on)}}$	$V_{\text{DD}}=15\text{V}$, $R_G=3\Omega$, $V_{\text{GS}}=10\text{V}$, $R_L=3\Omega$	---	5	---	ns
Rise Time	T_r		---	3	---	
Turn-Off Delay Time	$T_{\text{d(off)}}$		---	15	---	
Fall Time	T_f		---	3.5	---	
Input Capacitance	C_{iss}	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	485	---	pF
Output Capacitance	C_{oss}		---	65	---	
Reverse Transfer Capacitance	C_{rss}		---	54	---	

Drain-Source Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current ²	I_s		---	---	5.8	A
Diode Forward Voltage ¹	V_{SD}	$V_{\text{GS}}=0\text{V}$, $I_s=5\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V

Note:

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature

Typical Characteristics

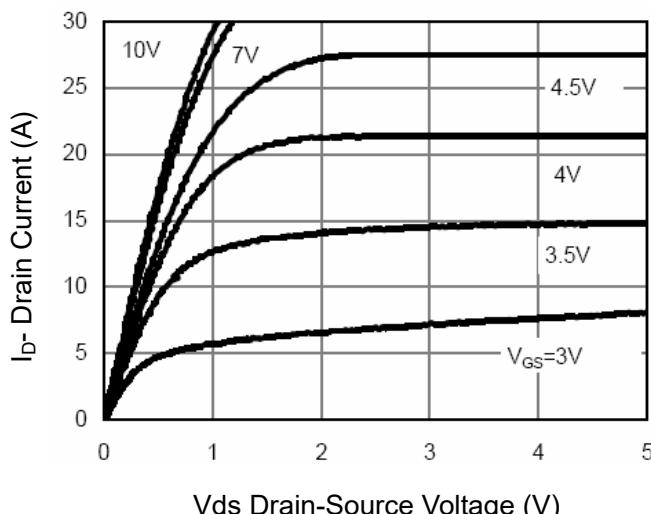


Figure 1 Output Characteristics

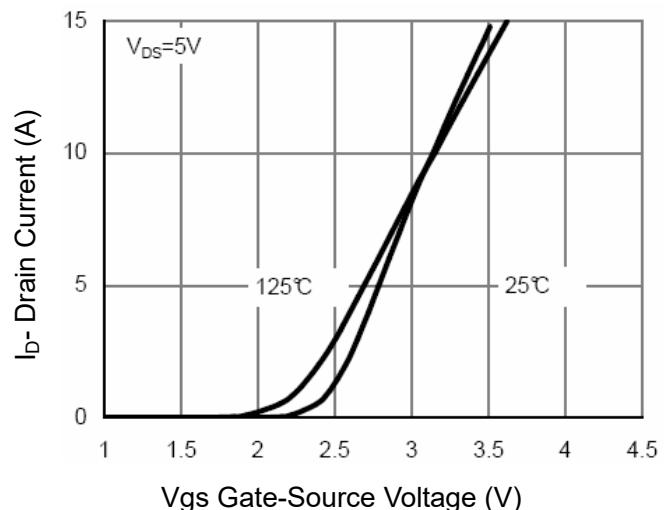


Figure 2 Transfer Characteristics

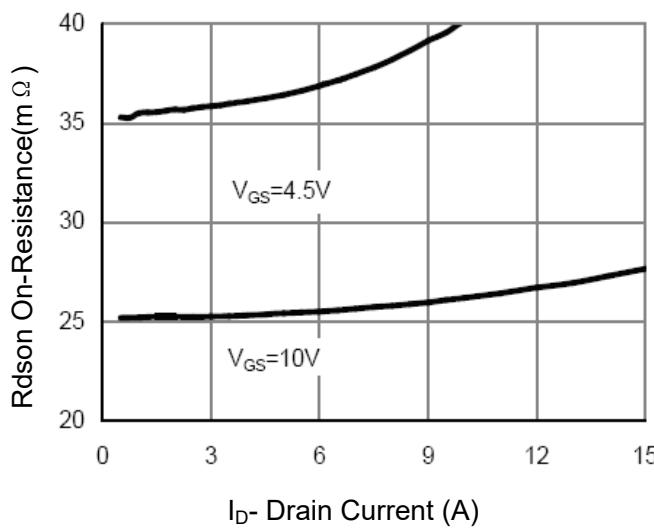


Figure 3 Drain-Source On-Resistance

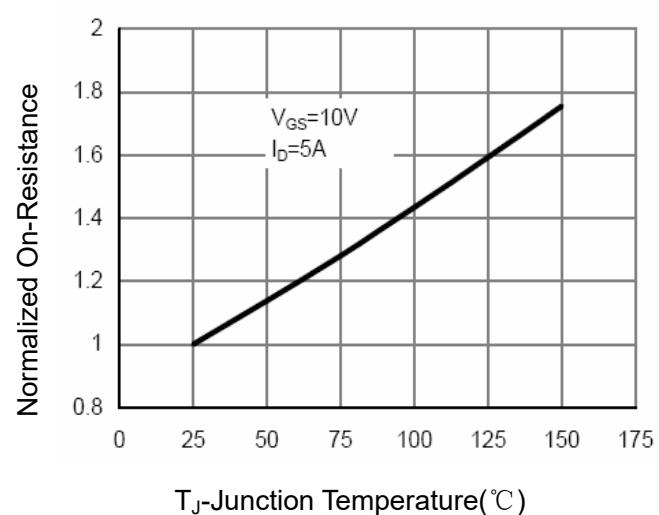


Figure 4 Drain-Source On-Resistance

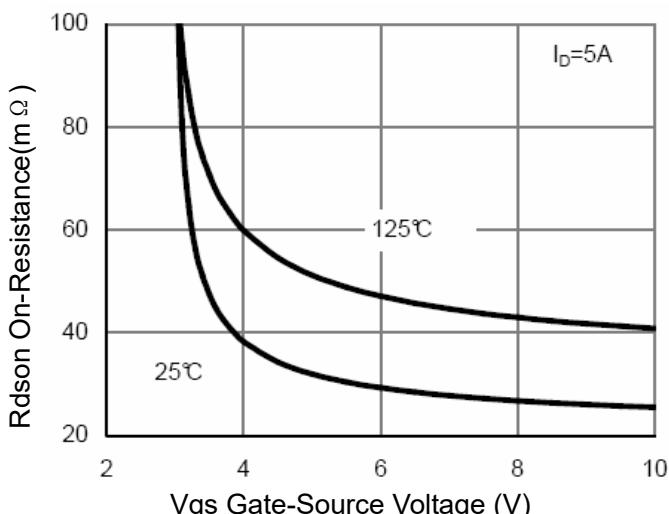


Figure 5 Rdson vs Vgs

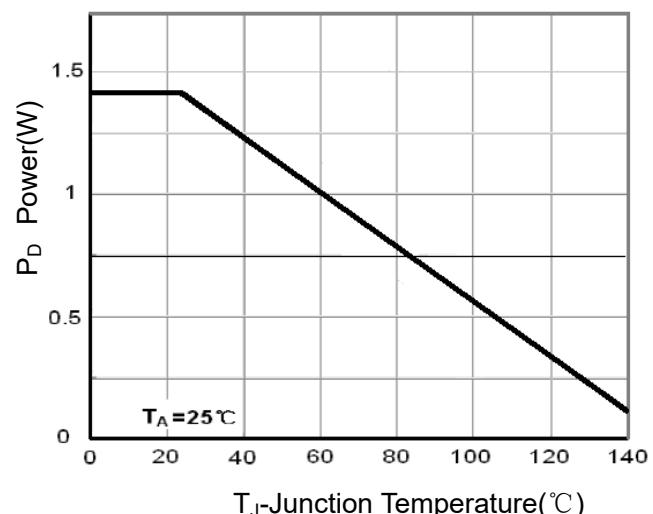
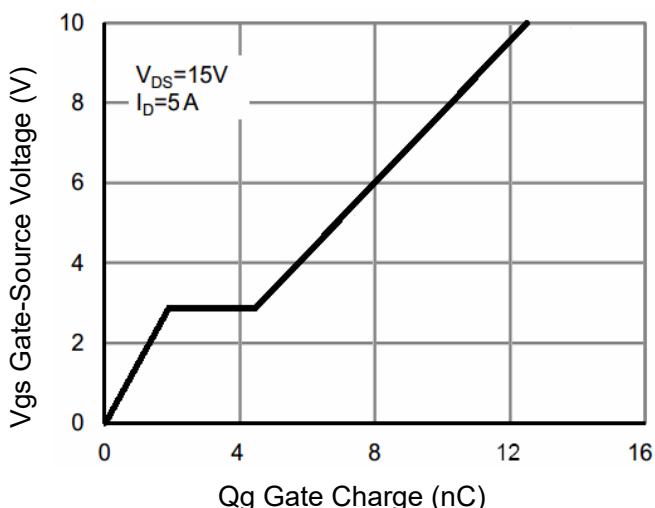
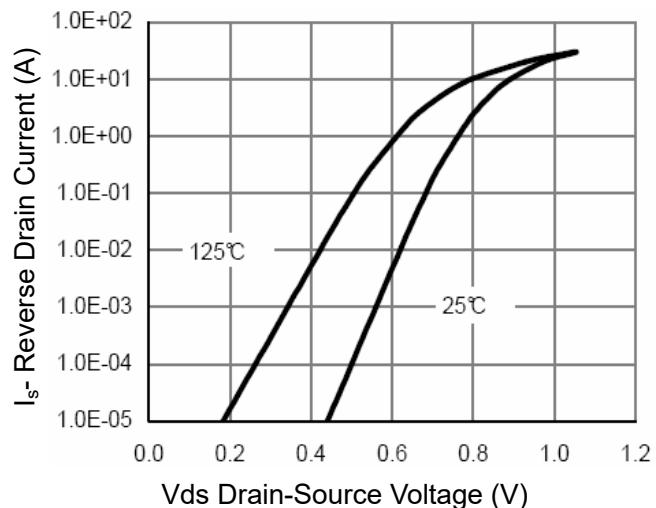
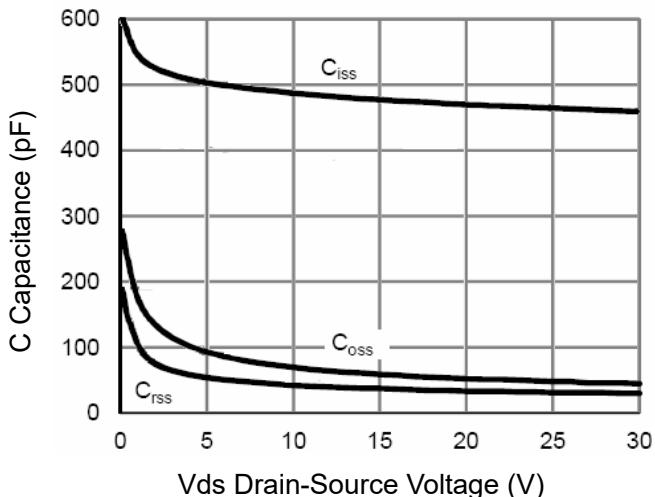
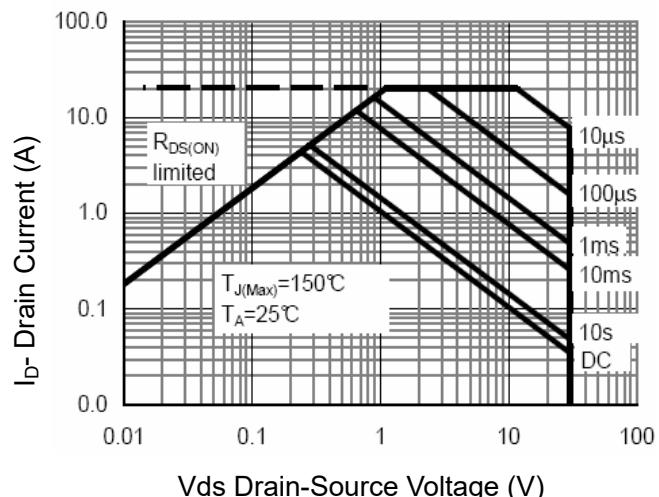
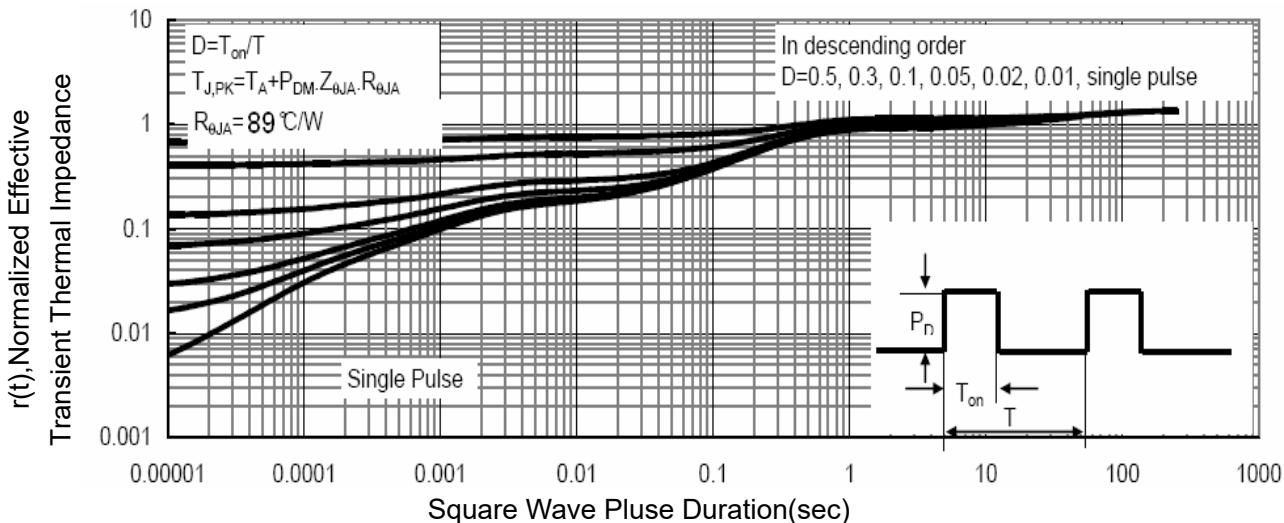
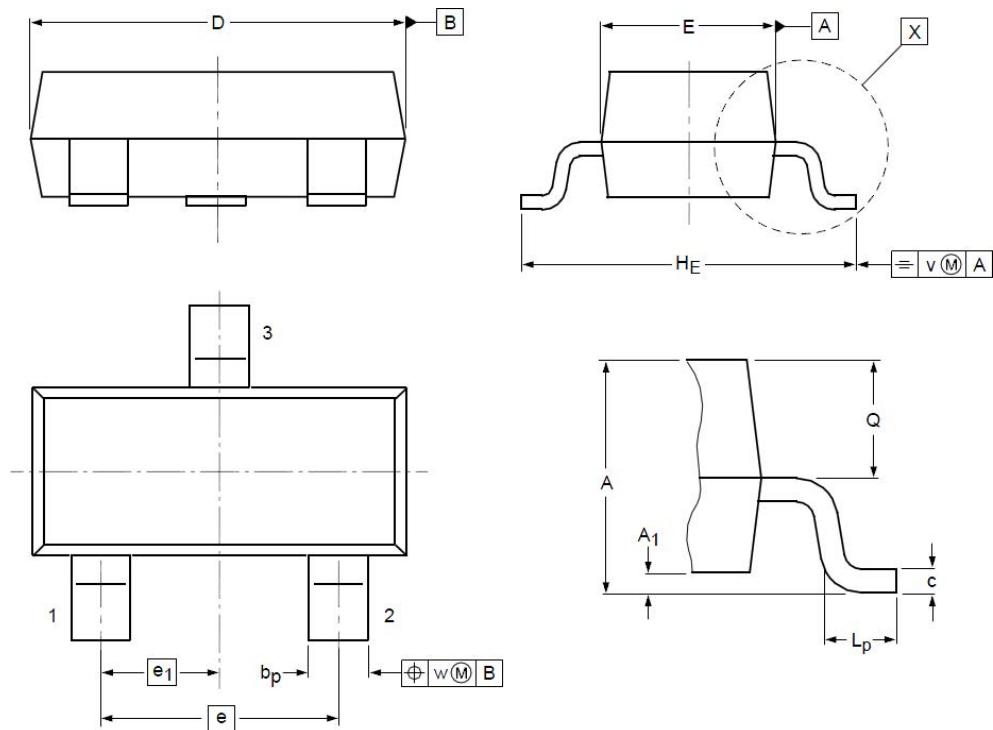


Figure 6 Power Dissipation


Figure 7 Gate Charge

Figure 8 Source- Drain Diode Forward

Figure 9 Capacitance vs Vds

Figure 10 Safe Operation Area

Figure 11 Normalized Maximum Transient Thermal Impedance

SOT23-3L Package Outline Dimensions



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	0.90	1.07	1.25	e₁	--	0.95	--
A₁	0.01	0.05	0.10	H_E	2.50	2.80	3.00
b_p	0.30	0.40	0.50	L_P	0.30	0.45	0.60
c	0.10	0.15	0.20	Q	0.23	0.28	0.33
D	2.70	2.90	3.10	V	--	0.20	--
E	1.40	1.55	1.75	W	--	0.20	--
e	--	1.90	--				