

Features

- Advanced Shield Gate Trench technology
- Super Low Gate Charge
- High-speed switching
- 100% EAS Guaranteed
- Green Device Available

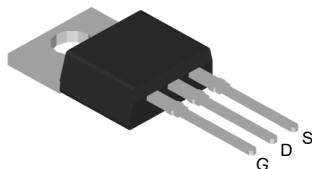
Product Summary



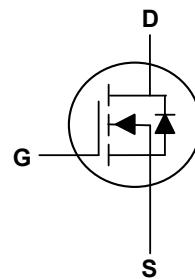
V_{DS}	60	V
I_D	72	A
$R_{DS(ON)}$ (at $V_{GS}=10V$)	3	mΩ
$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	3.7	mΩ

Applications

- High Frequency Point-of-Load,Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch



TO-220 Top View



Absolute Maximum Ratings($T_c=25^\circ C$, unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹	I_D	72	A
Pulsed Drain Current ²	I_{DM}	290	A
Single Pulse Avalanche Energy ³	EAS	184	mJ
Avalanche Current	I_{AS}	35	A
Total Power Dissipation ⁴	P_D	75	W
Storage Temperature Range	T_{STG}	-55 to 150	°C
Operating Junction Temperature Range	T_J	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	---	62	°C/W
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	---	2	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	60	---	---	V
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}$, $I_D=20\text{A}$	---	2.6	3	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=20\text{A}$	---	3.2	3.7	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	1.0	1.7	2.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
Total Gate Charge	Q_g	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=20\text{A}$	---	42	---	nC
Gate-Source Charge	Q_{gs}		---	10	---	
Gate-Drain Charge	Q_{gd}		---	12	---	
Gate plateau voltage	V_{plateau}		---	1.9	---	V
Turn-On Delay Time	$T_{\text{d(on)}}$	$V_{\text{DD}}=30\text{V}$, $I_D=40\text{A}$, $V_{\text{GS}}=10\text{V}$, $R_G=3\Omega$	---	8.7	---	ns
Rise Time	T_r		---	5.8	---	
Turn-Off Delay Time	$T_{\text{d(off)}}$		---	28	---	
Fall Time	T_f		---	6.4	---	
Input Capacitance	C_{iss}	$V_{\text{DS}}=25\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	4200	---	pF
Output Capacitance	C_{oss}		---	1080	---	
Reverse Transfer Capacitance	C_{rss}		---	41	---	

Drain-Source Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current ^{1,5}	I_s	$T_c=25^\circ\text{C}$	---	---	72	A
Pulsed Source Current ²	I_{SM}		---	---	290	A
Diode Forward Voltage ²	V_{SD}	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	0.17	---	V
Reverse Recovery Time	t_{rr}	$I_F=20\text{A}$, $V_R=30\text{V}$ $di/dt=100\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$	---	50	---	nS
Reverse Recovery Charge	Q_{rr}		---	44	---	nC
Peak Reverse Recovery Current	I_{rrm}		---	35	---	A

Note:

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.3\text{mH}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

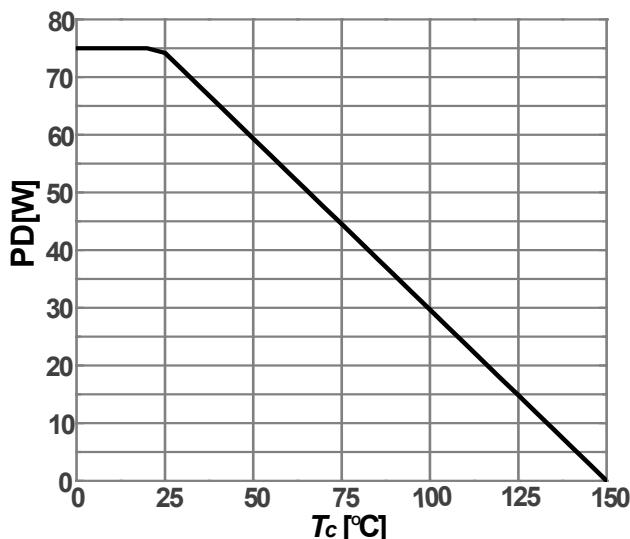


Diagram 1: Power dissipation

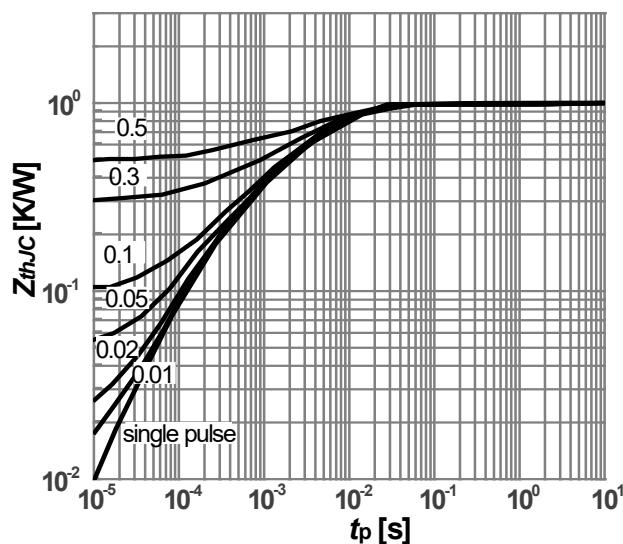


Diagram 2: Max. transient thermal impedance

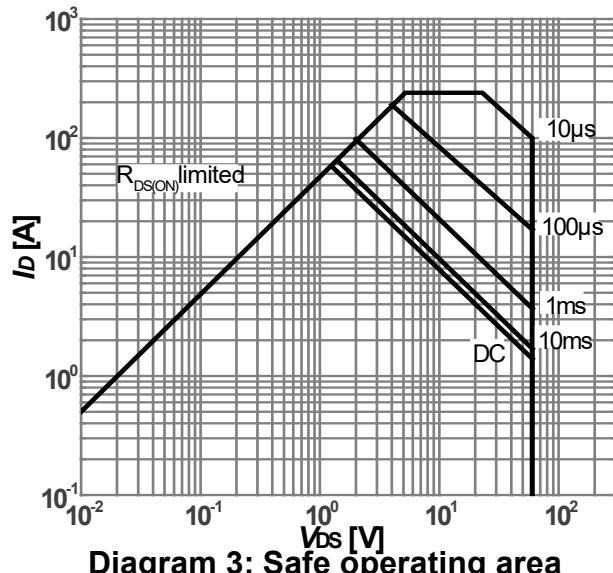


Diagram 3: Safe operating area

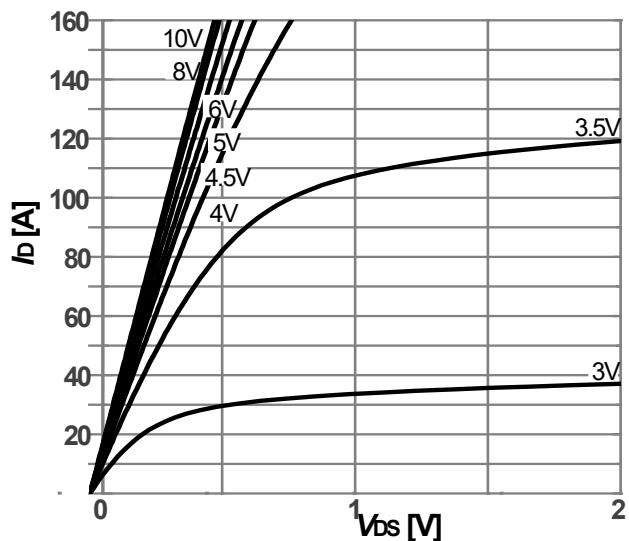


Diagram 4: Typ. output characteristics

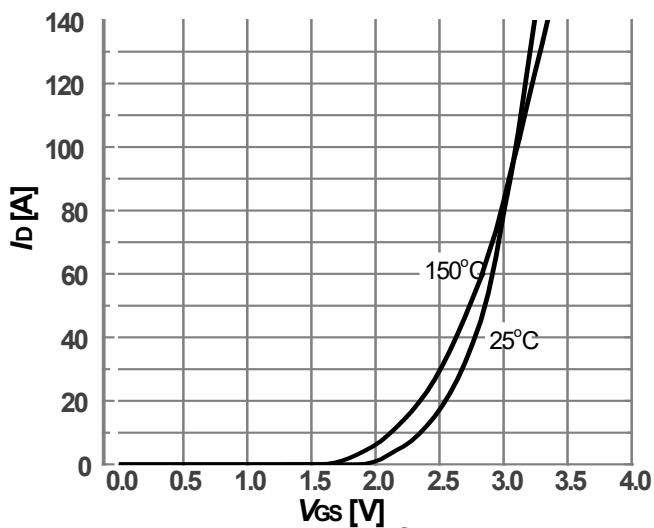


Diagram 5: Typ. transfer characteristics

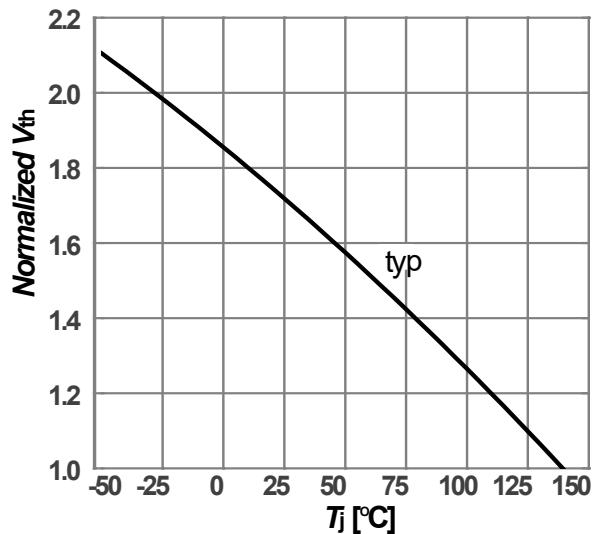


Diagram 6: Gate threshold voltage vs. Junction temperature

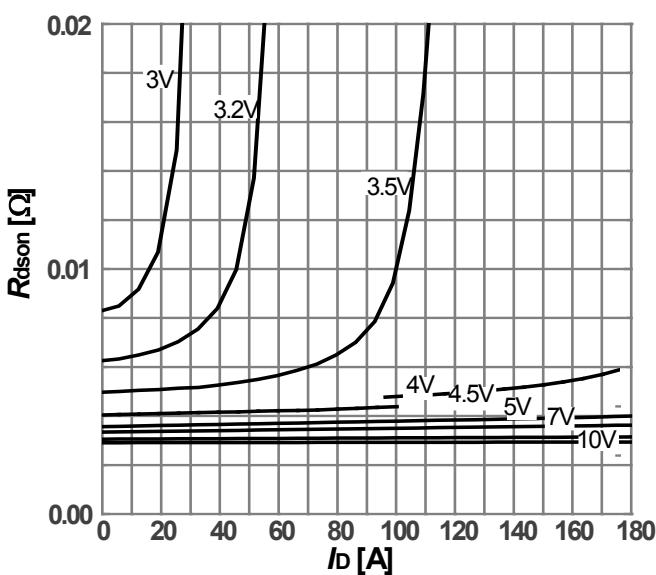


Diagram 7: On-state resistance vs. Drain current

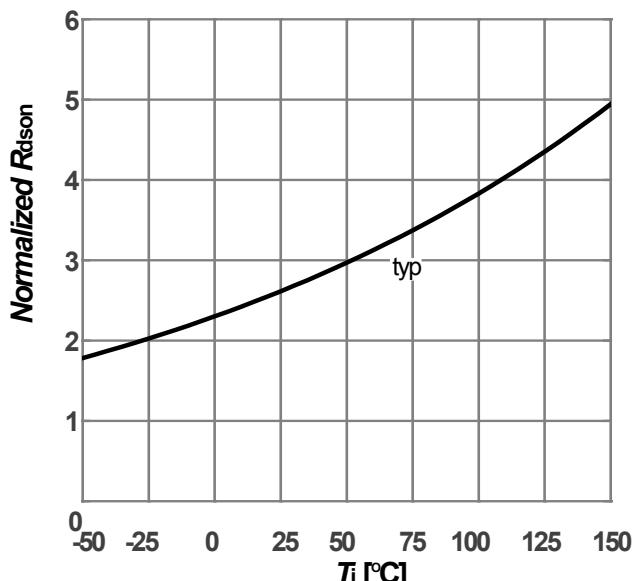


Diagram 8: On-state resistance vs. Junction temperature

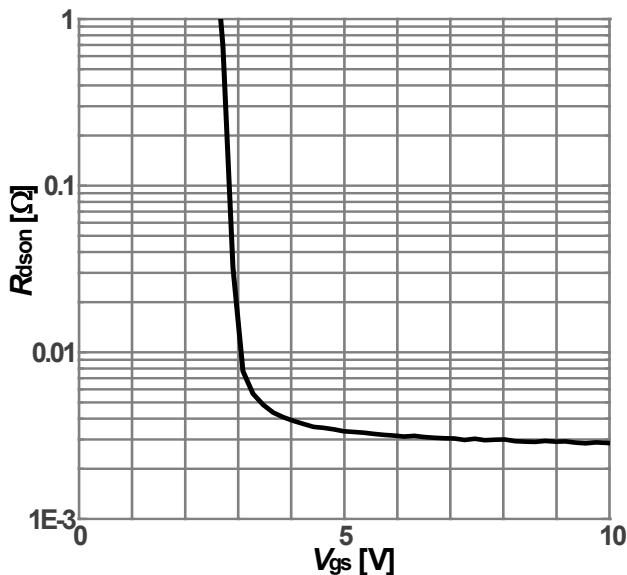


Diagram 9: On-state resistance vs. V_{gs} characteristics

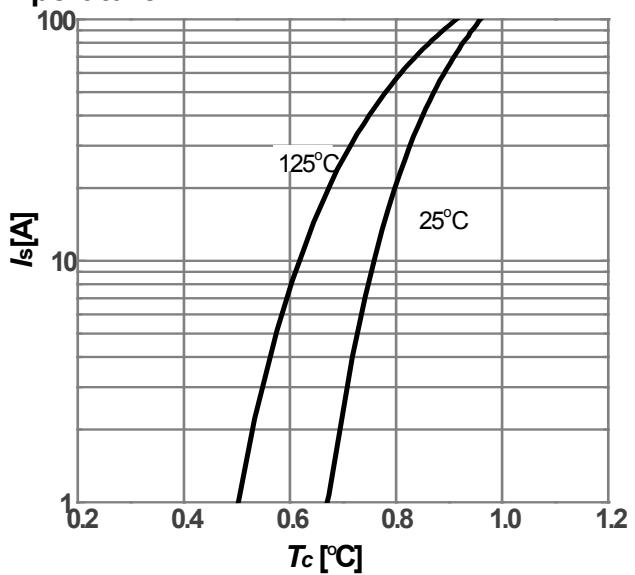


Diagram 10: Forward characteristics of reverse diode

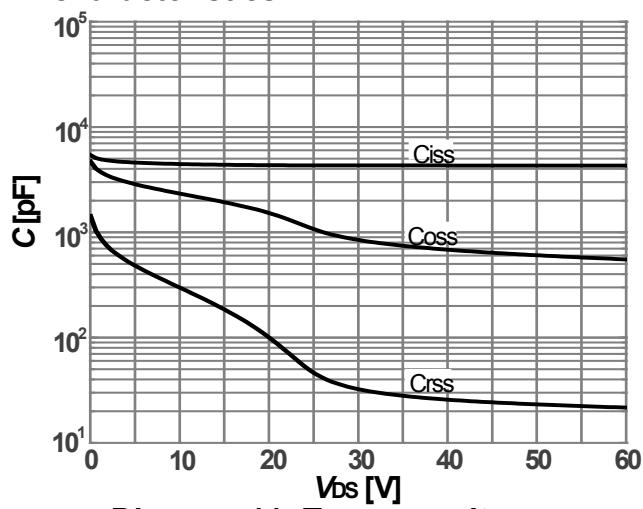


Diagram 11: Typ. capacitances

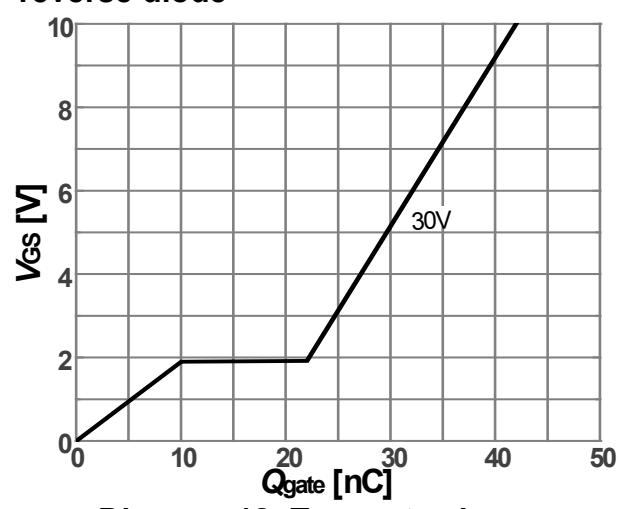
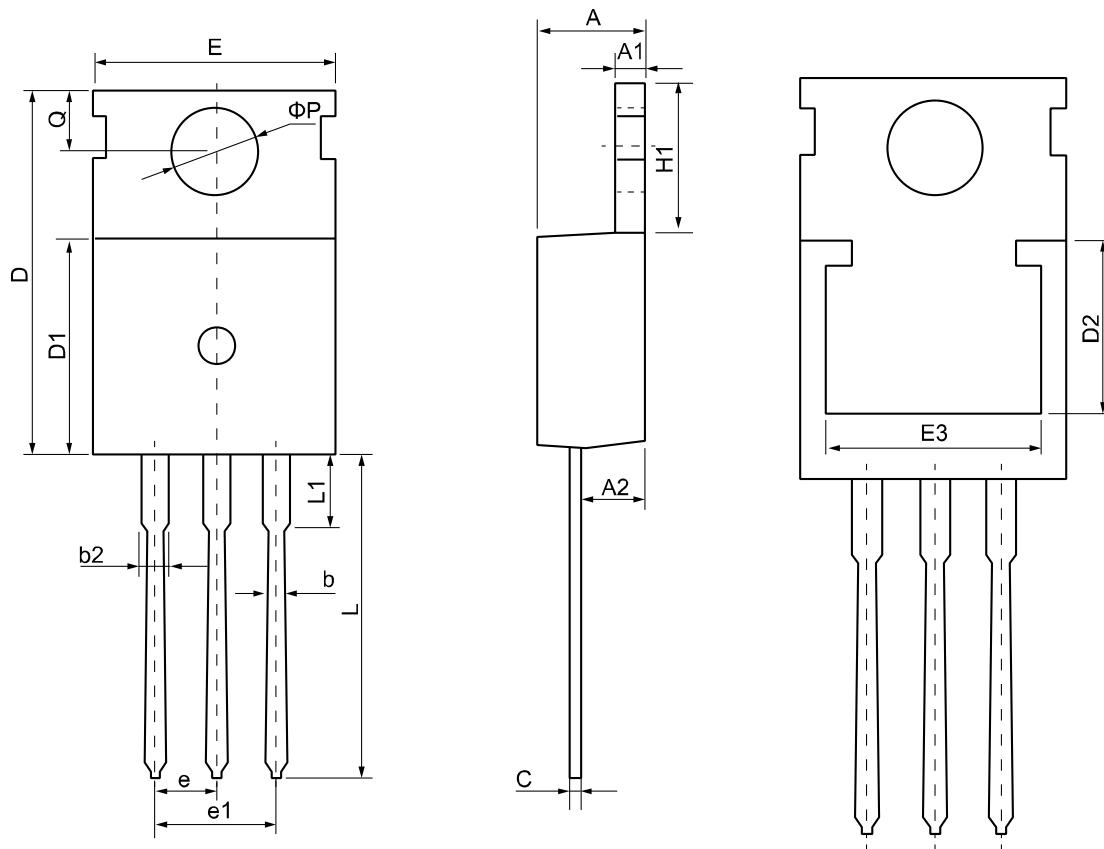


Diagram 12: Typ. gate charge

TO-220 Package Outline Dimensions



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	4.30	4.55	4.75	E	9.65	10.00	10.25
A1	1.15	1.30	1.45	E3	7.00	--	--
A2	2.20	2.40	2.60	e	2.54 BSC		
b	0.70	0.80	0.95	e1	5.08 BSC		
b2	1.17	1.27	1.47	H1	6.30	6.50	6.80
c	0.40	0.50	0.65	L	12.70	13.50	14.10
D	15.30	15.60	15.90	L1	--	3.20	3.95
D1	8.90	9.10	9.35	ϕP	3.40	3.60	3.80
D2	5.50	--	--	Q	2.60	2.80	3.00