

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

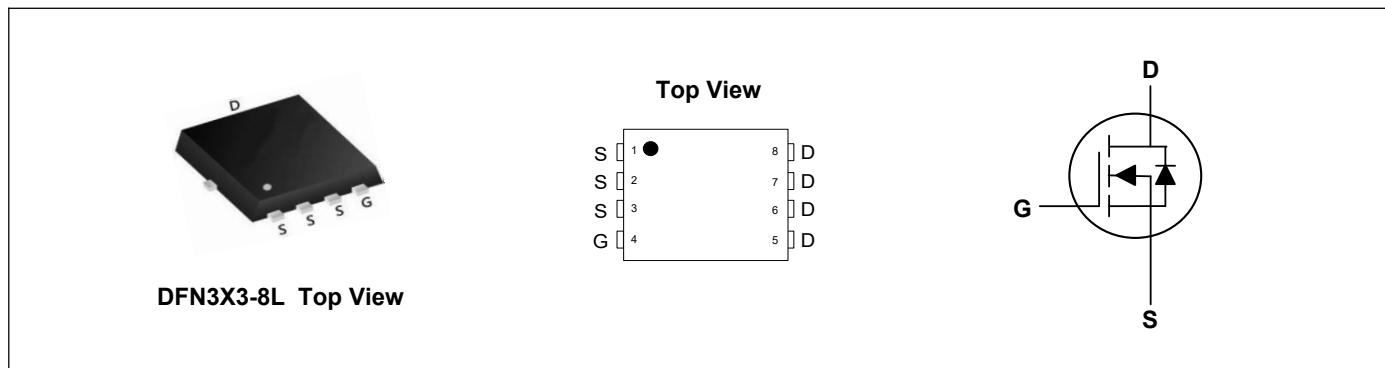
Product Summary



V_{DS}	60	V
I_D	44	A
$R_{DS(ON)}$ (at $V_{GS}=10V$)	12	mΩ
$R_{DS(ON)}$ (at $V_{GS}=4.5V$)	15	mΩ

Applications

- High Frequency Point-of-Load, Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch, LED applications



Absolute Maximum Ratings($T_A=25^\circ C$, unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹	$I_D @ T_C = 25^\circ C$	44	A
Continuous Drain Current ¹	$I_D @ T_C = 100^\circ C$	28	A
Pulsed Drain Current ²	I_{DM}	100	A
Single Pulse Avalanche Energy ³	EAS	72.2	mJ
Avalanche Current	I_{AS}	38	A
Total Power Dissipation ⁴	$P_D @ T_C = 25^\circ C$	42	W
Storage Temperature Range	T_{STG}	-55 to 150	°C
Operating Junction Temperature Range	T_J	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	---	75	°C/W
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	---	3	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}$, $I_{\text{D}}=250\mu\text{A}$	60	---	---	V
Static Drain-Source On-Resistance ²	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}$, $I_{\text{D}}=8\text{A}$	---	---	12	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=6\text{A}$	---	---	15	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}$, $I_{\text{D}}=250\mu\text{A}$	1.2	---	2.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=48\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{\text{DS}}=48\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	uA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
Forward Transconductance	g_{fs}	$V_{\text{DS}}=5\text{V}$, $I_{\text{D}}=8\text{A}$	---	75	---	S
Gate Resistance	R_g	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	1.5	---	Ω
Total Gate Charge (4.5V)	Q_g	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=4.5\text{V}$, $I_{\text{D}}=8\text{A}$	---	28.7	---	nC
Gate-Source Charge	Q_{gs}		---	10.5	---	
Gate-Drain Charge	Q_{gd}		---	9.9	---	
Turn-On Delay Time	$T_{\text{d}(\text{on})}$	$V_{\text{DD}}=30\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=3.3\Omega$, $I_{\text{D}}=8\text{A}$	---	10.4	---	ns
Rise Time	T_r		---	9.2	---	
Turn-Off Delay Time	$T_{\text{d}(\text{off})}$		---	63	---	
Fall Time	T_f		---	4.8	---	
Input Capacitance	C_{iss}	$V_{\text{DS}}=15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	3240	---	pF
Output Capacitance	C_{oss}		---	210	---	
Reverse Transfer Capacitance	C_{rss}		---	146	---	

Drain-Source Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current ^{1,5}	I_s	$V_G=V_D=0\text{V}$, Force Current	---	---	44	A
Pulsed Source Current ^{2,5}	I_{SM}		---	---	100	A
Diode Forward Voltage ²	V_{SD}	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V
Reverse Recovery Time	t_{rr}	$I_F=8\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$	---	18	---	nS
	Q_{rr}		---	14	---	nC

Note:

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=25\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.1\text{mH}$, $I_{\text{AS}}=38\text{A}$
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I_{D} and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics

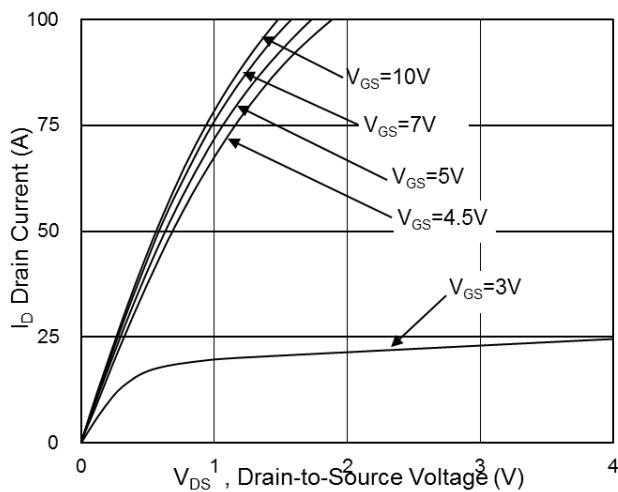


Fig.1 Typical Output Characteristics

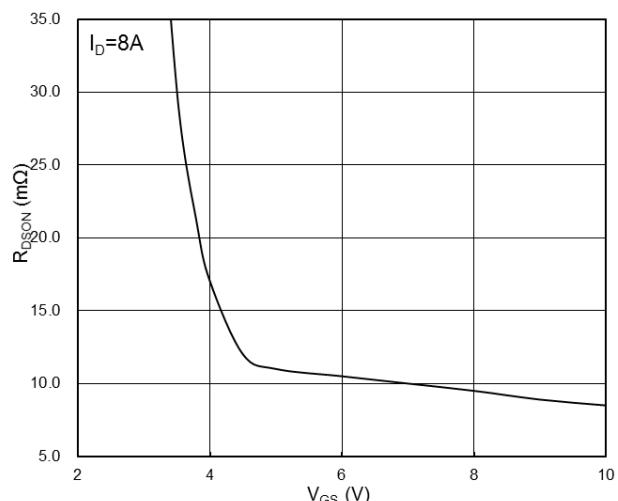


Fig.2 On-Resistance vs. Gate-Source Voltage

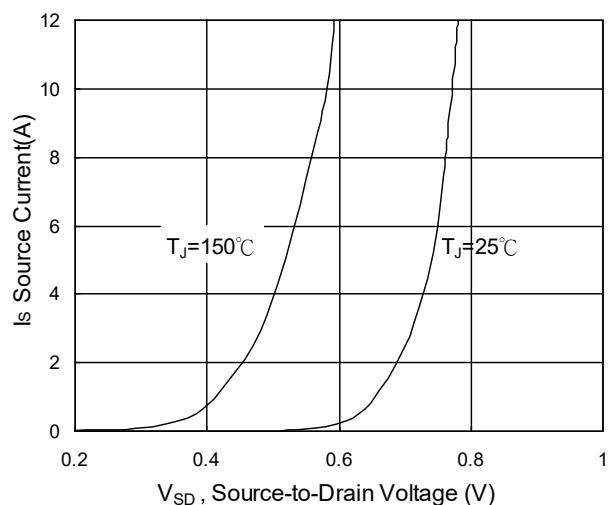


Fig.3 Forward Characteristics of Reverse

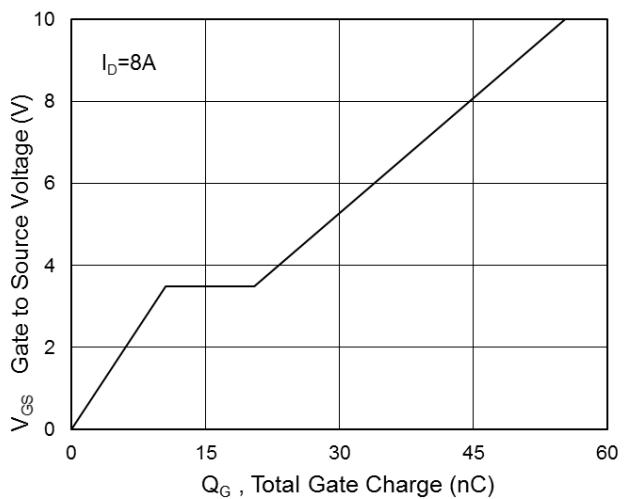


Fig.4 Gate-Charge Characteristics

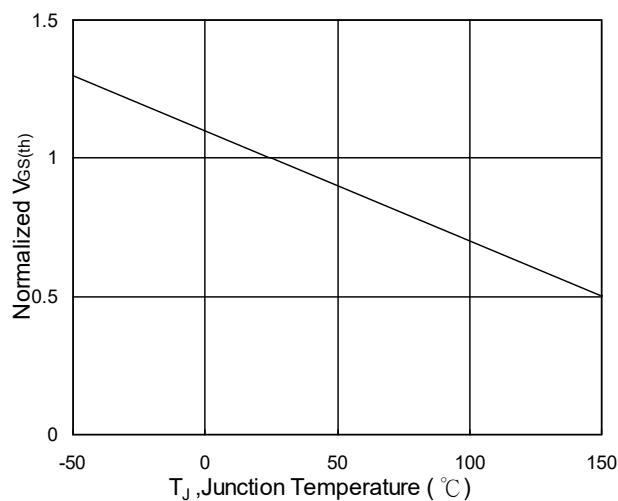


Fig.5 Normalized $V_{GS(th)}$ vs. T_J

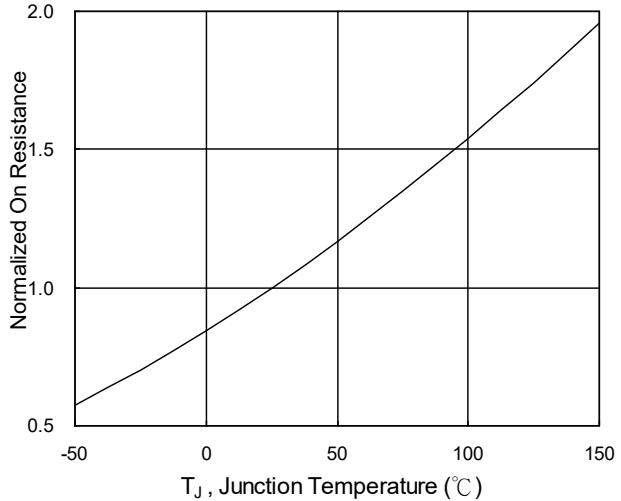
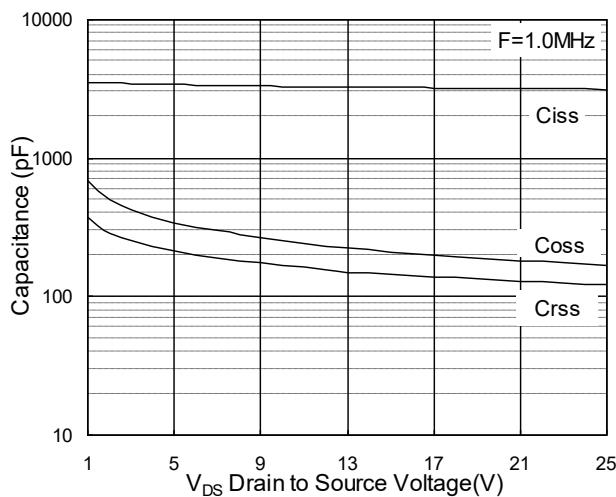
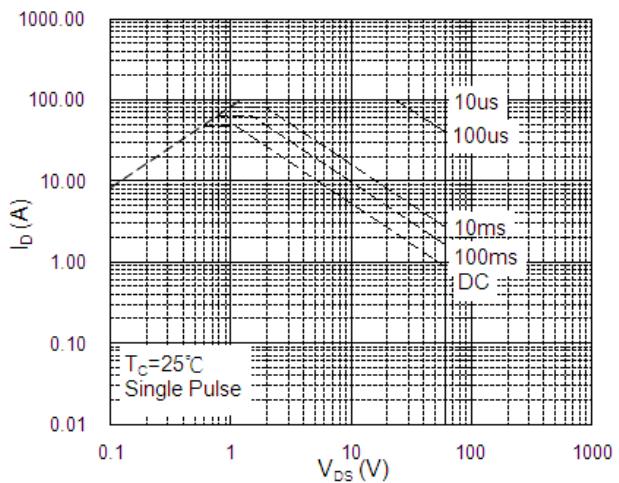
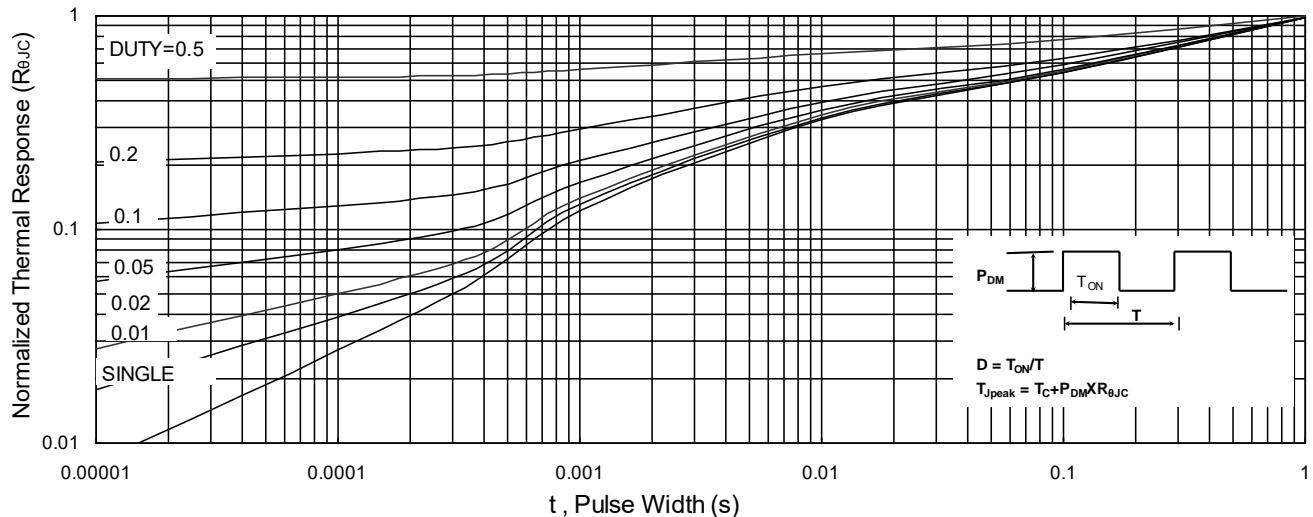
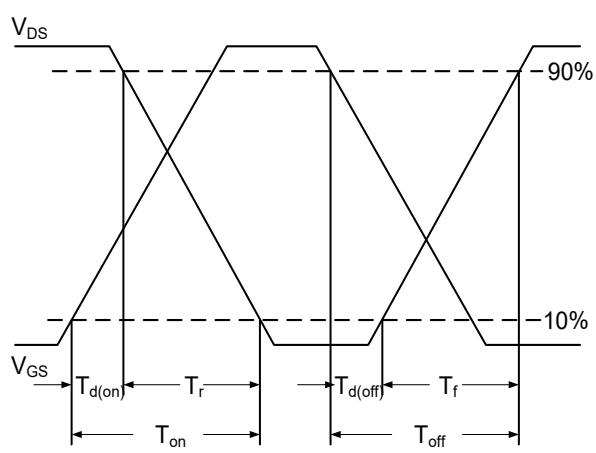
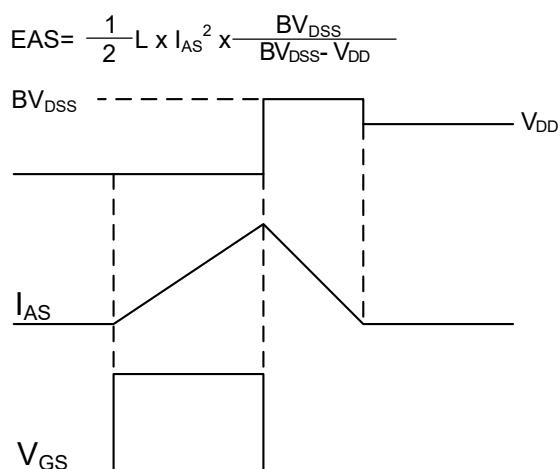
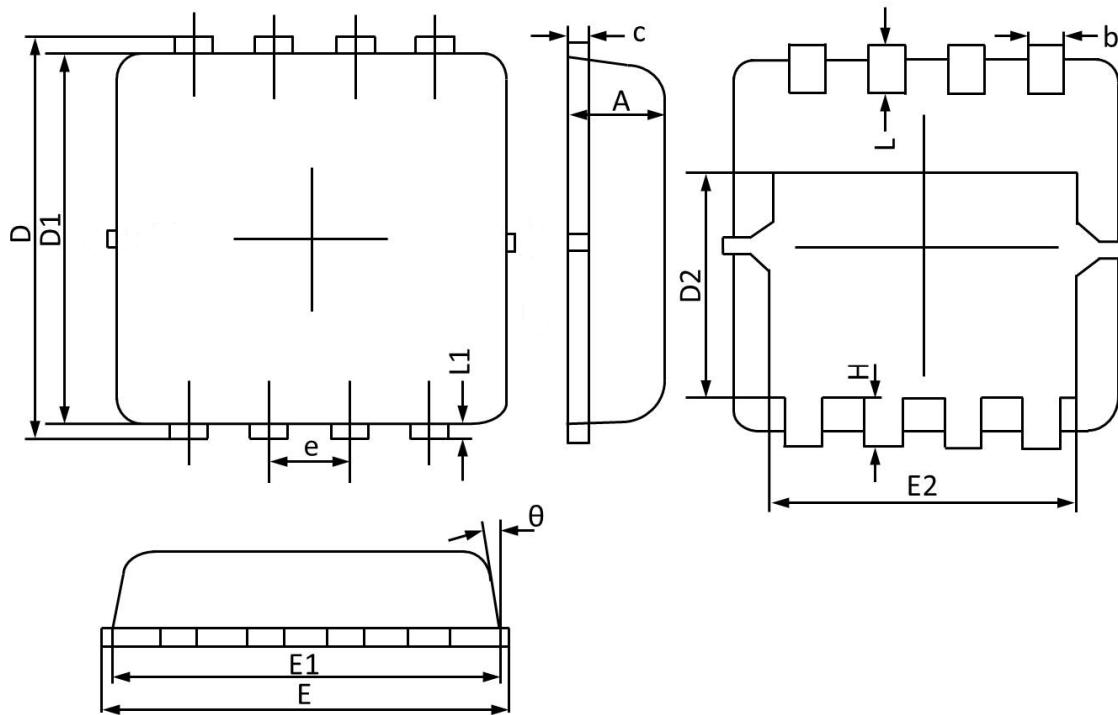


Fig.6 Normalized $R_{DS(on)}$ vs. T_J


Fig.7 Capacitance

Fig.8 Safe Operating Area

Fig.9 Normalized Maximum Transient Thermal Impedance

Fig.10 Switching Time Waveform

Fig.11 Unclamped Inductive Switching Waveform

DFN3X3-8L Package Outline Dimensions



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	0.70	0.75	0.85	E1	2.90	3.10	3.25
b	0.24	0.30	0.35	E2	2.35	2.50	2.60
c	0.10	0.17	0.25	e	0.65 BSC		
D	3.10	3.30	3.45	H	0.30	0.40	0.50
D1	2.90	3.05	3.20	L	0.30	0.40	0.50
D2	1.45	1.70	1.95	L1	--	0.13	--
E	3.05	3.25	3.40	θ	0°		14°