

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

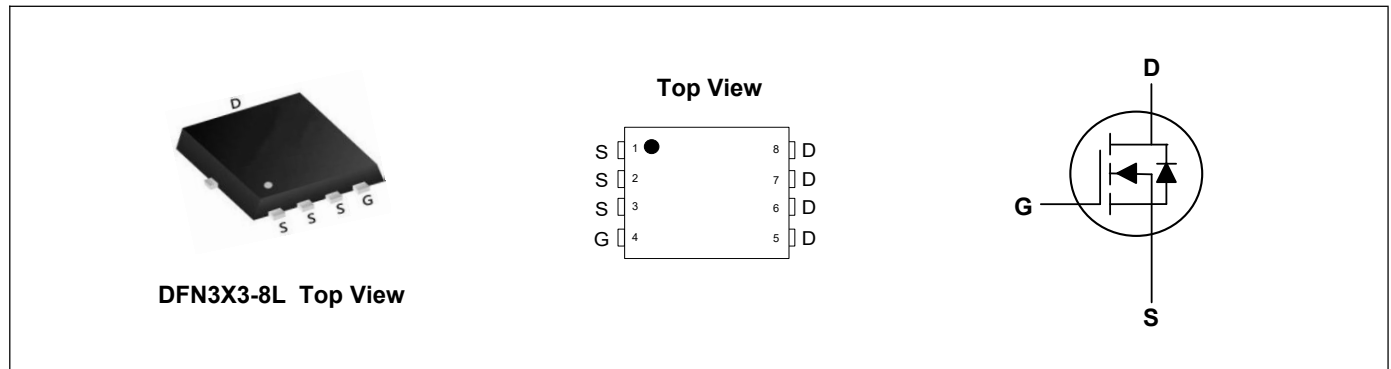
## Applications

- High Frequency Point-of-Load Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch

## Product Summary



$V_{DS}$	30	V
$I_D$	42	A
$R_{DS(ON)}$ (at $V_{GS}=10V$ )	5.8	m $\Omega$
$R_{DS(ON)}$ (at $V_{GS}=4.5V$ )	7.2	m $\Omega$



## Absolute Maximum Ratings( $T_C=25^\circ C$ , unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 10$	V
Continuous Drain Current <sup>1</sup>	$I_D@T_C=25^\circ C$	42	A
Continuous Drain Current <sup>1</sup>	$I_D@T_C=100^\circ C$	26	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	150	A
Single Pulse Avalanche Energy <sup>3</sup>	EAS	60	mJ
Avalanche Current	$I_{AS}$	40	A
Total Power Dissipation <sup>4</sup>	$P_D@T_C=25^\circ C$	31	W
Storage Temperature Range	$T_{STG}$	-55 to 150	$^\circ C$
Operating Junction Temperature Range	$T_J$	-55 to 150	$^\circ C$

## Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	---	35	$^\circ C/W$
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	---	4	$^\circ C/W$

**Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	30	---	---	V
Static Drain-Source On-Resistance <sup>2</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =12A	---	4.2	5.8	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =12A	---	4.6	7.2	mΩ
		V <sub>GS</sub> =2.5V, I <sub>D</sub> =10A	---	5.5	15	mΩ
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250μA	0.9	---	2.0	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	μA
		V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	5	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =12A	---	25	---	S
Gate Resistance	R <sub>g</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	---	1.4	---	Ω
Total Gate Charge (4.5V)	Q <sub>g</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =12A	---	18	---	nC
Gate-Source Charge	Q <sub>gs</sub>		---	10	---	
Gate-Drain Charge	Q <sub>gd</sub>		---	8	---	
Turn-On Delay Time	T <sub>d(on)</sub>	V <sub>DD</sub> =15V, V <sub>GS</sub> =10V, R <sub>G</sub> =1.5Ω, I <sub>D</sub> =12A	---	6	---	ns
Rise Time	T <sub>r</sub>		---	15	---	
Turn-Off Delay Time	T <sub>d(off)</sub>		---	18	---	
Fall Time	T <sub>f</sub>		---	4	---	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz	---	680	---	pF
Output Capacitance	C <sub>oss</sub>		---	170	---	
Reverse Transfer Capacitance	C <sub>riss</sub>		---	80	---	

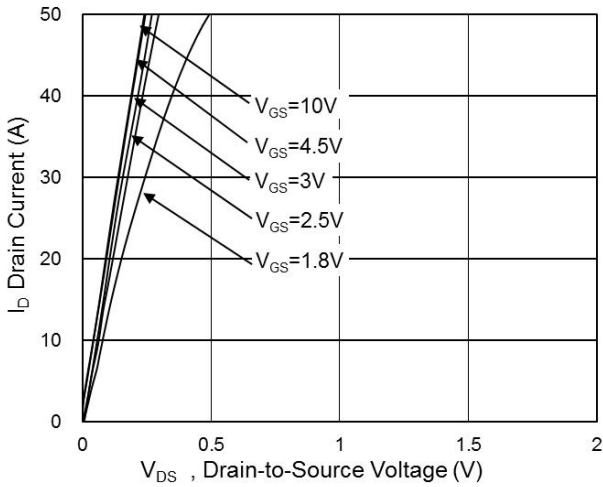
**Drain-Source Diode Characteristics**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current <sup>1,5</sup>	I <sub>S</sub>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	42	A
Pulsed Source Current <sup>2,5</sup>	I <sub>SM</sub>		---	---	150	A
Diode Forward Voltage <sup>2</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1	V

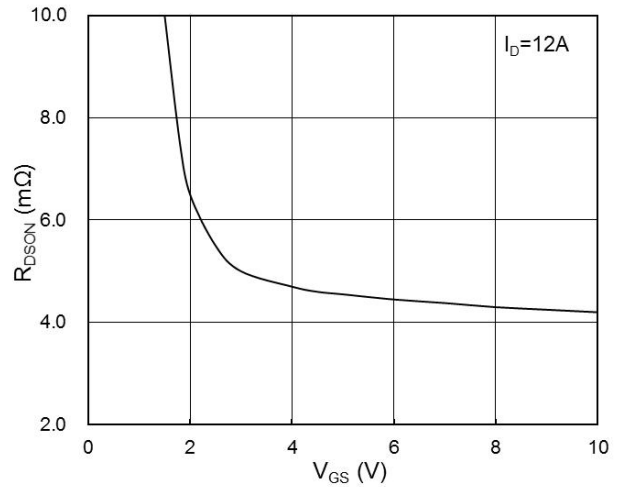
**Note:**

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%
- 3.The EAS data shows Max. rating. The test condition is V<sub>DD</sub>=25V, V<sub>GS</sub>=10V, L=0.1mH, I<sub>AS</sub>=50A
- 4.The power dissipation is limited by 150°C junction temperature
- 5.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.

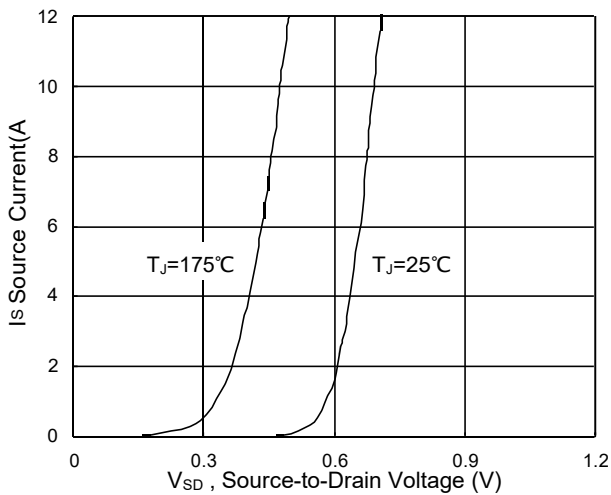
**Typical Characteristics**



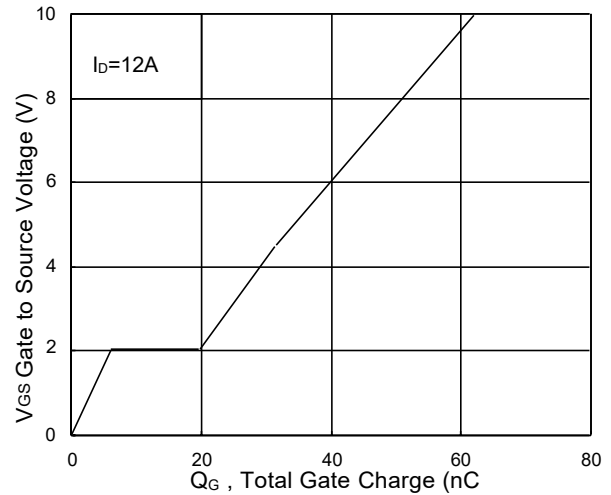
**Fig.1 Typical Output Characteristics**



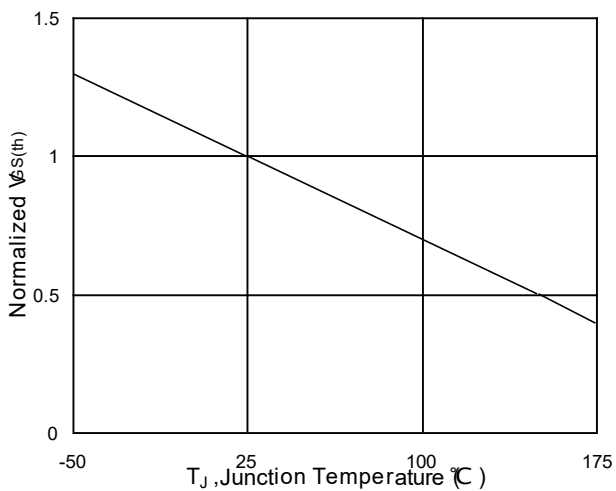
**Fig.2 On-Resistance vs. G-S Voltage**



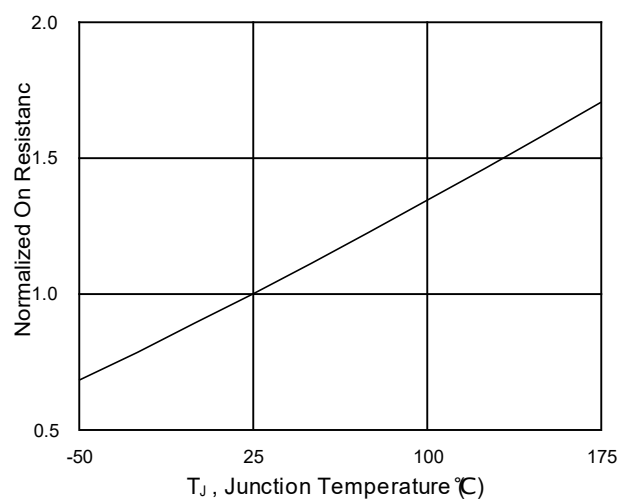
**Fig.3 Forward Characteristics of Reverse**



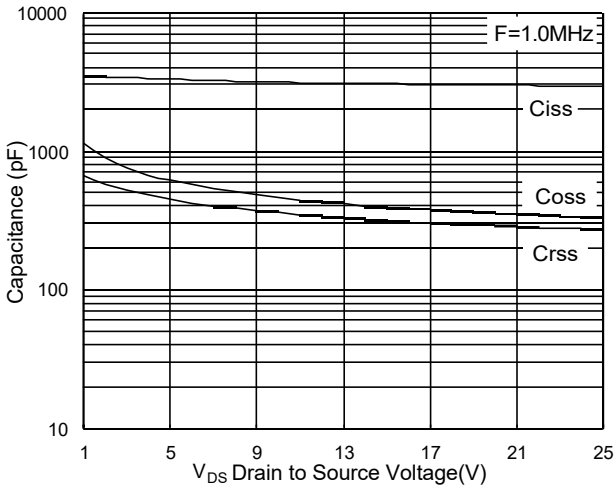
**Fig.4 Gate-Charge Characteristics**



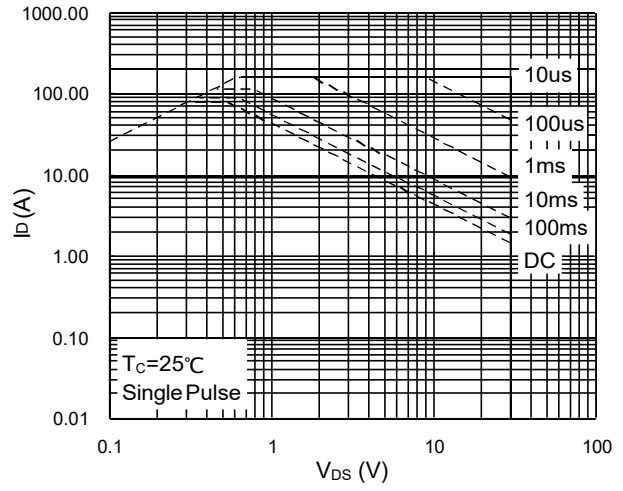
**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**



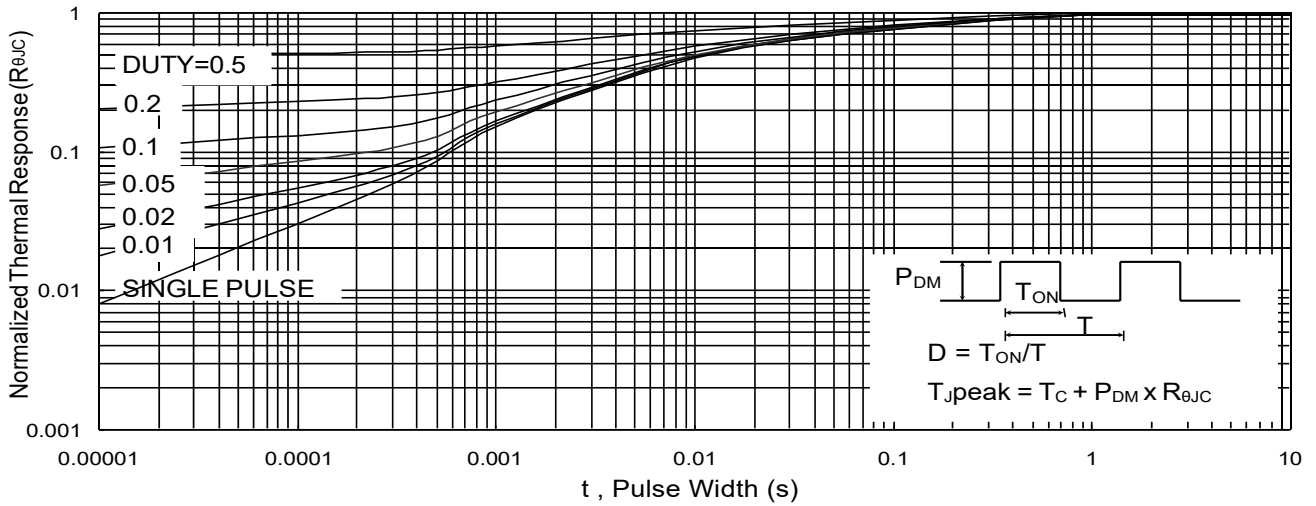
**Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**



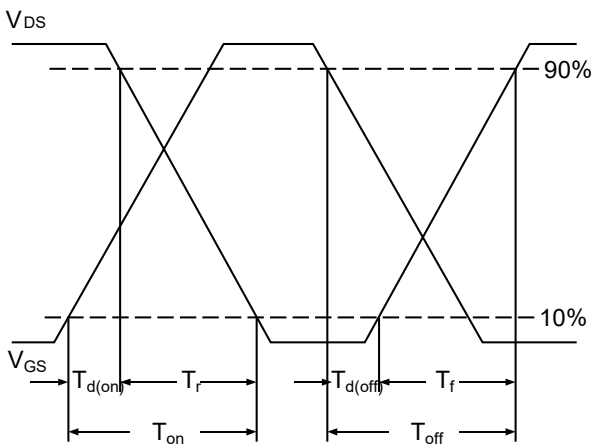
**Fig.7 Capacitance**



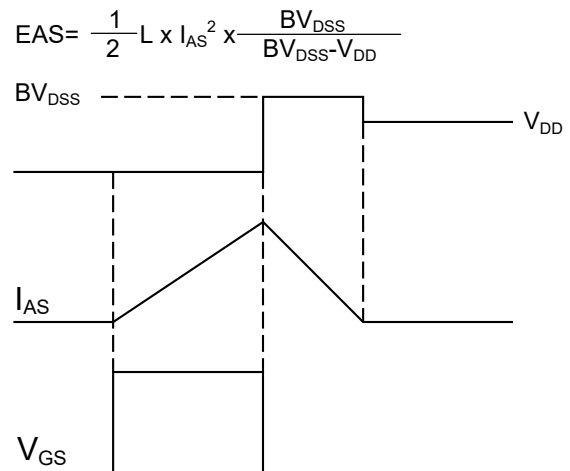
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**

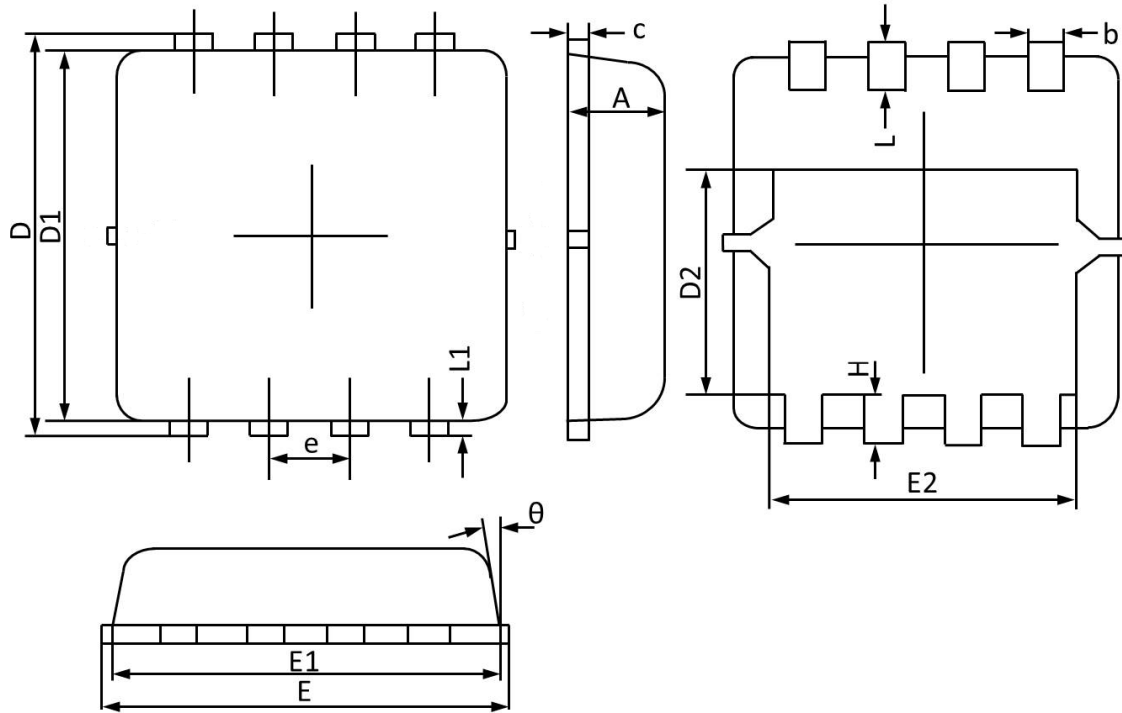


**Fig.10 Switching Time Waveform**



**Fig.11 Unclamped Inductive Switching Waveform**

**DFN3X3-8L Package Outline Dimensions**



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
<b>A</b>	0.70	0.75	0.85	<b>E1</b>	2.90	3.10	3.25
<b>b</b>	0.24	0.30	0.35	<b>E2</b>	2.35	2.50	2.60
<b>c</b>	0.10	0.17	0.25	<b>e</b>	0.65 BSC		
<b>D</b>	3.10	3.30	3.45	<b>H</b>	0.30	0.40	0.50
<b>D1</b>	2.90	3.05	3.20	<b>L</b>	0.30	0.40	0.50
<b>D2</b>	1.45	1.70	1.95	<b>L1</b>	--	0.13	--
<b>E</b>	3.05	3.25	3.40	<b>theta</b>	0°		14°