

**Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

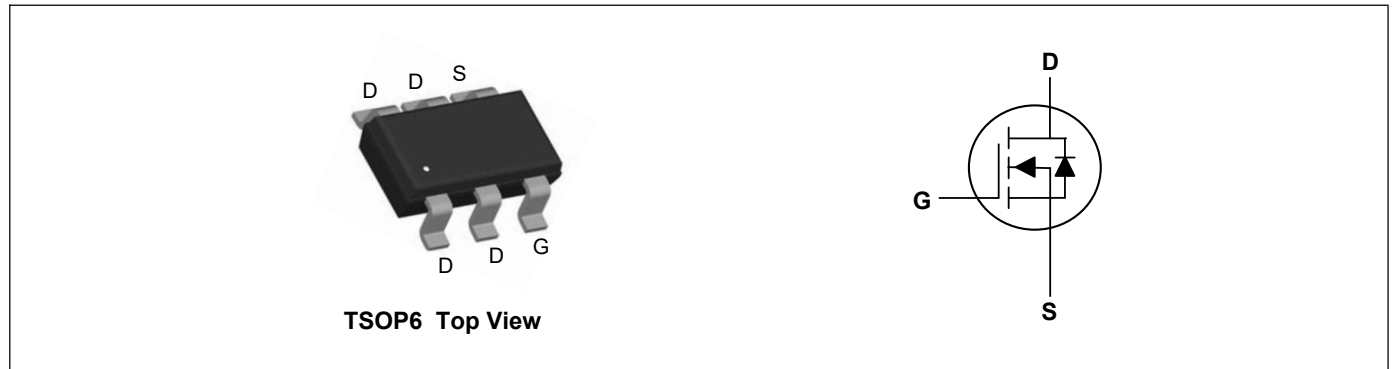
**Product Summary**



$V_{DS}$	30	V
$I_D$	5	A
$R_{DS(ON)}$ (at $V_{GS}=10V$ )	28	m $\Omega$
$R_{DS(ON)}$ (at $V_{GS}=4.5V$ )	40	m $\Omega$

**Applications**

- High Frequency Point-of-Load, Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch



**Absolute Maximum Ratings ( $T_A=25^\circ C$ , unless otherwise noted)**

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current, $V_{GS}$ @ 10V <sup>1</sup>	$I_D@T_A=25^\circ C$	5	A
Continuous Drain Current, $V_{GS}$ @ 10V <sup>1</sup>	$I_D@T_A=70^\circ C$	4	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	10	A
Total Power Dissipation <sup>3</sup>	$P_D@T_A=25^\circ C$	1.1	W
Storage Temperature Range	$T_{STG}$	-55 to 150	$^\circ C$
Operating Junction Temperature Range	$T_J$	-55 to 150	$^\circ C$

**Thermal Characteristics**

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient <sup>1</sup>	$R_{\theta JA}$	---	110	$^\circ C/W$
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	---	70	$^\circ C/W$

**Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	30	---	---	V
BV <sub>DSS</sub> Temperature Coefficient	ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	Reference to 25°C, I <sub>D</sub> =1mA	---	0.02	---	V/°C
Static Drain-Source On-Resistance <sup>2</sup>	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =5A	---	---	28	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =4A	---	---	40	mΩ
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =250uA	1.0	---	2.5	V
V <sub>GS(th)</sub> Temperature Coefficient	ΔV <sub>GS(th)</sub>		---	-4.6	---	mV/°C
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	1	uA
		V <sub>DS</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	5	
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =5A	---	6.3	---	S
Gate Resistance	R <sub>g</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	---	2.5	5	Ω
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =4.5V, I <sub>D</sub> =5A	---	7.2	---	nC
Gate-Source Charge	Q <sub>gs</sub>		---	1.4	---	
Gate-Drain Charge	Q <sub>gd</sub>		---	2.2	---	
Turn-On Delay Time	T <sub>d(on)</sub>	V <sub>DD</sub> =12V, V <sub>GS</sub> =10V, R <sub>G</sub> =3.3Ω, I <sub>D</sub> =5A	---	4.1	---	ns
Rise Time	T <sub>r</sub>		---	9.8	---	
Turn-Off Delay Time	T <sub>d(off)</sub>		---	15.5	---	
Fall Time	T <sub>f</sub>		---	6	---	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =15V, V <sub>GS</sub> =0V, f=1MHz	---	572	---	pF
Output Capacitance	C <sub>oss</sub>		---	81	---	
Reverse Transfer Capacitance	C <sub>rss</sub>		---	65	---	

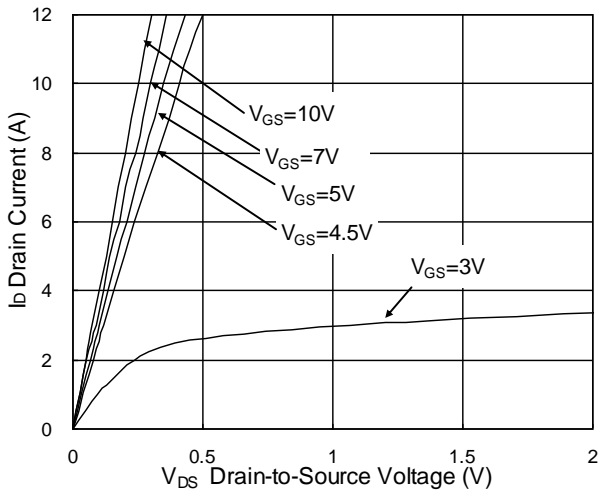
**Drain-Source Diode Characteristics**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current <sup>1,4</sup>	I <sub>S</sub>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	5	A
Pulsed Source Current <sup>2,4</sup>	I <sub>SM</sub>		---	---	10	A
Diode Forward Voltage <sup>2</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =1A, T <sub>J</sub> =25°C	---	---	1.2	V

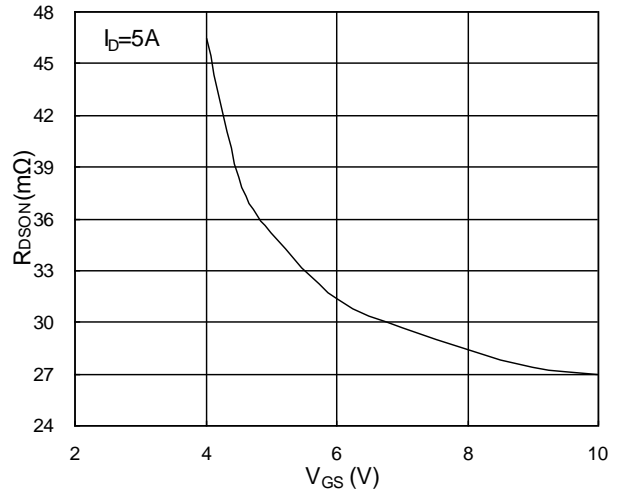
**Note:**

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.

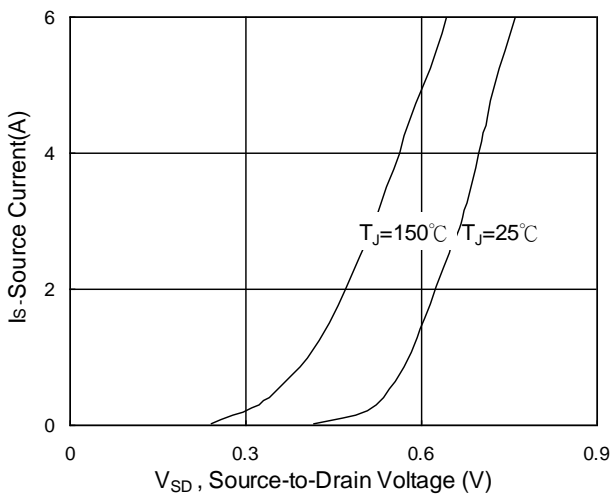
**Typical Characteristics**



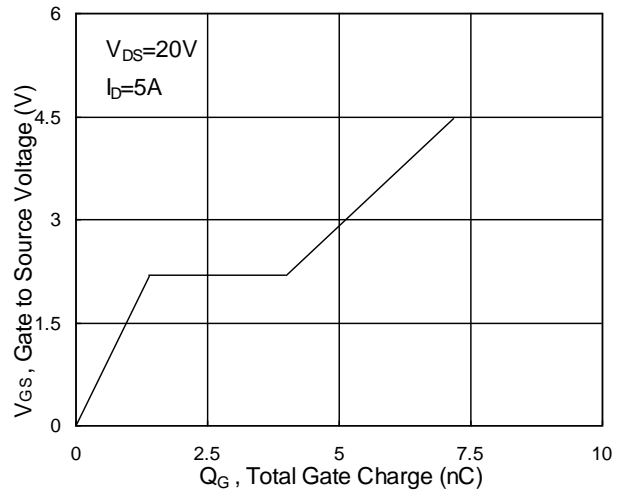
**Fig.1 Typical Output Characteristics**



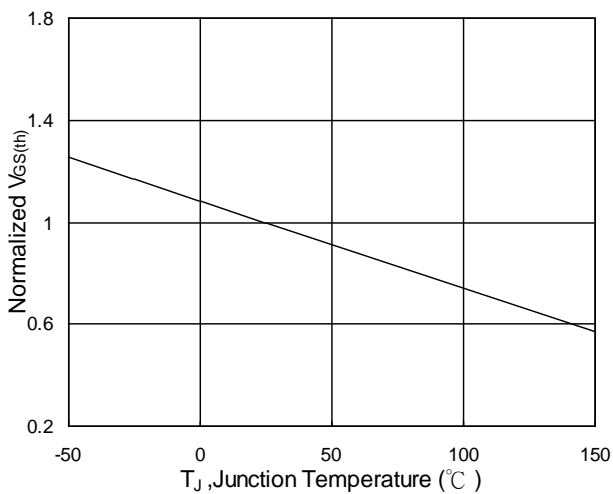
**Fig.2 On-Resistance vs. Gate-Source**



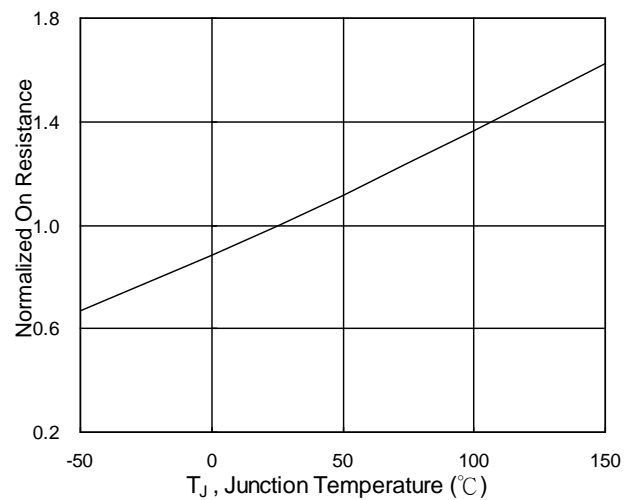
**Fig.3 Forward Characteristics Of Reverse**



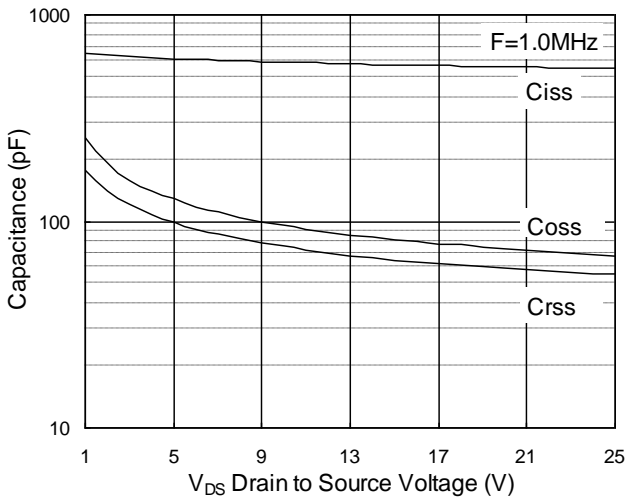
**Fig.4 Gate-Charge Characteristics**



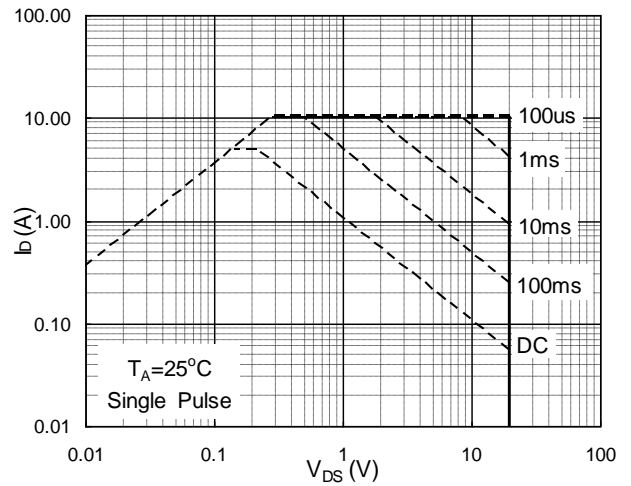
**Fig.5 Normalized  $V_{GS(th)}$  vs.  $T_J$**



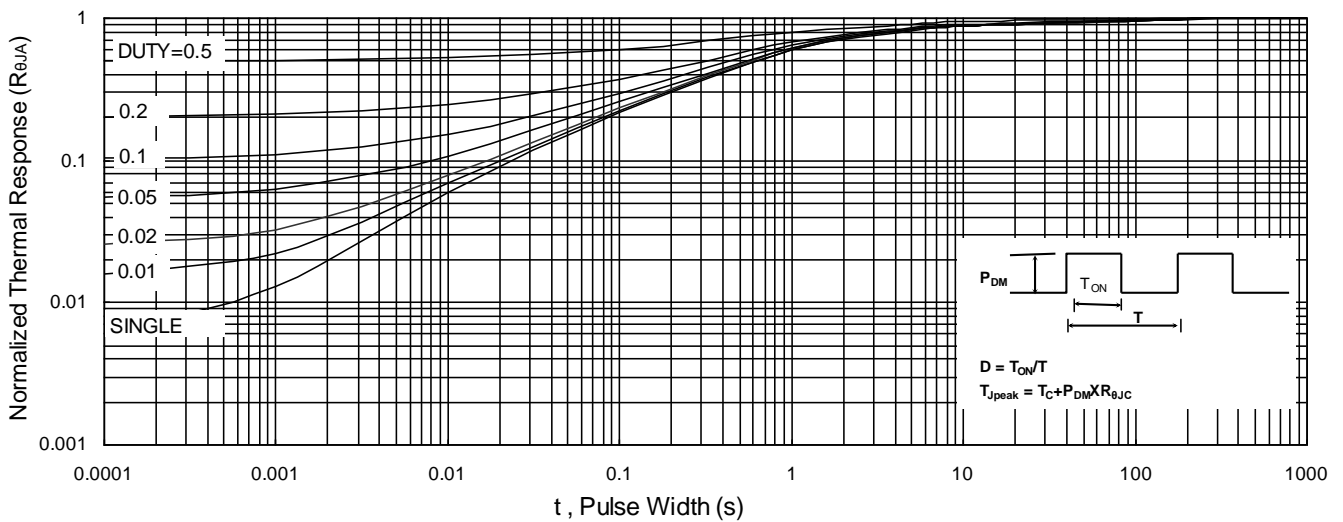
**Fig.6 Normalized  $R_{DS(on)}$  vs.  $T_J$**



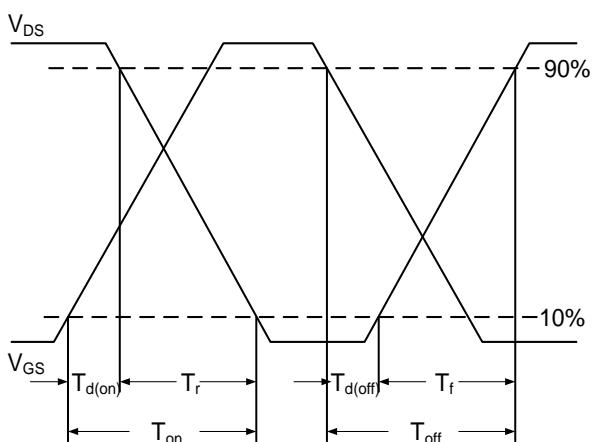
**Fig.7 Capacitance**



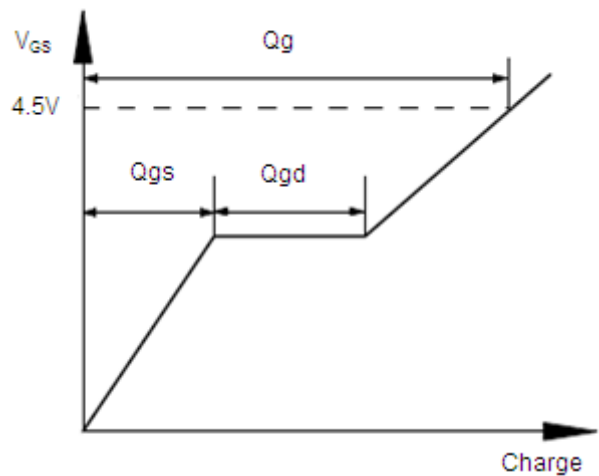
**Fig.8 Safe Operating Area**



**Fig.9 Normalized Maximum Transient Thermal Impedance**

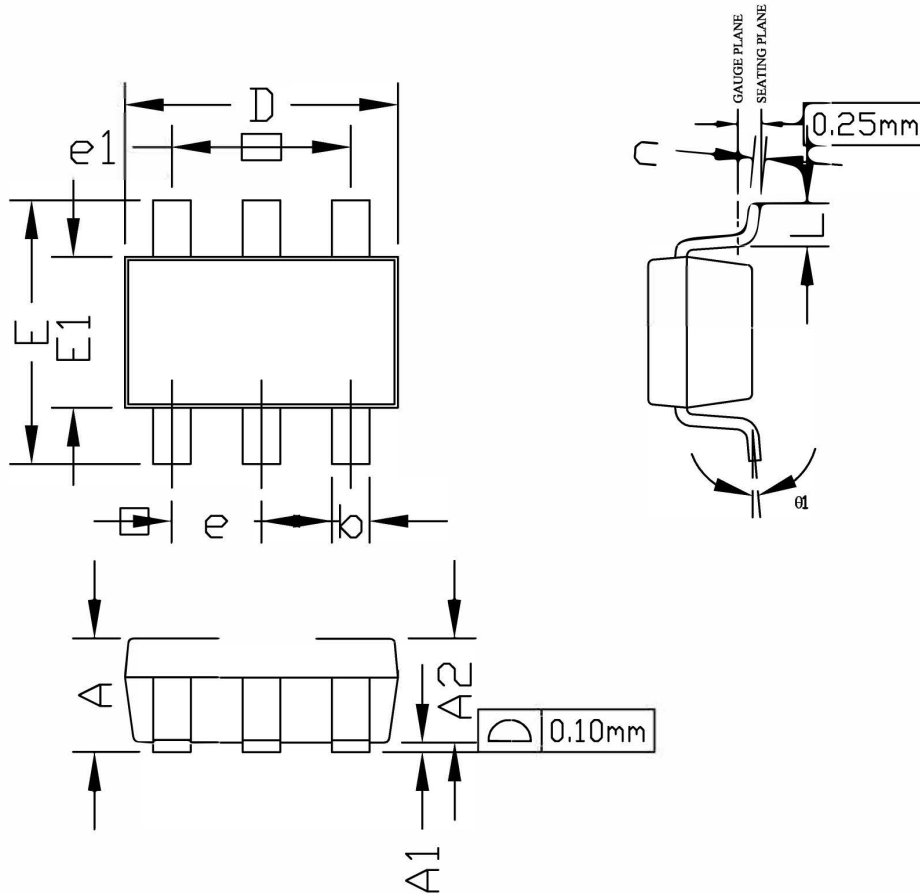


**Fig.10 Switching Time Waveform**



**Fig.11 Gate Charge Waveform**

**TSOP6 Package Outline Dimensions**



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
<b>A</b>	0.80	1.00	1.25	<b>E</b>	2.50	2.80	3.10
<b>A1</b>	0.00	---	0.15	<b>E1</b>	1.50	1.60	1.70
<b>A2</b>	0.80	1.10	1.20	<b>e</b>	0.95 REF		
<b>b</b>	0.25	0.35	0.45	<b>e1</b>	1.90 REF		
<b>c</b>	0.08	0.13	0.20	<b>L</b>	0.30	0.45	0.60
<b>D</b>	2.70	2.90	3.10	<b>theta1</b>	0°		8°