

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

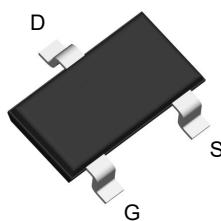
Product Summary



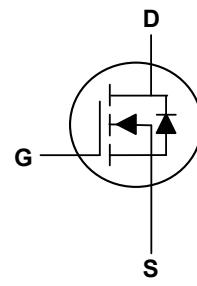
V_{DS}	100	V
I_D	2	A
$R_{DS(ON)}$ (at $V_{GS}=10V$)	240	mΩ

Applications

- High Frequency Point-of-Load,Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- Load Switch



SOT23 Top View



Absolute Maximum Ratings($T_A=25^\circ C$, unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	100	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	$I_D @ T_A=25^\circ C$	2	A
Pulsed Drain Current ²	I_{DM}	5	A
Total Power Dissipation ³	$P_D @ T_A=25^\circ C$	1.25	W
Storage Temperature Range	T_{STG}	-55 to 150	°C
Operating Junction Temperature Range	T_J	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	---	100	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	100	110	---	V
Static Drain-Source On-Resistance ²	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}$, $I_D=1\text{A}$	---	210	240	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	1.2	1.8	2.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=100\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{\text{DS}}=100\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=55^\circ\text{C}$	---	---	5	
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
Forward Transconductance	g_{fs}	$V_{\text{DS}}=5\text{V}$, $I_D=1\text{A}$	1	---	---	S
Total Gate Charge	Q_g	$V_{\text{DS}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=1.3\text{A}$	---	5.2	---	nC
Gate-Source Charge	Q_{gs}		---	0.75	---	
Gate-Drain Charge	Q_{gd}		---	1.4	---	
Turn-On Delay Time	$T_{\text{d(on)}}$	$V_{\text{DD}}=50\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=1\Omega$, $I_D=1.3\text{A}$, $R_L=39\Omega$	---	6	---	ns
Rise Time	T_r		---	10	---	
Turn-Off Delay Time	$T_{\text{d(off)}}$		---	10	---	
Fall Time	T_f		---	6	---	
Input Capacitance	C_{iss}	$V_{\text{DS}}=50\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	190	---	pF
Output Capacitance	C_{oss}		---	22	---	
Reverse Transfer Capacitance	C_{rss}		---	13	---	

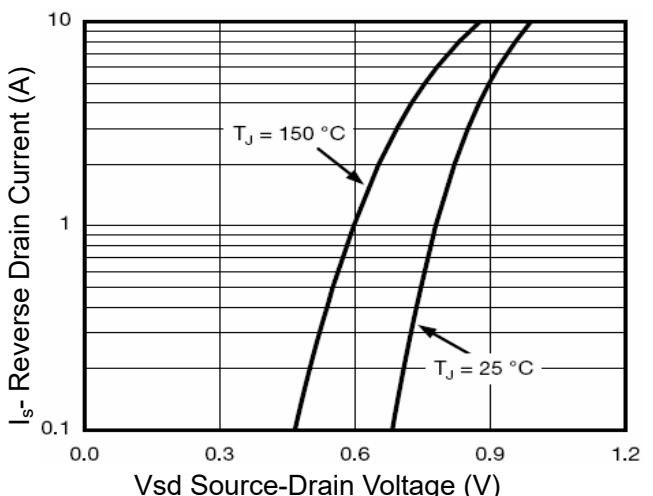
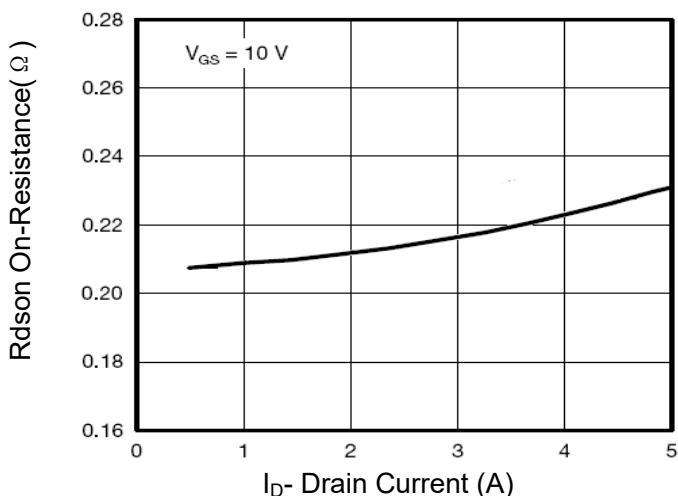
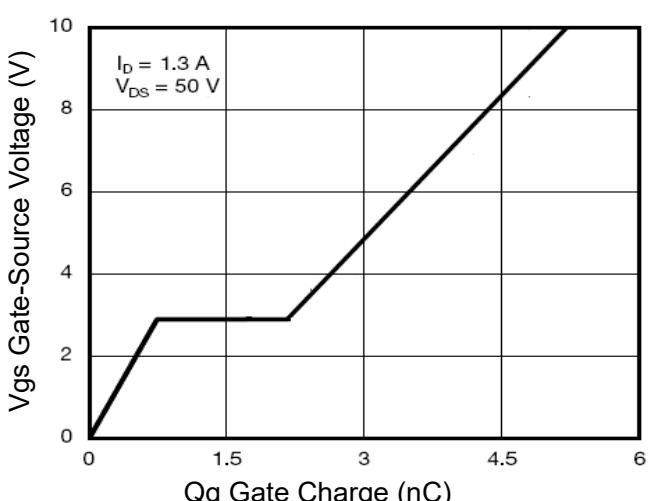
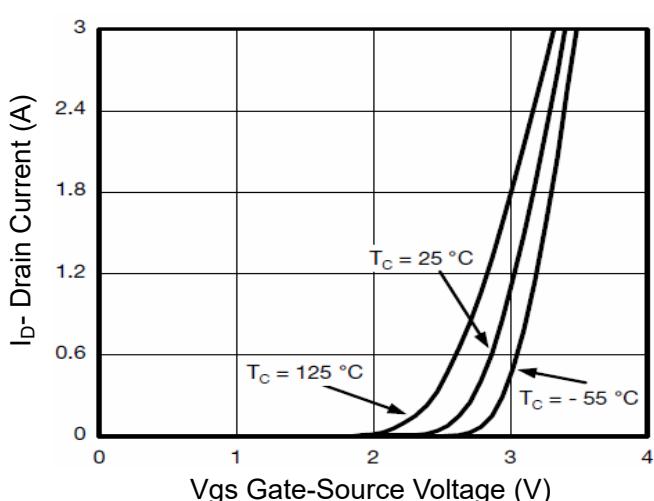
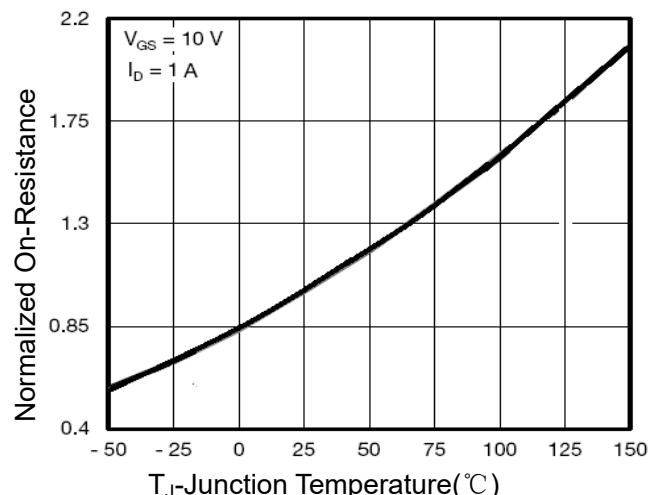
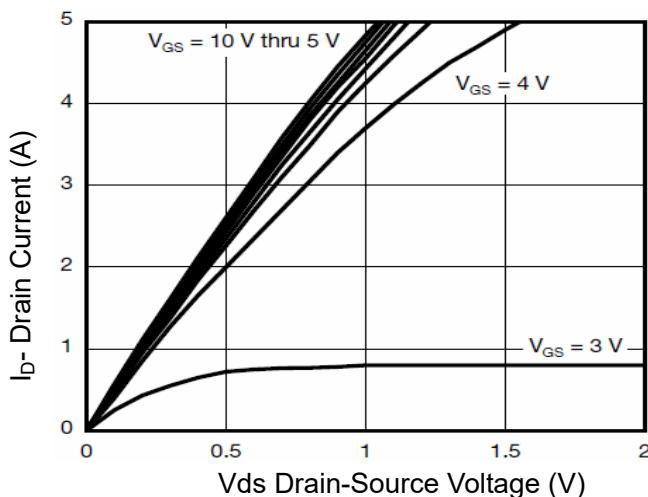
Drain-Source Diode Characteristics

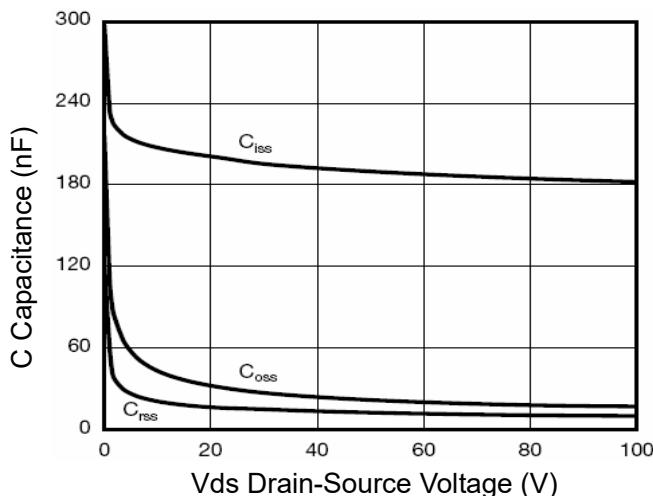
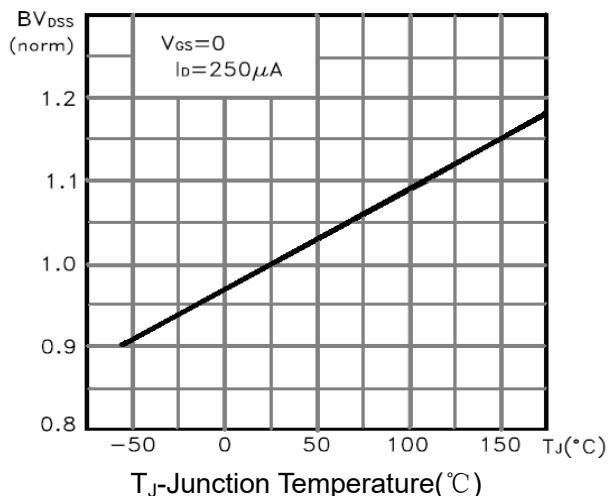
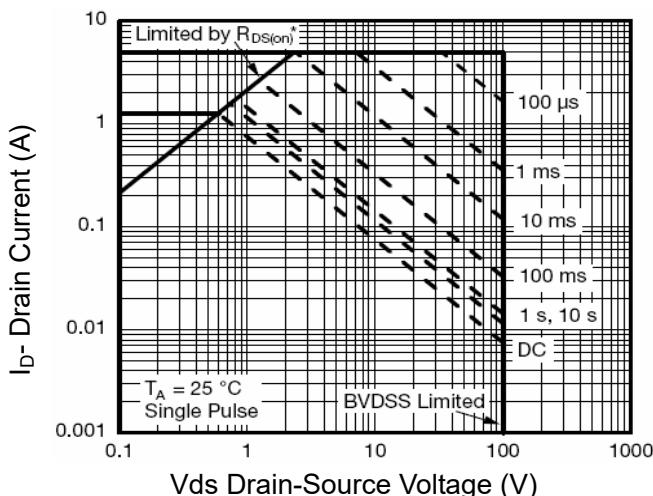
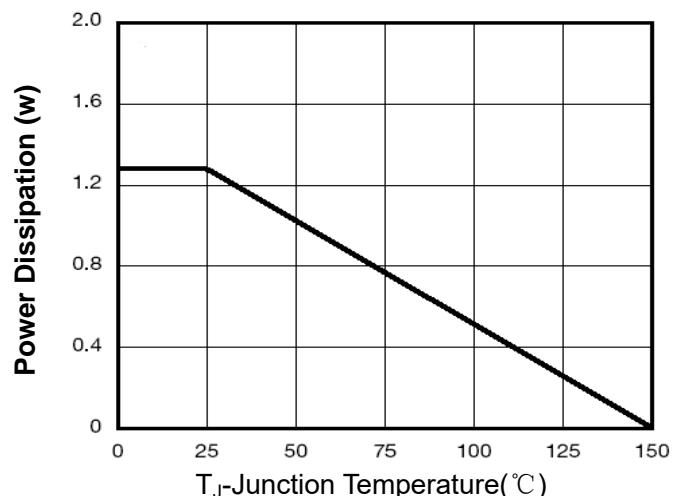
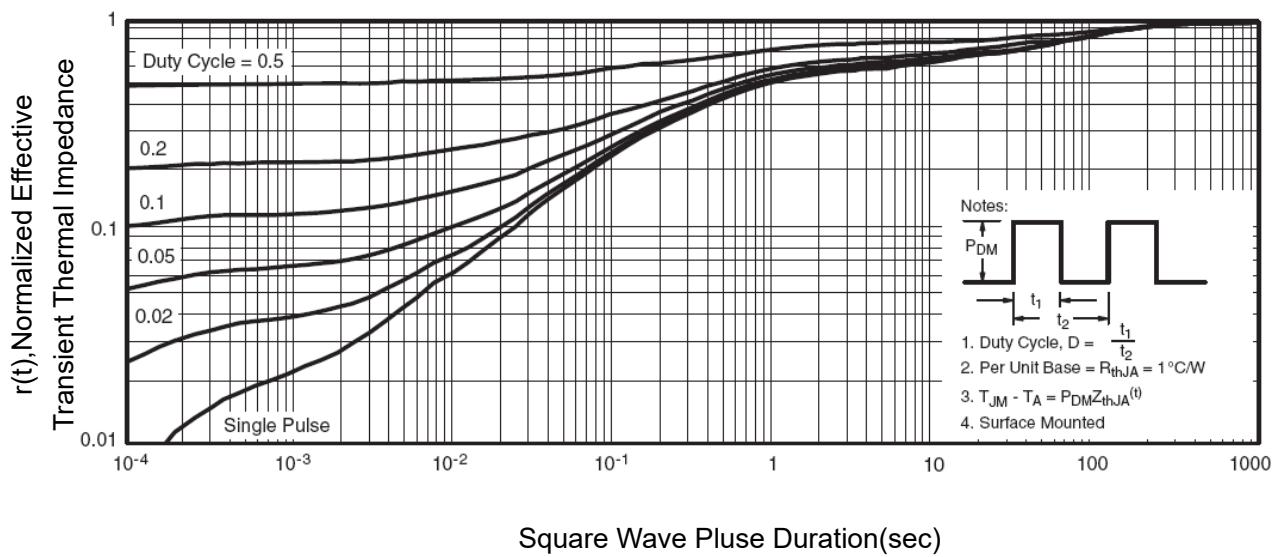
Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current ^{1,4}	I_s	$V_G=V_D=0\text{V}$, Force Current	---	---	2	A
Diode Forward Voltage ²	V_{SD}	$V_{\text{GS}}=0\text{V}$, $I_s=1.3\text{A}$, $T_J=25^\circ\text{C}$	---	---	1.2	V

Note:

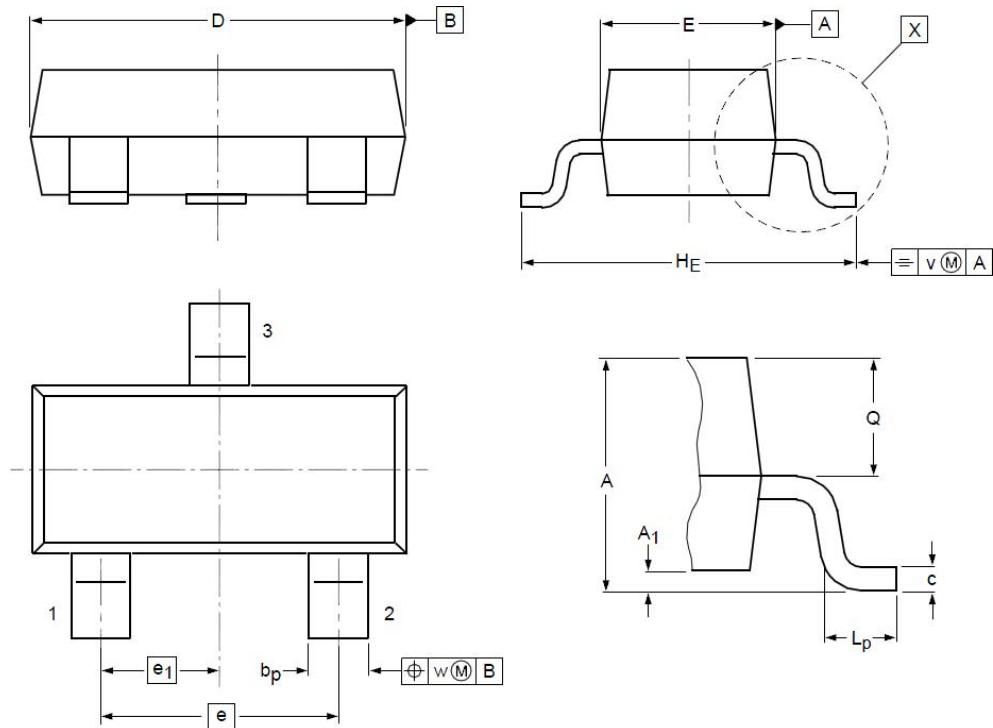
- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I_D and I_{DM} , in real applications , should be limited by total power dissipation.

Typical Characteristics




Figure 7 Capacitance vs Vds

Figure 9 BV_{DSS} vs Junction Temperature

Figure 8 Safe Operation Area

Figure 10 Power De-rating

Figure 11 Normalized Maximum Transient Thermal Impedance

SOT23 Package Outline Dimensions



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	0.90	1.05	1.20	e₁	--	0.95	--
A₁	0.01	0.05	0.10	H_E	2.10	2.40	2.50
b_p	0.38	0.42	0.48	L_P	0.40	0.50	0.60
c	0.09	0.13	0.15	Q	0.45	0.49	0.55
D	2.80	2.92	3.00	V	--	0.20	--
E	1.20	1.33	1.40	W	--	0.10	--
e	--	1.90	--				