

## Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

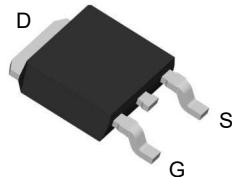
## Product Summary



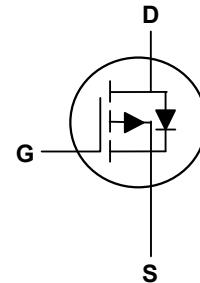
$V_{DS}$	-60	V
$I_D$	-25	A
$R_{DS(ON)}$ (at $V_{GS}=-10V$ )	40	mΩ

## Applications

- High Frequency Point-of-Load,Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch



TO-252 Top View



## Absolute Maximum Ratings( $T_c=25^\circ\text{C}$ , unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	-60	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current <sup>1</sup>	$I_D$	-25	A
Continuous Drain Current <sup>1</sup>	$I_D$	-17.7	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	-60	A
Single Pulse Avalanche Energy <sup>3</sup>	EAS	300	mJ
Total Power Dissipation <sup>4</sup>	$P_D$	90	W
Storage Temperature Range	$T_{STG}$	-55 to 150	°C
Operating Junction Temperature Range	$T_J$	-55 to 150	°C

## Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	---	1.4	°C/W

**Electrical Characteristics ( $T_J=25^\circ\text{C}$ , unless otherwise noted)**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}$ , $I_D=-250\mu\text{A}$	-60	---	---	V
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}$ , $I_D=-20\text{A}$	---	33	40	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}$ , $I_D = -250\mu\text{A}$	-1	-1.5	-2.5	V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=-48\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $T_J=25^\circ\text{C}$	---	---	-1	$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}$ , $V_{\text{DS}}=0\text{V}$	---	---	$\pm 100$	$\text{nA}$
Forward Transconductance	$g_{\text{fs}}$	$V_{\text{DS}}=-10\text{V}$ , $I_D=-10\text{A}$	---	25	---	S
Total Gate Charge	$Q_g$	$V_{\text{DS}}=-30\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $I_D=-10\text{A}$	---	46	---	nC
Gate-Source Charge	$Q_{\text{gs}}$		---	9.5	---	
Gate-Drain Charge	$Q_{\text{gd}}$		---	10.5	---	
Turn-On Delay Time	$T_{\text{d}(\text{on})}$	$V_{\text{DD}}=-30\text{V}$ , $V_{\text{GS}}=-10\text{V}$ , $R_G=3\Omega$ , $R_L=1.5\Omega$	---	12	---	ns
Rise Time	$T_r$		---	15	---	
Turn-Off Delay Time	$T_{\text{d}(\text{off})}$		---	38	---	
Fall Time	$T_f$		---	15	---	
Input Capacitance	$C_{\text{iss}}$	$V_{\text{DS}}=-30\text{V}$ , $V_{\text{GS}}=0\text{V}$ , $f=1\text{MHz}$	---	3130	---	pF
Output Capacitance	$C_{\text{oss}}$		---	321	---	
Reverse Transfer Capacitance	$C_{\text{rss}}$		---	212	---	

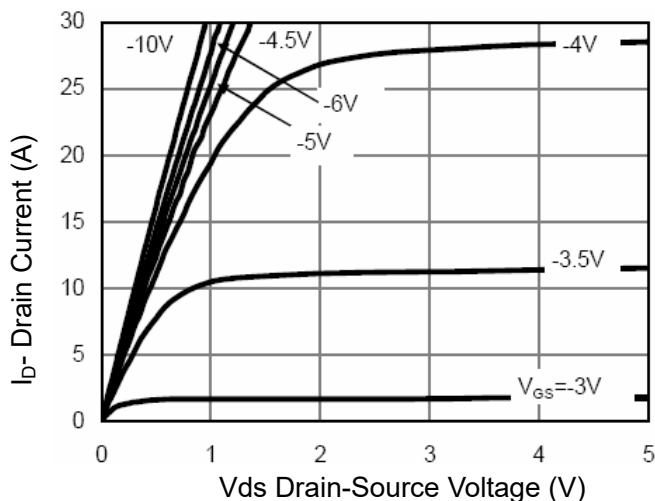
**Drain-Source Diode Characteristics**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current <sup>1</sup>	$I_s$	$T_c=25^\circ\text{C}$	---	---	-25	A
Diode Forward Voltage <sup>2</sup>	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}$ , $I_s=-10\text{A}$ , $T_J=25^\circ\text{C}$	---	---	-1.1	V
Reverse Recovery Time	$t_{\text{rr}}$	$I_F=-10\text{A}$ , $dI/dt=100\text{A}/\mu\text{s}$ , $T_J=25^\circ\text{C}$	---	47	---	nS
Reverse Recovery Charge	$Q_{\text{rr}}$		---	53	---	nC

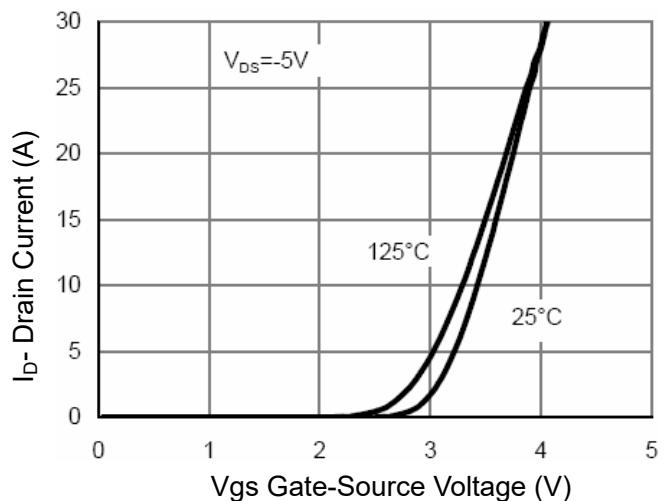
**Note:**

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
2. The data tested by pulsed , pulse width  $\leq 300\mu\text{s}$  , duty cycle  $\leq 2\%$
3. The EAS data shows Max. rating . The test condition is  $V_{\text{DD}}=-20\text{V}$ ,  $V_{\text{GS}}=-10\text{V}$ ,  $L=1\text{mH}$
4. The power dissipation is limited by  $150^\circ\text{C}$  junction temperature

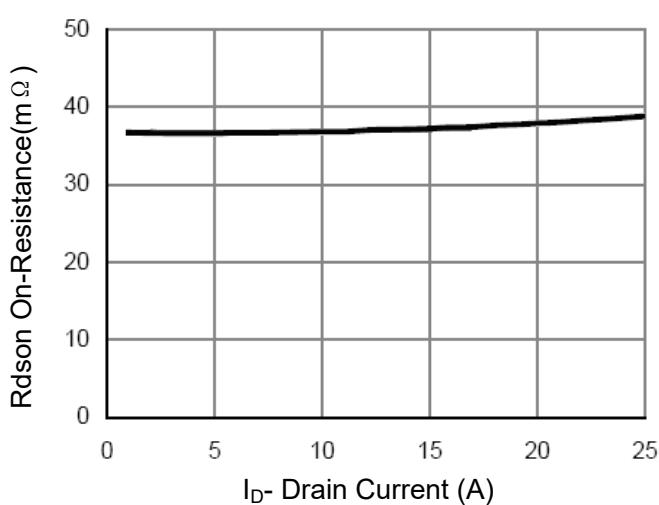
## Typical Characteristics



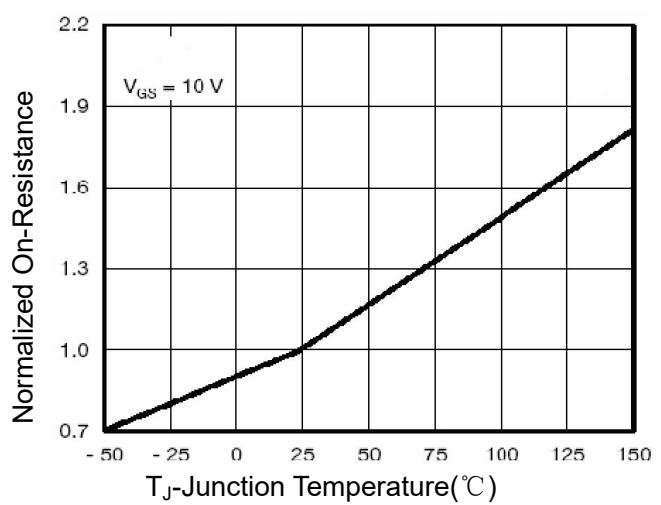
**Figure 1 Output Characteristics**



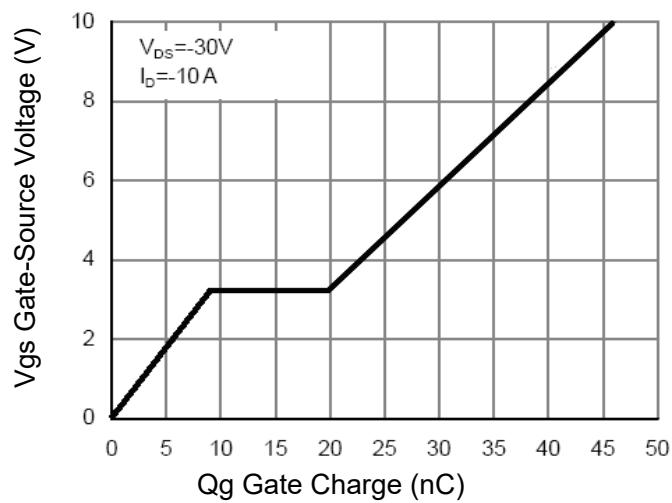
**Figure 2 Transfer Characteristics**



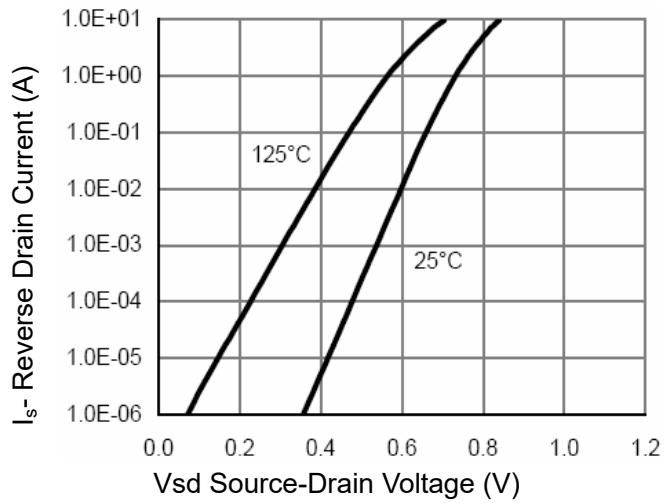
**Figure 3 Rdson- Drain Current**



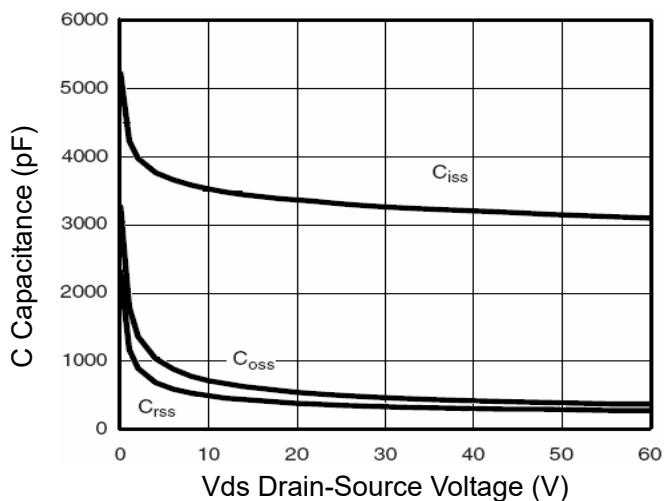
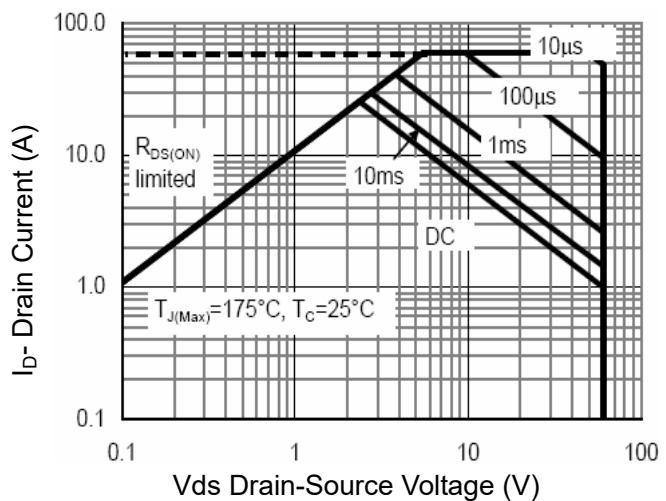
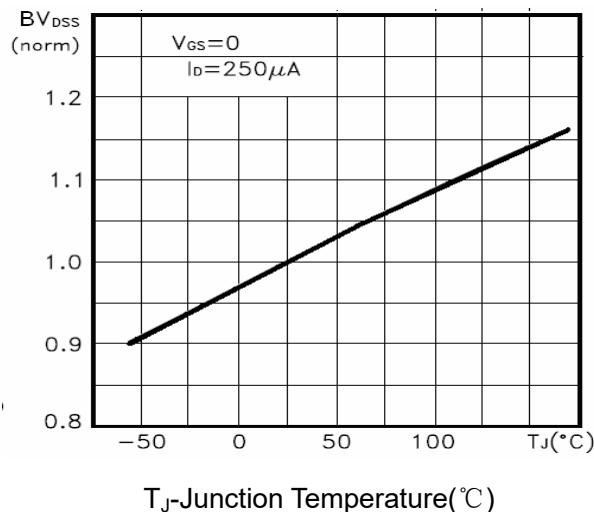
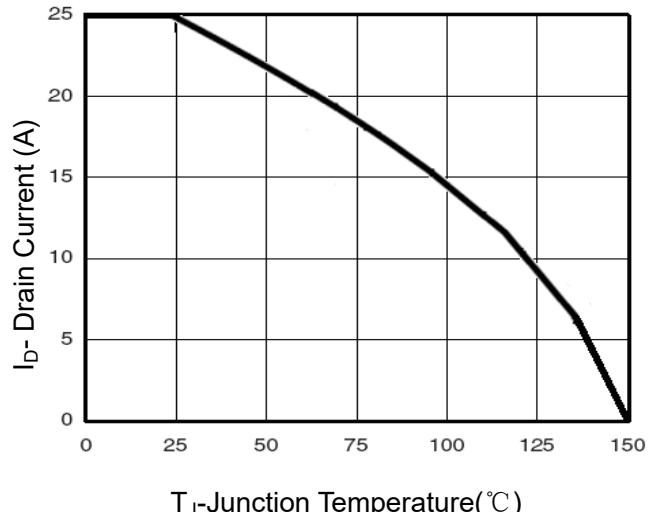
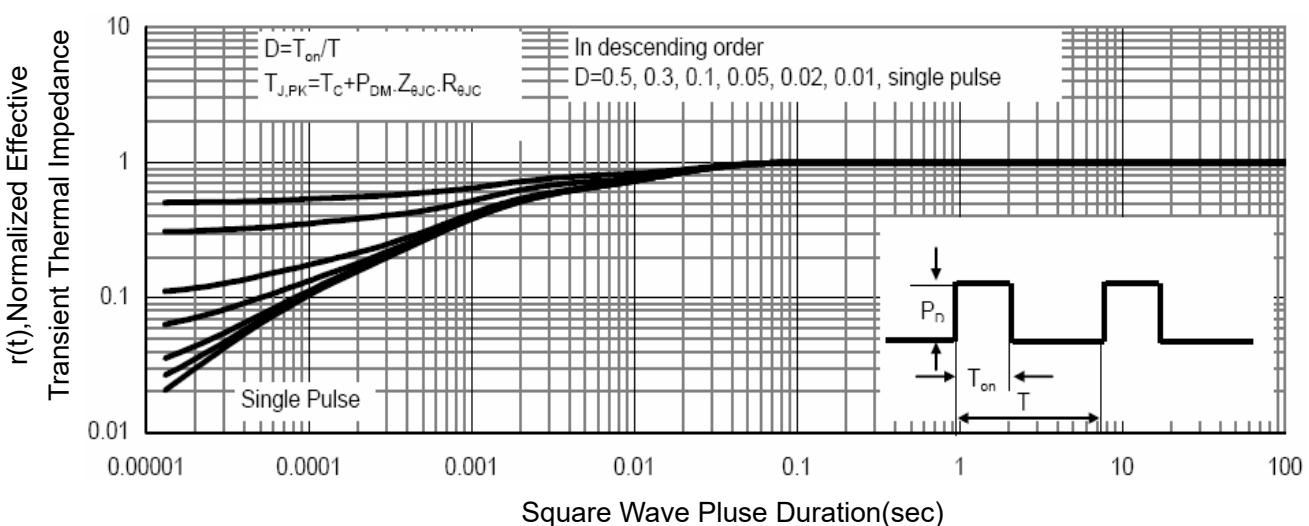
**Figure 4 Rdson-Junction Temperature**



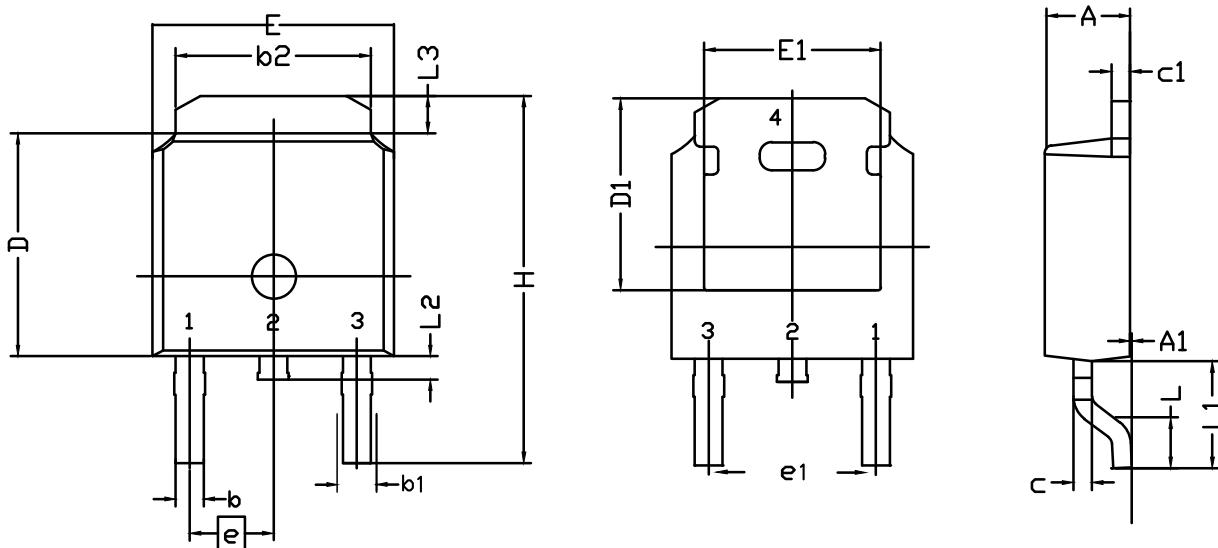
**Figure 5 Gate Charge**



**Figure 6 Source- Drain Diode Forward**


**Figure 7 Capacitance vs Vds**

**Figure 8 Safe Operation Area**

**Figure 9  $BV_{DSS}$  vs Junction Temperature**

**Figure 10 ID Current De-rating**

**Figure 11 Normalized Maximum Transient Thermal Impedance**

### TO-252 Package Outline Dimensions



<b>Symbol</b>	<b>Dimensions (unit:mm)</b>			<b>Symbol</b>	<b>Dimensions (unit:mm)</b>		
	<b>Min</b>	<b>Typ</b>	<b>Max</b>		<b>Min</b>	<b>Typ</b>	<b>Max</b>
<b>A</b>	2.20	2.30	2.38	<b>E</b>	6.40	6.60	6.731
<b>A<sub>1</sub></b>	0.00	0.10	0.20	<b>E<sub>1</sub></b>	4.40	--	--
<b>b</b>	0.64	0.76	0.89	<b>e</b>	2.286 BSC		
<b>b<sub>1</sub></b>	0.77	0.85	1.14	<b>e<sub>1</sub></b>	4.572 BSC		
<b>b<sub>2</sub></b>	5.00	5.33	5.46	<b>H</b>	9.40	10.00	10.40
<b>c</b>	0.458	0.508	0.610	<b>L</b>	1.40	1.52	1.77
<b>C<sub>1</sub></b>	0.458	0.508	0.620	<b>L<sub>1</sub></b>	--	2.743	--
<b>D</b>	5.98	6.10	6.223	<b>L<sub>2</sub></b>	0.60	0.80	1.01
<b>D<sub>1</sub></b>	5.20	5.25	5.38	<b>L<sub>3</sub></b>	0.90	1.06	1.25