

**Features**

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- Green Device Available

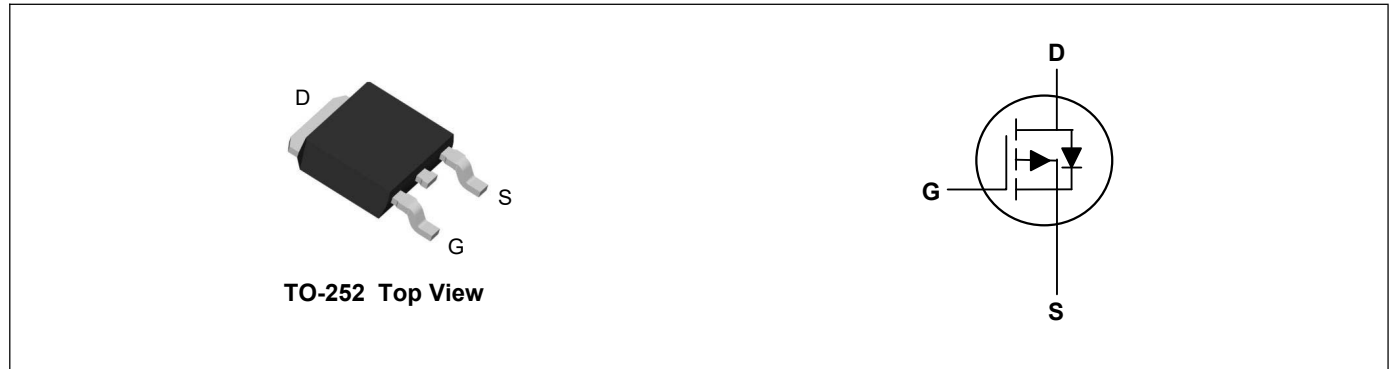
**Product Summary**



$V_{DS}$	-40	V
$I_D$	-15	A
$R_{DS(ON)}$ (at $V_{GS}=-10V$ )	36	m $\Omega$
$R_{DS(ON)}$ (at $V_{GS}=-4.5V$ )	52	m $\Omega$

**Applications**

- High Frequency Point-of-Load, Synchronous Buck Converter for MB/NB/UMPC/VGA
- Networking DC-DC Power System
- CCFL Back-light Inverter



**Absolute Maximum Ratings( $T_C=25^{\circ}C$ , unless otherwise noted)**

Parameter	Symbol	Rating	Units
Drain-Source Voltage	$V_{DS}$	-40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current	$I_D@T_C=25^{\circ}C$	-15	A
Continuous Drain Current	$I_D@T_C=100^{\circ}C$	-10	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	-80	A
Total Power Dissipation <sup>3</sup>	$P_D@T_C=25^{\circ}C$	37.5	W
Total Power Dissipation <sup>3</sup>	$P_D@T_C=100^{\circ}C$	19	W
Storage Temperature Range	$T_{STG}$	-55 to 175	$^{\circ}C$
Operating Junction Temperature Range	$T_J$	-55 to 175	$^{\circ}C$

**Thermal Characteristics**

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Case <sup>1</sup>	$R_{\theta JC}$	---	4	$^{\circ}C/W$

**Electrical Characteristics (T<sub>J</sub>=25°C, unless otherwise noted)**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-40	---	---	V
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-20A	---	25	36	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-10A	---	42	52	mΩ
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-1	-2	-3	V
Drain-Source Leakage Current	I <sub>DSS</sub>	V <sub>DS</sub> =-32V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	-1	uA
		V <sub>DS</sub> =-32V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	-5	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	---	---	±100	nA
Forward Transconductance	g <sub>fs</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-10A	---	25	---	S
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =-10V, I <sub>D</sub> =-15A	---	20	---	nC
Gate-Source Charge	Q <sub>gs</sub>		---	2.5	---	
Gate-Drain Charge	Q <sub>gd</sub>		---	4.5	---	
Turn-On Delay Time	T <sub>d(on)</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =-10V, R <sub>L</sub> =1.6Ω, R <sub>G</sub> =3Ω	---	5	---	ns
Rise Time	T <sub>r</sub>		---	12	---	
Turn-Off Delay Time	T <sub>d(off)</sub>		---	20	---	
Fall Time	T <sub>f</sub>		---	4.5	---	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-25V, V <sub>GS</sub> =0V, f=1MHz	---	840	---	pF
Output Capacitance	C <sub>oss</sub>		---	92	---	
Reverse Transfer Capacitance	C <sub>rss</sub>		---	60	---	

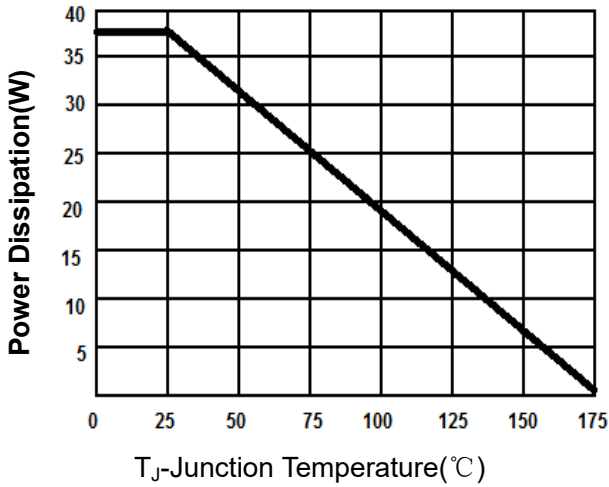
**Drain-Source Diode Characteristics**

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Continuous Source Current <sup>1,4</sup>	I <sub>S</sub>	V <sub>G</sub> =V <sub>D</sub> =0V, Force Current	---	---	-20	A
Diode Forward Voltage <sup>2</sup>	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-20A, T <sub>J</sub> =25°C	---	---	-1.2	V

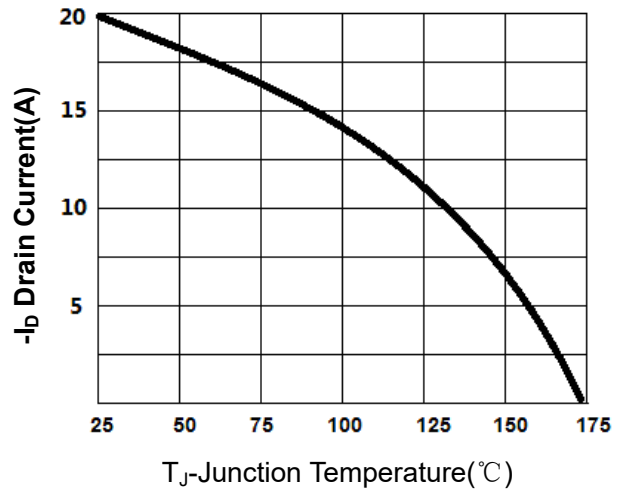
**Note:**

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper.
- 2.The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%
- 3.The power dissipation is limited by 175°C junction temperature
- 4.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub>, in real applications, should be limited by total power dissipation.

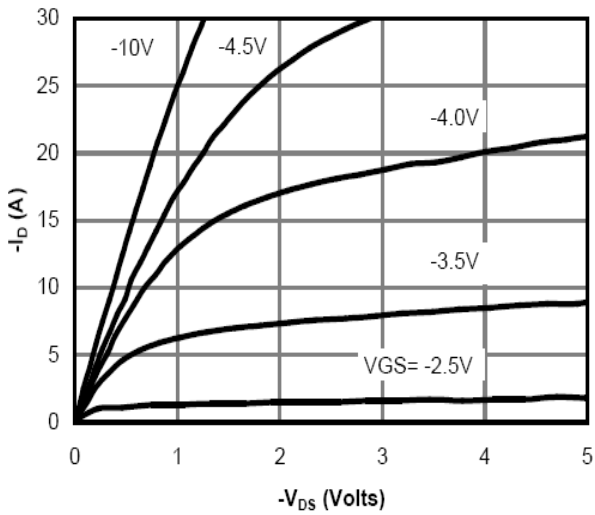
**Typical Characteristics**



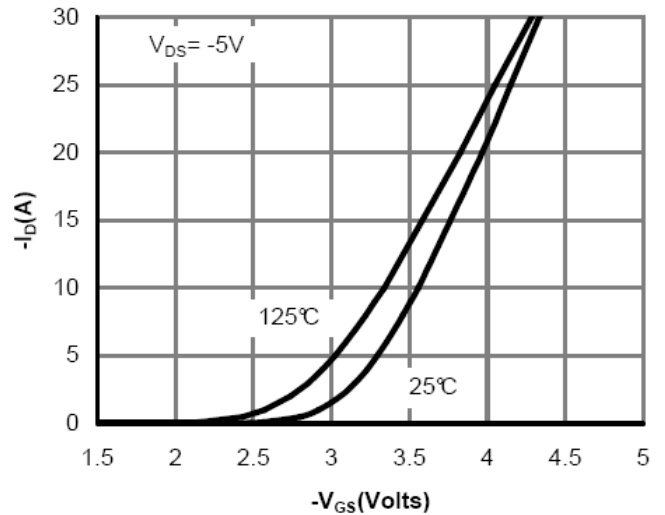
**Figure 1 Power Dissipation**



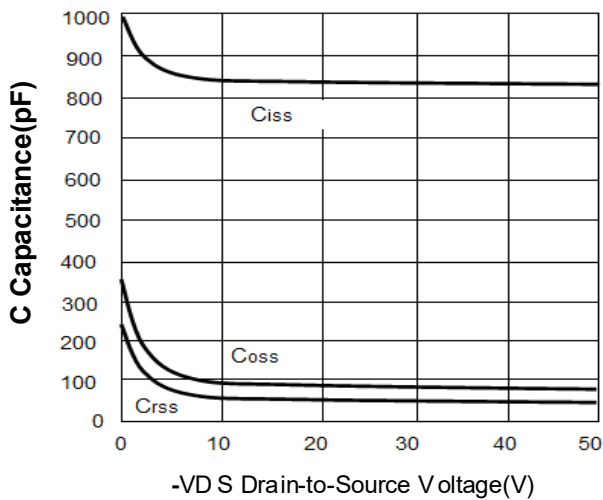
**Figure 2 Drain Current**



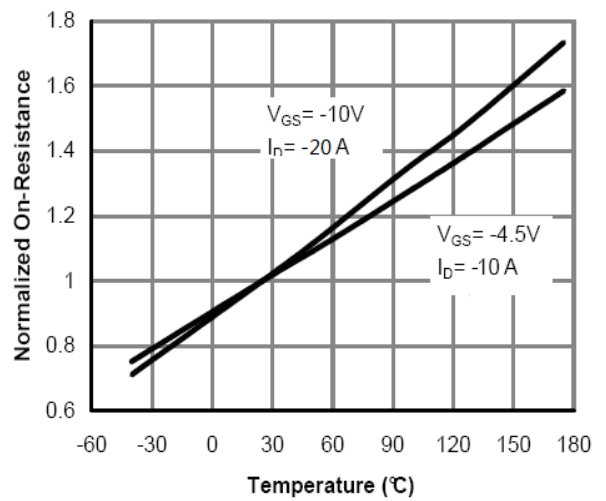
**Figure 3 Output Characteristics**



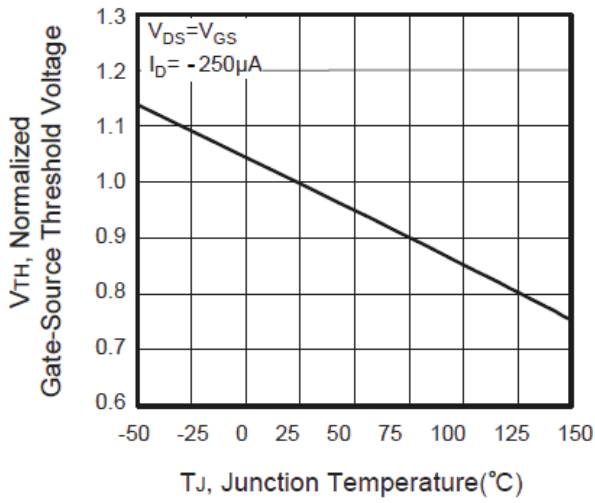
**Figure 4 Transfer Characteristics**



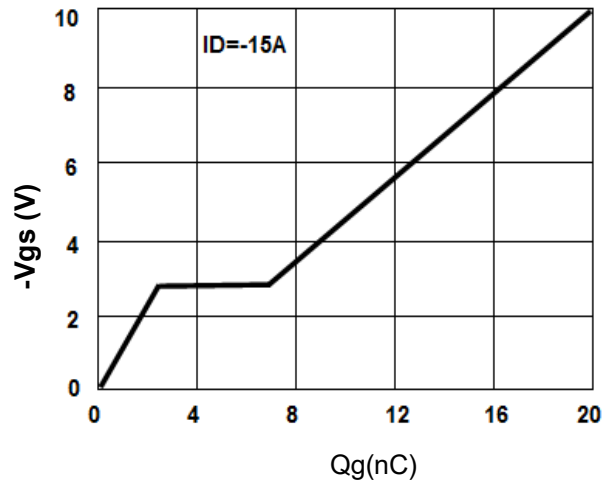
**Figure 5. Capacitance**



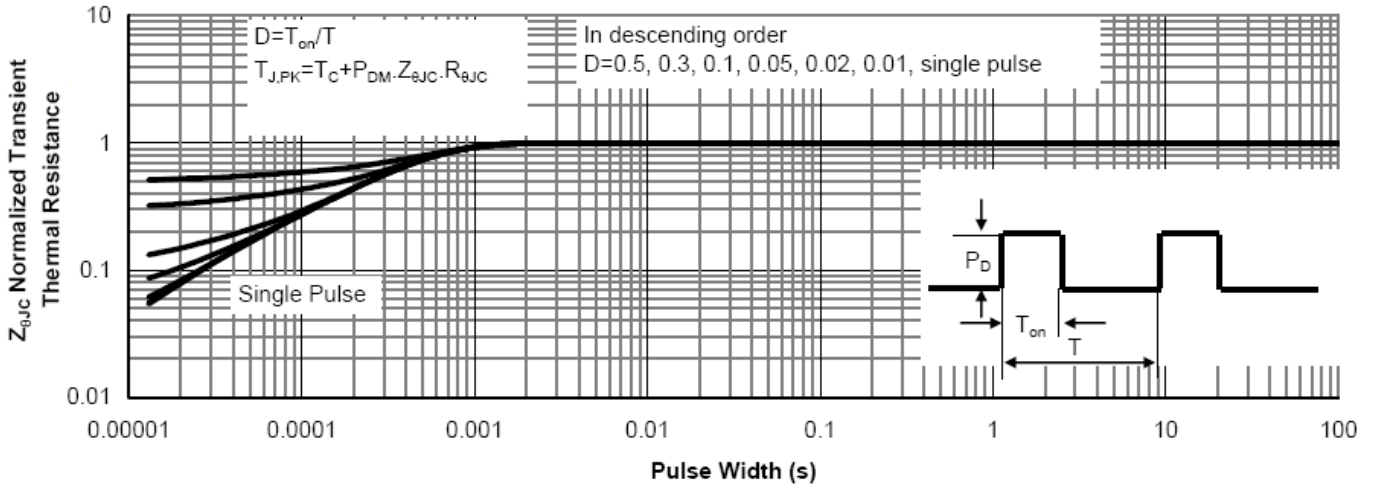
**Figure 6.  $R_{DS(ON)}$  vs Junction Temperature**



**Figure7.  $V_{GS(th)}$  vs Junction Temperature**

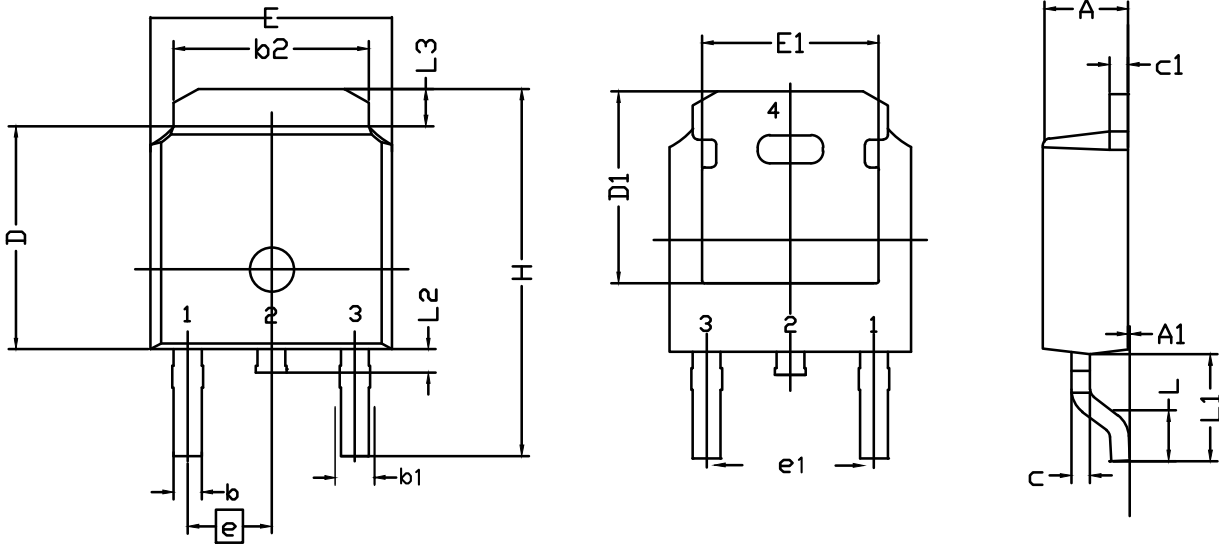


**Figure8. Gate Charge Waveforms**



**Figure9. Normalized Maximum Transient Thermal Impedance**

**TO-252 Package Outline Dimensions**



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
<b>A</b>	2.20	2.30	2.38	<b>E</b>	6.40	6.60	6.731
<b>A<sub>1</sub></b>	0.00	0.10	0.20	<b>E<sub>1</sub></b>	4.40	--	--
<b>b</b>	0.64	0.76	0.89	<b>e</b>	2.286 BSC		
<b>b<sub>1</sub></b>	0.77	0.85	1.14	<b>e<sub>1</sub></b>	4.572 BSC		
<b>b<sub>2</sub></b>	5.00	5.33	5.46	<b>H</b>	9.40	10.00	10.40
<b>c</b>	0.458	0.508	0.610	<b>L</b>	1.40	1.52	1.77
<b>C<sub>1</sub></b>	0.458	0.508	0.620	<b>L<sub>1</sub></b>	--	2.743	--
<b>D</b>	5.98	6.10	6.223	<b>L<sub>2</sub></b>	0.60	0.80	1.01
<b>D<sub>1</sub></b>	5.20	5.25	5.38	<b>L<sub>3</sub></b>	0.90	1.06	1.25