

Features

- Advanced high cell density Trench technology
- Super Low Gate Charge
- Excellent CdV/dt effect decline
- 100% EAS Guaranteed
- Green Device Available

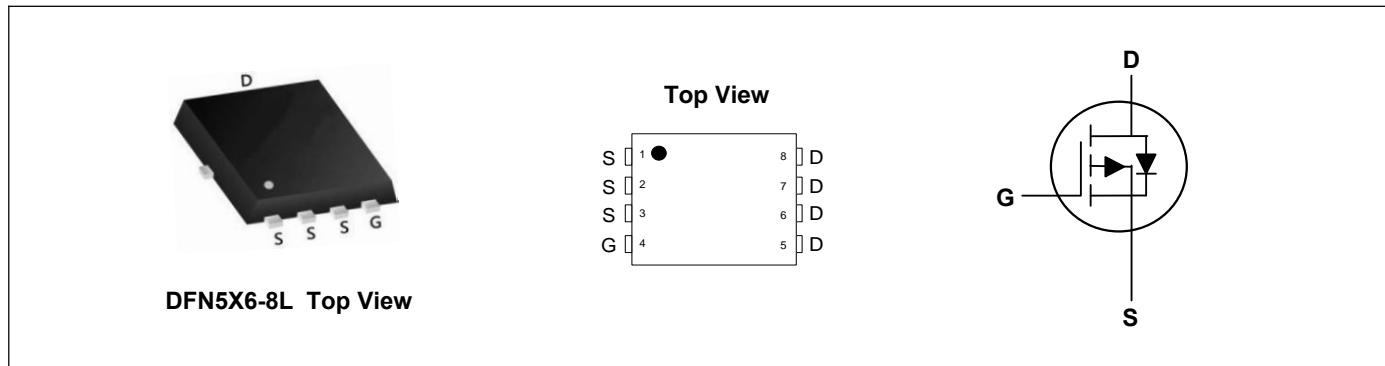
Product Summary



V_{DS}	-30	V
I_D	-67	A
$R_{DS(ON)}$ (at $V_{GS}=-10V$)	8	mΩ
$R_{DS(ON)}$ (at $V_{GS}=-4.5V$)	10.7	mΩ

Applications

- High Frequency Point-of-Load,Synchronous Buck Converter
- Networking DC-DC Power System
- Load Switch



Absolute Maximum Ratings($T_c=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ¹	$I_D @ T_c = 25^\circ\text{C}$	-67	A
Continuous Drain Current ¹	$I_D @ T_c = 100^\circ\text{C}$	-43	A
Pulsed Drain Current ²	I_{DM}	-141	A
Single Pulse Avalanche Energy ³	EAS	55	mJ
Avalanche Current	I_{AS}	-33	A
Total Power Dissipation ⁴	$P_D @ T_c = 25^\circ\text{C}$	50	W
Total Power Dissipation ⁴	$P_D @ T_c = 100^\circ\text{C}$	20	W
Storage Temperature Range	T_{STG}	-55 to 150	°C
Operating Junction Temperature Range	T_J	-55 to 150	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Unit
Thermal Resistance Junction-Ambient ¹	$R_{\theta JA}$	---	50	°C/W
Thermal Resistance Junction-Case ¹	$R_{\theta JC}$	---	2.5	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}$, $I_D=-250\mu\text{A}$	-30	---	---	V
Static Drain-Source On-Resistance ²	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}$, $I_D=-12\text{A}$	---	6.5	8	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}$, $I_D=-9\text{A}$	---	8.2	10.7	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}$, $I_D = -250\mu\text{A}$	-1.0	-1.5	-2.0	V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=-24\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	-1	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA
Forward Transconductance	g_{fs}	$V_{\text{DS}}=-5\text{V}$, $I_D=12\text{A}$	---	35	---	S
Gate Resistance	R_g	$V_{\text{DS}}=0\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	11	---	Ω
Total Gate Charge	Q_g	$V_{\text{DS}}=-15\text{V}$, $V_{\text{GS}}=-10\text{V}$, $I_D=10\text{A}$	---	88	---	nC
Gate-Source Charge	Q_{gs}		---	14	---	
Gate-Drain Charge	Q_{gd}		---	8.5	---	
Turn-On Delay Time	$T_{\text{d}(\text{on})}$	$V_{\text{DD}}=-15\text{V}$, $V_{\text{GS}}=-10\text{V}$, $R_G=3\Omega$, $I_D=-1\text{A}$	---	51	---	ns
Rise Time	T_r		---	40	---	
Turn-Off Delay Time	$T_{\text{d}(\text{off})}$		---	77	---	
Fall Time	T_f		---	56	---	
Input Capacitance	C_{iss}	$V_{\text{DS}}=-15\text{V}$, $V_{\text{GS}}=0\text{V}$, $f=1\text{MHz}$	---	4430	---	pF
Output Capacitance	C_{oss}		---	500	---	
Reverse Transfer Capacitance	C_{rss}		---	373	---	

Drain-Source Diode Characteristics

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Diode Forward Voltage ²	V_{SD}	$V_{\text{GS}}=0\text{V}$, $I_s=3.6\text{A}$, $T_J=25^\circ\text{C}$	---	-0.75	-1.1	V
Reverse Recovery Time	t_{rr}	$I_F=-3.6\text{A}$, $dI/dt=100\text{A}/\mu\text{s}$, $T_J=25^\circ\text{C}$	---	25	---	nS
			---	15	---	nC

Note:

- 1.The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$
- 3.The EAS data shows Max. rating . The test condition is $V_{\text{DD}}=-25\text{V}$, $V_{\text{GS}}=-10\text{V}$, $L=0.1\text{mH}$
- 4.The power dissipation is limited by 150°C junction temperature

Typical Characteristics

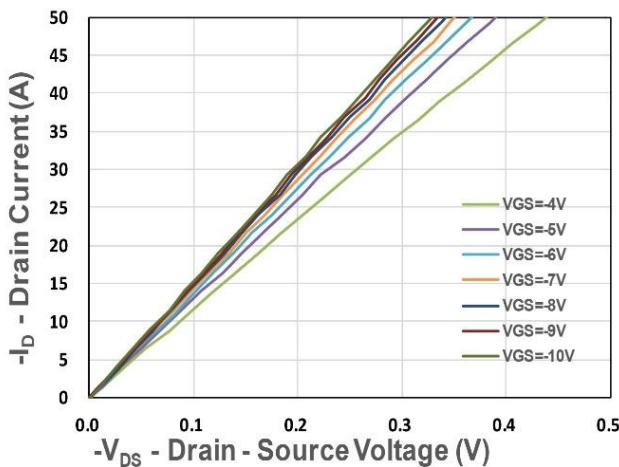


Figure 1. Output Characteristics

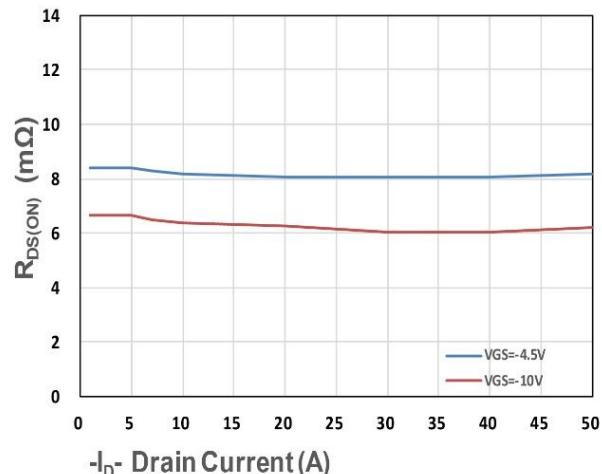


Figure 2. On-Resistance vs. ID

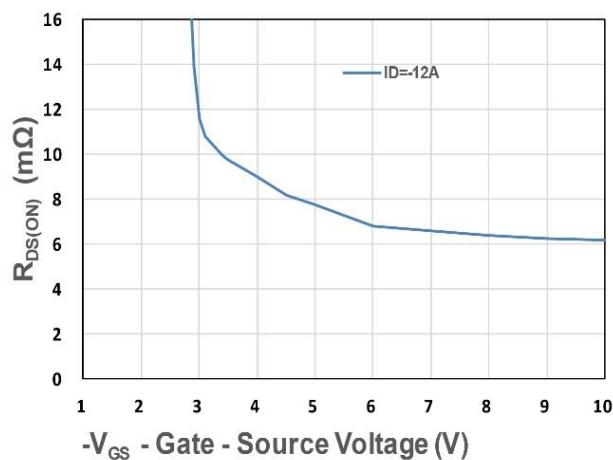


Figure 3. On-Resistance vs. VGS

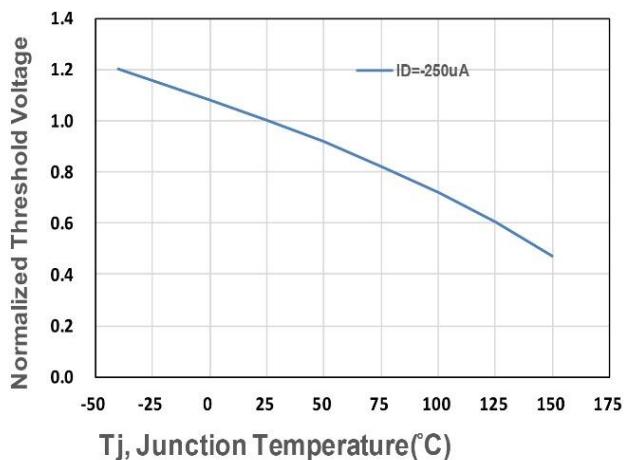


Figure 4. Gate Threshold Voltage

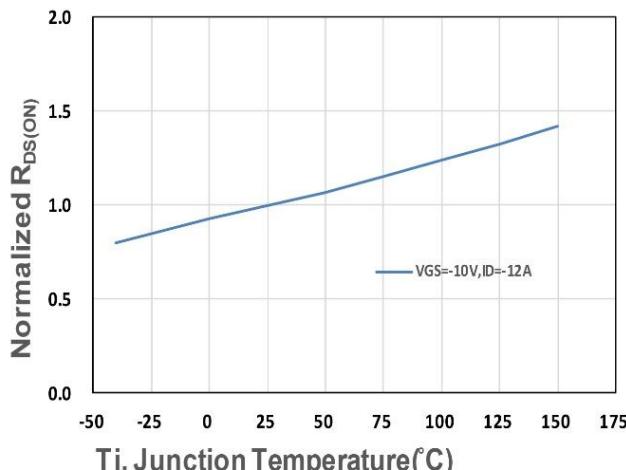


Figure 5. Drain-Source On Resistance

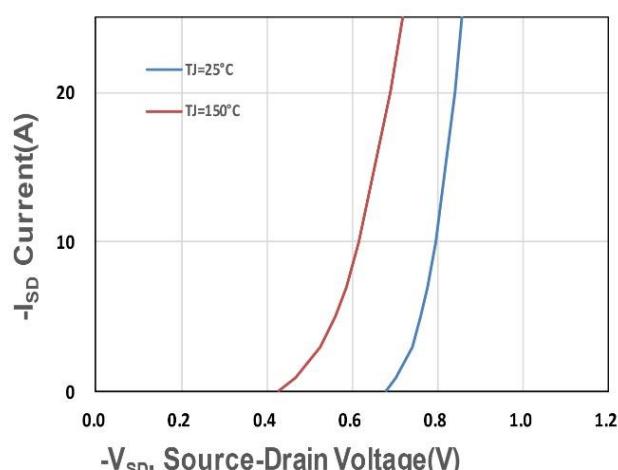
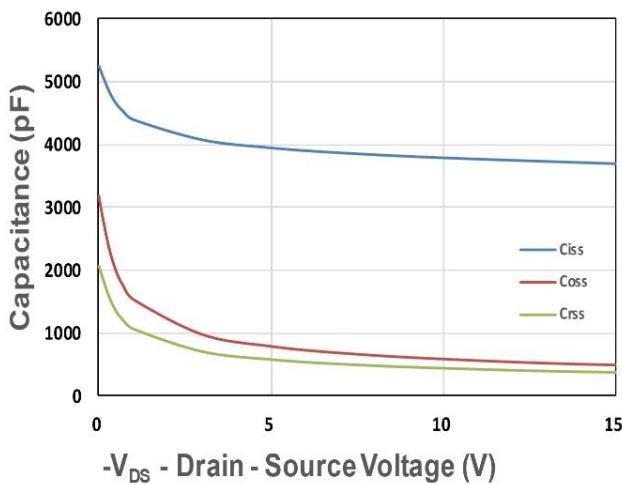
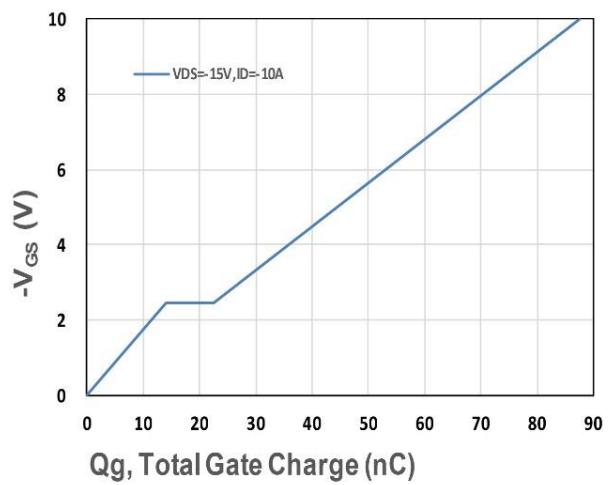


Figure 6. Source-Drain Diode Forward



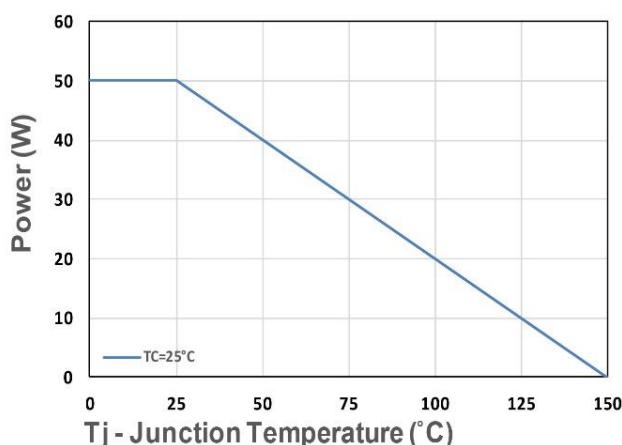
-V_{DS} - Drain - Source Voltage (V)

Figure 7. Capacitance



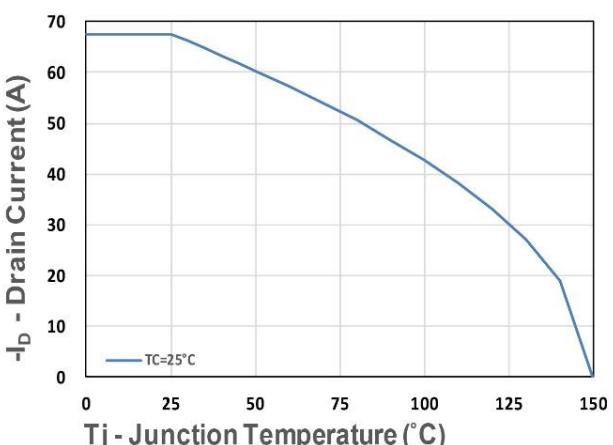
-V_{GS} (V)

Figure 8. Gate Charge Characteristics



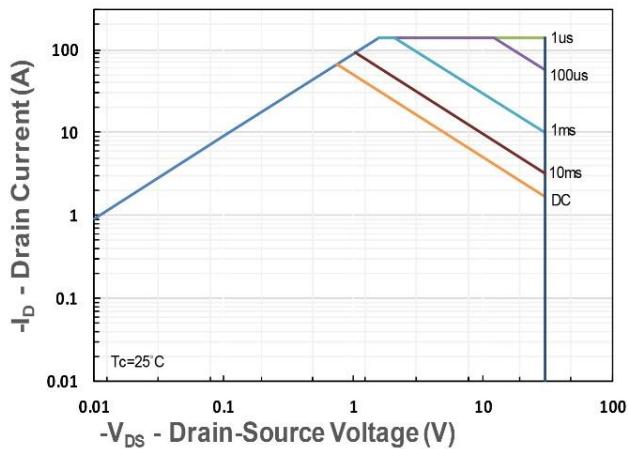
T_j - Junction Temperature (°C)

Figure 9. Power Dissipation



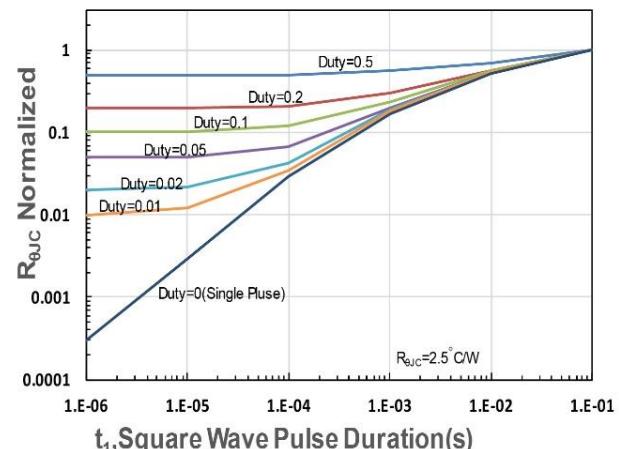
T_j - Junction Temperature (°C)

Figure 10. Drain Current



-V_{DS} - Drain-Source Voltage (V)

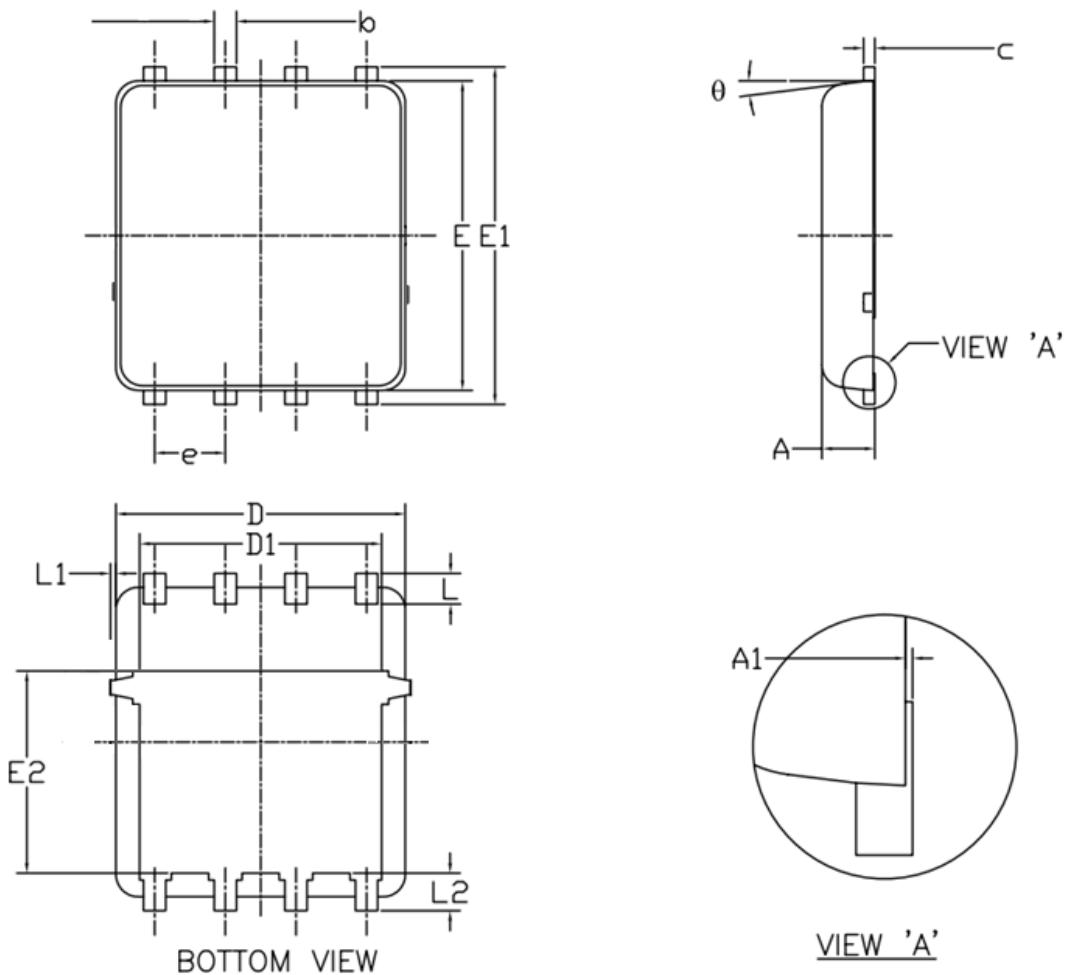
Figure 11. Safe Operating Area



R_{θJC} Normalized

Figure 12. R_{θJC} Transient Thermal Impedance

DFN5X6-8L Package Outline Dimensions



Symbol	Dimensions (unit:mm)			Symbol	Dimensions (unit:mm)		
	Min	Typ	Max		Min	Typ	Max
A	0.90	1.00	1.20	E1	5.90	6.10	6.35
A1	0.00	--	0.05	E2	3.38	3.58	3.92
b	0.30	0.40	0.51	e	1.27 BSC		
c	0.20	0.25	0.33	L	0.51	0.61	0.71
D	4.80	4.90	5.40	L1	--	--	0.15
D1	3.61	4.00	4.25	L2	0.41	0.51	0.61
E	5.65	5.80	6.06	θ	0°	--	12°